



| <b>J1: FDC Controller Base I/O</b> |           |
|------------------------------------|-----------|
| <b>Designation / Base I/O</b>      | <b>J1</b> |
| Primary FDC controller / 3F0h      | Open      |
| Secondary FDC controller / 370h    | Closed    |

Notes:

### **FDC ROM Mapping**

The FDC ROM mapping is hardwired at CA00h.

### **Detection / POST sequence**

When the FDC is designated as Secondary / 370h (J1: Closed) the POST freezes unless there is another Primary FDC present. FDD detection is sequential (i.e. Drive 0,1,2,3) and stops when no FDD is detected. For example, if Drives 0,1 and 3 are attached, only Drives 0 and 1 are detected.

### **Booting / Performance**

FDD booting appears to be possible only from Drive 0 (A:). Firmware v2.00 appears to be buggy; when loaded, no further UMA scanning for option ROMs (e.g. HDC BIOS) is possible. Also, Firmware v2.00 appears to have a major negative impact on FDD performance/throughput. Usage of Firmware v1.0b is recommended.