● **GENERAL DESCRIPTION**

The PCI- SCSI CONTROLLER is with a Peripheral Component Interconnect (PCI) front end. It integrates a high-performance SCSI core and a PCI bus master DMA core with a SCSI SCRIPTS processor to accommodate the flexibility requirements of not only to SCSI-2, but future SCSI standards as well.
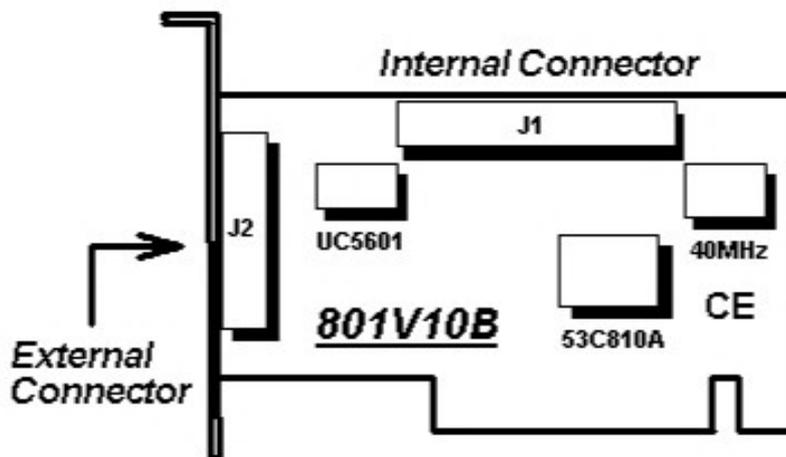
● **BOARD LAYOUT**

Figure 1 : BOARD LAYOUT

● **FEATURES**

* Supports 32-bit word data bursts with variable burst lengths
* Performs zero wait-state bus master data bursts faster than 110 MB/s (@ 33MHz)
* Performs complex bus sequences without interrupts, including restore data pointers
* Unique interrupt status reporting method reduces ISR overhead
* Performs high-speed async/sync single-ended SCSI bus transfers
    · up to 7 MB/s asynchronous
    · 10 MB/s synchronous
* Bursts 2,4,8,or 16 dwords across PCI bus with 80-byte DMA FIFO
* Supports PCI Cache Line Size Register
* Supports execution of multi-threaded I/O algorithms in SCSI SCRIPTS with fast I/O context switching
* Termination automatically Enabled / Disabled
* Supports Plug & Play

● **INSTALLING THE HOST ADAPTER BOARD**

SCSI Bus Termination Case #1:
Attaching only Internal SCSI peripheral devices to the Host Adapter Card