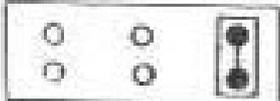
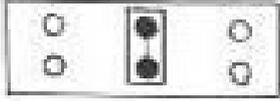
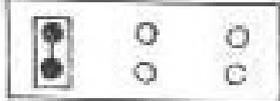


I/O ADDRESS & IRQ ASSIGNMENT

The IRQ assignment of KW-507B is fixed at IRQ7.

The I/O address can be jumper-selected among LPTA, LPTB, LPTC (which have the base I/O address 3BC, 378, and 278, respectively) and made disabled by configuring on JP1 as below:

<u>I/O Address</u>	<u>Jumper Setting of JP1</u>		
	1	2	3
LPTA (3BC-3BF)			
LPTB (378-37F)*			
LPTC (278-27F)			
Disable			

* Factory setting

INSTALLATION

To install KW-507B into the personal computer, choose a 62-pin DIP socket expansion slot to plug in the board with the bottom golden finger edge connector (J1). Once the KW-507B is seated in the slot firmly, secure the side bracket to the chassis of the computer so that the connector faces outward from an open rear-panel cut-out. The connector (J2) is then ready for attachment of an 25-pin to 36-pin cable from the parallel peripheral device, which supports Centronics interface at 36-pin end.