OLD IOMEGA CONTROLLERS INFO   adapters

Iomega Adapters and Cables

PC2 SWITCH AND JUMPER SETTINGS

The PC2F/50F adapter has one bank of 8 DIP switch settings and one bank of 4 ROM address jumper settings. Their purpose and settings are described below.

DIP Switches:

The PC2B/50F option DIP switches are set at the factory for the most typical configuration used with your computer (Switches 1 ON, 2 ON, 3 OFF, 4 ON, 5/6/7/8 OFF). However, you can change the switch configurations for special applications.

```
Switch Number        Switch Purpose & Default Setting
 1, 2, 5             Port Addresses (1 ON, 2 ON, 5 OFF)
 3                   DMA Channel (DMA channel 3, 3 OFF)
 4                   PlO/DMA Select (rio enabled, 4 ON)
 6                   IRQ (6 OFF
 7                   Reserved (7 OFF)
 8                   Number of Drives in Subsystem (8 OFF)

Switches 1, 2, and 5


   Port Addresses:
Port Address Switch #1    2       3    4    5      6    7    8
 340H              on   on            off
 350H              off  on            off
 360H              on   off           off
 370H              off  off           off
 348H              on   on            on
 358H              off  on            on
 368H              on   off           on
```

Switch 3

Direct Memory Access (DMA) channel. This switch selects DMA channel 1 or DMA channel 3. Most networking schemes use DMA channel 1 for communication; so if the computer is part of a network, DMA channel 3 is recommended. DMA Channel 1 - Switch 3 ON, DMA Channel 3 - Switch 3 OFF.

Switch 4

DMA or Programmed Input/Output (rio). Selecting "rio" disables DMA communications, decreases or increases transfer rates in some systems, and prevents problems with other hardware using DMA. rio Enabled - Switch 4 ON, DMA Enabled - Switch 4 OFF.

Switch 6

IRQ Settings. This switch has two interrupt request (IRQ) settings. MS DOS does not support IRQ; some operating systems do. For IRQ information relating to other operating system environments, refer to the Iomega driver manual for that operating system. IRQ 5 - Switch 6 OFF, IRQ 7 - Switch 6 ON.

Switch 7

This switch is reserved. Do not change its setting.

Switch 8

Number of 10Mb drives in the subsystem. This switch selects the number of 8 inch drives in a 10MB Bernoulli external subsystem connected to the host adapter board. The switch is set at the factory for two drives (this applies to 8 inch Bernoulli drives only and does not affect the operation of other Iomega drives). 1 10MB drive - Switch 8 ON, 2 10MB drives - Switch 8 OFF.

ROM Address Jumper Settings:
The address jumpers are found only on the PC2B/50 (at J3 near the edge connector). They are the four connectors that enable the selection of the starting address of the ROM. The ROM address is the memory location where the computer finds the host adapter board. The board currently used 8k bytes of memory; the connectors are installed at the factory at memory address CE00:0000.

```
Factory Default ROM Address (CE00:0000):
[][]oo
[][][][]
oo [][]


Additional ROM Address Options:
ROM         Jumper              ROM         Jumper
Address:    Settings:           Address:    Settings:
c800:0000   [][][][]            D800:0000   o [][][]
C800:1FFF*  [][][][]            DS00:1FFF   [][][][]
            o o o o                         []o o o


cA00:0000   [][][]o             DA00:0000   o [][]o
CA00:1FFF   [][][][]            DA00:1FFF   [][][][]
            o o o []                        []o o []
```

```
cc00:0000  [][]o []          DC00:0000  o []o []
CC00:1FFF  [][][][]          DC00:1FFF  [][][][]
           [][]o []                     []o []o

CE00:0000  [][]o o           DE00:0000  o []o o
CE00:1FFF  [][][][] **       DE00:1FFF  [][][][]
           o o [][]                     []o [][]

D000:0000  []o [][]          E000:0000  o o [][]
D000:1FFF  [][][][]          E000:1FFF  [][][][]  +
           o []o o                      [][]o o

D200:0000  []o []o *         E200:0000  o o []o
D200:1FFF  [][][][]          E200:1FFF  [][][][]
           o []o []                     [][]o []

D400:0000  []o o []          E400:0000  o o o []
D400:1FFF  [][][][]          E400:1FFF  [][][][]
           o [][]o                      [][][]o

D600:0000  []o o o  *        ROM/RAM    o o o o
D600:1FFF  [][][][]          Disabled   [][][][]
           o [][][]                     [][][][]
```

* Reserved by computers containing a fixed disk drive.
** Factory Setting + Reserved by some 80286-based computers.
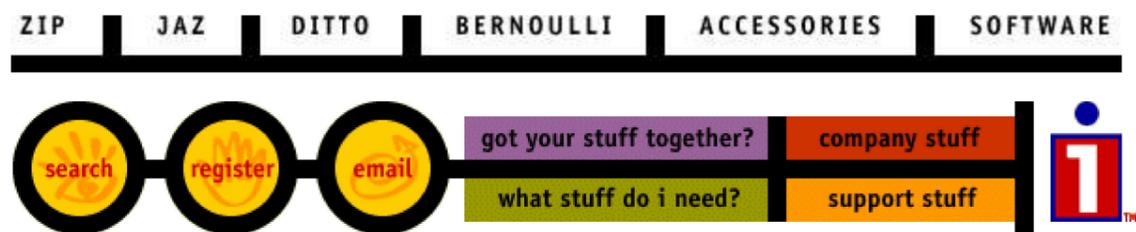
If you move the jumpers from one connector post to another, be aware of the following possible problems:

* If the jumpers are taken off the PC2B/50 board, it will function with the RAM/ROM disabled. However, keeping the jumpers stored on one pin when not in use is advisable to avoid loss of the jumpers.

RAM/ROM Disable:

Setting all the jumpers (or switches) on your adapter board in the OFF position will disable the ROM and RAM on the board. If the ROM and RAM are disabled, an Iomega drive cannot be used to boot your system.

If any other hardware accesses the same address, conflicts will occur. For example, some 8088-based computers use the address starting at C800-0000, so if you set the adapter board address at that location, your computer and subsystem may not operate. Some 80286- and 80386-based computers reserve ROM address E000 and above. If you set the ROM address jumpers on the adapter board at E000 or above, the drive in your Iomega supported subsystem may not boot and other devices in or peripheral to the computer may be affected.

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                               what stuff do i need?   support stuff