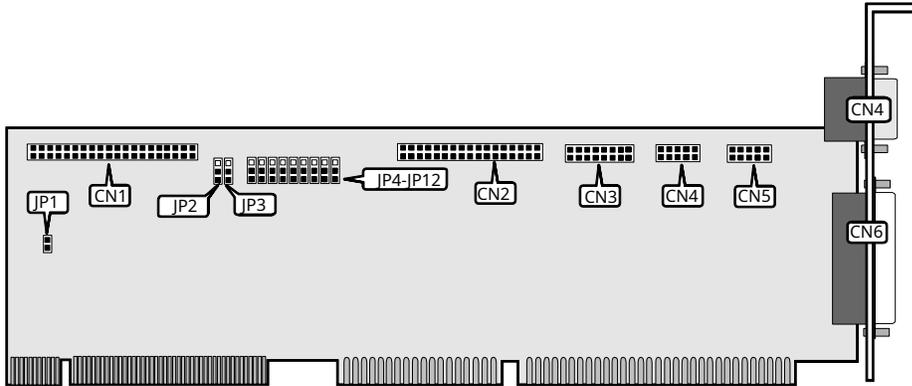


ADI FSIOL-UC2C

Q2/96

Data bus: 32-bit, VL-Bus
 Size: Three quarter-length, half-height card
 Hard drive supported: Two IDE (AT) interface drives
 Floppy drives supported: 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS		
Function	Location	
40-pin primary IDE(AT) interface	CN1	
34-pin control cable connector - floppy drive	CN2	
16-pin Game port	CN3	
9-pin serial port 1 - external, 10-pin serial port 1 - internal	CN4	
10-pin serial port 2 - internal	CN5	
25-pin parallel port	CN6	
2-pin drive active LED for primary IDE	JP1	

CPU CLOCK CONFIGURATION		
CPU Clock	JP2	JP3
40 /50 MHz	open	open
Below 20 MHz or Enhanced IDE support	open	closed
25 /33 MHz	closed	open
Under 40 MHz or Non-ATA support	closed	closed

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
Primary IDE interface enabled	JP4	pins 1 & 2 closed
Primary IDE interface disabled	JP4	pins 2 & 3 closed
Parallel port is enabled	JP5	pins 1 & 2 closed
Parallel port is disabled	JP5	pins 2 & 3 closed
Floppy drive interface enabled	JP9	pins 1 & 2 closed
Floppy drive interface disabled	JP9	pins 2 & 3 closed
Parallel port is LPT2	JP10	pins 1 & 2 closed
Parallel port is LPT3	JP10	pins 2 & 3 closed
Game port enabled	JP11	pins 1 & 2 closed
Game port disabled	JP11	pins 2 & 3 closed

SERIAL PORT 1 CONFIGURATION		
COM	JP6	JP8
COM1	pins 1 & 2 closed	pins 1 & 2 closed
COM3	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	N/A

SERIAL PORT 2 CONFIGURATION		
COM	JP9	JP12
COM2	pins 1 & 2 closed	pins 1 & 2 closed
COM4	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	N/A