

6.1 DIP8P

The DIP8P is a high-speed 32-bit memory card designed for the PAT38PI. The DIP8P consists of two memory banks with each bank containing 36 DIP chip sockets. The DIP sockets of the DIP8P accept 1M x 1 DRAMs, and when fully populated, will extend the PAT38PI's Page Interleave memory by an additional 8MB.

Jumper Settings

The DIP8P can be used in two ways, depending on how its memory banks are assigned. There are ten jumpers, JP1 through JP10, on the DIP8P which allow the user to configure the memory banks on the DIP8P as Banks 0 and 1 or as Banks 2 and 3.

Assigning the memory banks on the DIP8P as Banks 0 and 1 will allow the user to utilize DIP memory chips for Banks 0 and 1 instead of SIPs.

Assigning the memory banks on the DIP8P as Banks 2 and 3 will allow the user to add an additional 8MB to the system.

Figures 4 and 5 show the necessary jumper settings.

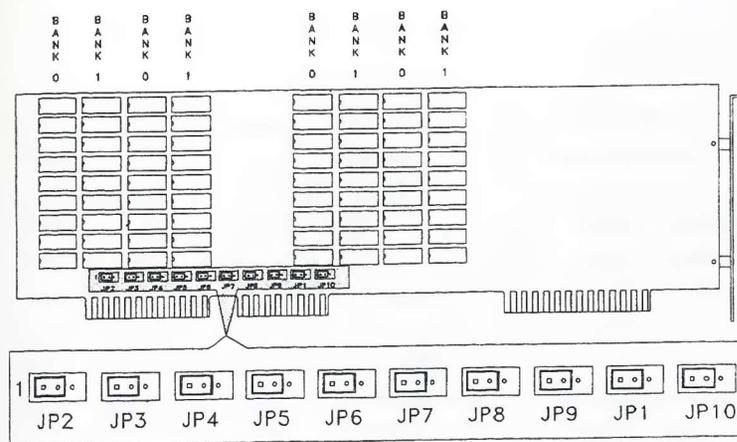


Figure 4: DIP8P configured as Banks 0 & 1 of the PAT38PI

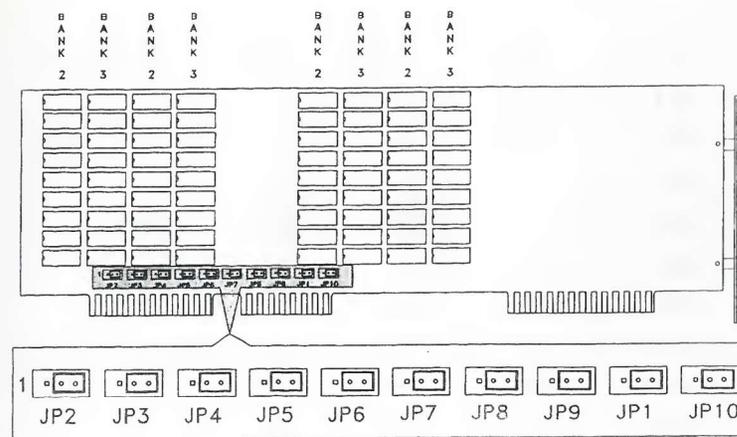


Figure 5: DIP8P configured as Banks 2 & 3 of the PAT38PI