SEALEVEL SYSTEMS, INC.
PIO-48

Card Type	Parallel card
Chip Set	Unidentified
I/O Options	Parallel ports (2), 40-pin connectors (2)
Data Bus	8-bit ISA
Card Size	Half-length, full-height
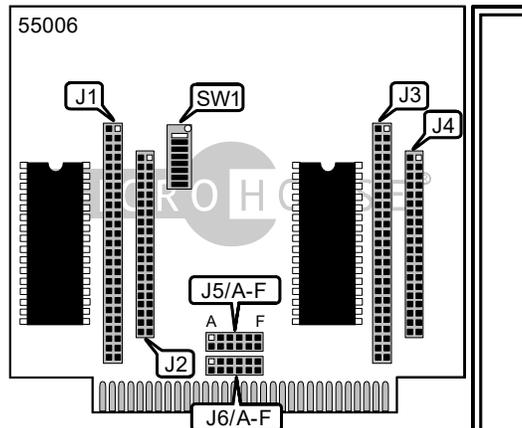
CONNECTIONS			
Function	Label	Function	Label
Parallel port 1	J1	Parallel port 2	J3
40-pin connector A	J2	40-pin connector B	J4

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Wait State enabled	SW1/8	On
Wait State disabled	SW1/8	Off

BASE I/O ADDRESS SELECTION							
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
280h	Off	On	Off	On	On	On	On
2A0h	Off	On	Off	On	Off	On	On
1F8	Off	Off	Off	Off	Off	Off	On
2F8h	Off	Off	Off	Off	Off	On	Off
378h	Off	Off	Off	Off	On	Off	Off
3B8h	Off	Off	Off	On	Off	Off	Off
3D8h	Off	Off	On	Off	Off	Off	Off
3E8h	Off	On	Off	Off	Off	Off	Off
3F0h	On	Off	Off	Off	Off	Off	Off
3F8h	Off	Off	Off	Off	Off	Off	Off
Note:	A total of 255 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/7 is the Most Significant Bit and switch SW1/1 is the Least Significant Bit. The switches have the following decimal values: SW1/7=512, SW1/6=256, SW1/5=128, SW1/4=64, SW1/3=32, SW1/2=16, SW1/1=8. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)						

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PARALLEL PORT 1 INTERRUPT SELECTION						
IRQ	J5/A	J5/B	J5/C	J5/D	J5/E	J5/F
IRQ2	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed

PARALLEL PORT 2 INTERRUPT SELECTION						
IRQ	J6/A	J6/B	J6/C	J6/D	J6/E	J6/F
IRQ2	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed