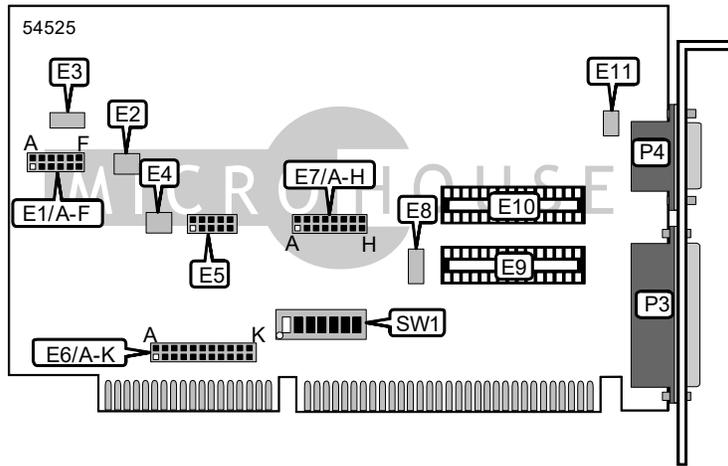


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ACB 56

| | |
|-------------------------|--------------------------|
| Card Type | Serial card |
| Chip Set | Unidentified |
| Maximum Onboard Memory | Unidentified |
| I/O Options | Serial ports (2) |
| Hard Drives supported | None |
| Floppy drives supported | None |
| Data Bus | 16-bit ISA |
| Card Size | Full-length, full-height |



| CONNECTIONS | | | |
|----------------------|-------|------------------------|-------|
| Function | Label | Function | Label |
| Serial Port 1 (V.35) | P3 | Serial Port 2 (RS-232) | P4 |

| USER CONFIGURABLE SETTINGS | | | |
|-------------------------------------|-------|--------------|--|
| Function | Label | Position | |
| í Factory configured - do not alter | E2 | Unidentified | |
| í Factory configured - do not alter | E3 | Unidentified | |
| í Factory configured - do not alter | E4 | Unidentified | |
| í Factory configured - do not alter | E8 | Unidentified | |
| í Port 2 enabled | E9 | Closed | |
| Port 2 disabled | E9 | Open | |
| í Port 1 enabled | E10 | Closed | |
| Port 1 disabled | E10 | Open | |
| í Factory configured - do not alter | E11 | Unidentified | |

Note: The size and orientation of E2, E3, E4, E8, and E11 is unidentified.

| BASE I/O ADDRESS SELECTION | | | | | | | |
|----------------------------|-------|-------|-------|-------|-------|-------|-------|
| Setting | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 |
| í 238h | Off | On | On | On | Off | Off | Off |
| 280h | Off | On | Off | On | On | On | On |
| 2A0h | Off | On | Off | On | Off | On | On |
| 2E8h | Off | On | Off | Off | Off | On | Off |
| 300h | Off | Off | On | On | On | On | On |
| 328h | Off | Off | On | On | Off | On | Off |
| 3E8h | Off | Off | Off | Off | Off | On | Off |

Continued on next page . . .

| SERIAL PORT INTERRUPT SELECTION | | | | | | | | | |
|---------------------------------|--------|--------|--------|--------|--------|--------|--------|--------|--------|
| IRQ | E6/A | E6/B | E6/C | E6/D | E6/E | E6/F | E6/G | E6/H | E6/I |
| 2/9 | Closed | Open |
| 3 | Open | Closed | Open |
| 4 | Open | Open | Closed | Open | Open | Open | Open | Open | Open |
| 5 | Open | Open | Open | Closed | Open | Open | Open | Open | Open |
| 7 | Open | Open | Open | Open | Closed | Open | Open | Open | Open |
| 10 | Open | Open | Open | Open | Open | Closed | Open | Open | Open |
| 11 | Open | Open | Open | Open | Open | Open | Closed | Open | Open |
| 12 | Open | Closed | Open |
| 15 | Open | Closed |

| SERIAL INTERRUPT MODE SELECTION | | |
|---------------------------------|--------|--------|
| IRQ | E6/J | E6/K |
| Normal interrupt mode | Closed | Open |
| Shared interrupt mode | Open | Closed |

| DMA TRI-STATE DRIVER MODE | | |
|--|--------|--------|
| Mode | E7/G | E7/H |
| DMA -State drivers permanently enabled | Closed | Open |
| DMA Tri-State drivers enabled by status/control port bit 7 | Open | Closed |
| DMA disabled | Open | Open |

| DMA CHANNEL SELECTION (HALF-DUPLEX) | | | | | | |
|-------------------------------------|------|------|------|--------|--------|--------|
| Channel | E1/A | E1/B | E1/C | E1/D | E1/E | E1/F |
| 1 | Open | Open | Open | Closed | Open | Closed |
| 3 | Open | Open | Open | Closed | Closed | Open |

Note: DMA must be enabled by setting the DMA Tri-State driver mode.

| DMA CHANNEL SELECTION (HALF-DUPLEX) (CON'T) | | | | | | |
|---|------|--------|------|--------|------|------|
| Channel | E7/A | E7/B | E7/C | E7/D | E7/E | E7/F |
| 1 | Open | Open | Open | Closed | Open | Open |
| 3 | Open | Closed | Open | Open | Open | Open |

| DMA CHANNEL SELECTION (FULL DUPLEX) | | | | | | | |
|-------------------------------------|--------|------|------|--------|------|------|--|
| Channel | E1/A | E1/B | E1/C | E1/D | E1/E | E1/F | |
| 1 Receive/3 Transmit data | Closed | Open | Open | Closed | Open | Open | |
| 1 Transmit/3 Receive data | Closed | Open | Open | Closed | Open | Open | |

Note: DMA must be enabled by setting the DMA Tri-State driver mode.

SEALEVEL SYSTEMS, INC.
ACB 56

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| DMA CHANNEL SELECTION (FULL DUPLEX) (CON') | | | | | | |
|--|--------|--------|--------|--------|------|--------|
| Channel | E7/A | E7/B | E7/C | E7/D | E7/E | E7/F |
| 1 Receive/3 Transmit data | Closed | Open | Open | Closed | Open | Closed |
| f1 Transmit/3 Receive data | Open | Closed | Closed | Open | Open | Closed |

| CLOCK INPUT MODES | |
|------------------------------|--------------------|
| MODE | E5 |
| Normal receive | Pins 1 & 2 closed |
| Inverted receive | Pins 3 & 4 closed |
| Normal transmit | Pins 5 & 6 closed |
| Inverted transmit | Pins 7 & 8 closed |
| Transmit clock input enabled | Pins 9 & 10 closed |