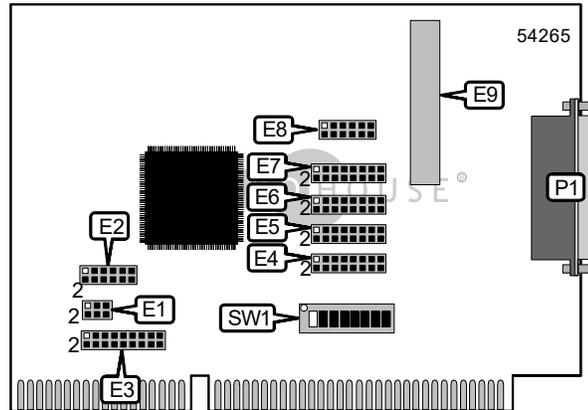


# SEA LEVEL SYSTEMS, INC.

## ACB-530

**Card Type**                    Serial card  
**Chip Set**                      Unidentified  
**I/O Options**                Serial port  
**Data Bus**                     16-bit ISA



CONNECTIONS	
Function	Label
Serial port	P1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured – do not alter	E2	Unidentified
í Factory configured – do not alter	E9	Unidentified
í Factory configured – do not alter	SW1/8	Unidentified

SERIAL PORT INTERRUPT SELECTION	
IRQ	E3
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
í 5	Pins 7 & 8 closed
7	Pins 9 & 10 closed
10	Pins 11 & 12 closed
11	Pins 13 & 14 closed
12	Pins 15 & 16 closed
15	Pins 17 & 18 closed

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SERIAL PORT INTERRUPT MODE SELECTION	
Setting	E1
DMA terminal count IRQ	Pins 1 & 2 closed
Single IRQ	Pins 3 & 4 closed
Shared IRQ	Pins 5 & 6 closed

BASE I/O ADDRESS SELECTION							
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
í 238	Off	On	On	On	Off	Off	Off
280	Off	On	Off	On	On	On	On
2A0	Off	On	Off	On	Off	On	On
2E8	Off	On	Off	Off	Off	On	Off
300	Off	Off	On	On	On	On	On
328	Off	Off	On	On	Off	On	Off
3E8	Off	Off	Off	Off	Off	On	Off

DMA SELECTION	
Setting	E2
Software enabled channel A	Pins 1 & 2 closed
Channel A always enabled	Pins 3 & 4 closed
No DMA for channel A	Pins 5 & 6 closed
Software enabled channel B	Pins 7 & 8 closed
Channel B always enabled	Pins 9 & 10 closed
No DMA for channel B	Pins 11 & 12 closed

DMA SELECTION (SINGLE CHANNEL A HALF DUPLEX)		
Setting	E4	E5
Channel A/channel 0, half duplex channel B no DMA	Open	1 & 2, 3 & 4
Channel A/channel 1, half duplex channel B no DMA	Open	5 & 6, 7 & 8
Channel A/channel 2, half duplex channel B no DMA	Open	9 & 10, 11 & 12
Channel A/channel 3, half duplex channel B no DMA	Open	13 & 14, 15 & 16
Note: Pins designated should be in the closed position.		

DMA SELECTION (SINGLE CHANNEL B HALF DUPLEX)		
Setting	E6	E7
Channel B/channel 0, half duplex channel A no DMA	Open	1 & 2, 3 & 4
Channel B/channel 1, half duplex channel A no DMA	Open	5 & 6, 7 & 8
Channel B/channel 2, half duplex channel A no DMA	Open	9 & 10, 11 & 12
Channel B/channel 3, half duplex channel A no DMA	Open	13 & 14, 15 & 16
Note: Pins designated should be in the closed position.		

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DMA SELECTION (TWO CHANNELS HALF DUPLEX)				
Setting	E4	E5	E6	E7
Channel A/channel 1, half duplex	Open	5 & 6, 7 & 8	Open	13 & 14, 15 & 16
Channel B/channel 3, half duplex	Open	5 & 6, 7 & 8	Open	13 & 14, 15 & 16
Channel A/channel 0, half duplex	Open	1 & 2, 3 & 4	Open	9 & 10, 11 & 12
Channel B/channel 2, half duplex	Open	1 & 2, 3 & 4	Open	9 & 10, 11 & 12
Note: Pins designated should be in the closed position.				

DMA SELECTION (FOUR CHANNELS FULL DUPLEX)				
Setting	E4	E5	E6	E7
Channel A/channel 1, receive data	13 & 14, 15 & 16	5 & 6, 7 & 8	9 & 10, 11 & 12	1 & 2, 3 & 4
Channel A/channel 3, transmit data	13 & 14, 15 & 16	5 & 6, 7 & 8	9 & 10, 11 & 12	1 & 2, 3 & 4
Channel B/channel 0, receive	13 & 14, 15 & 16	5 & 6, 7 & 8	9 & 10, 11 & 12	1 & 2, 3 & 4
Channel B/channel 2, transmit	13 & 14, 15 & 16	5 & 6, 7 & 8	9 & 10, 11 & 12	1 & 2, 3 & 4
Note: Pins designated should be in the closed position.				

DMA SELECTION (CHANNEL A FULL DUPLEX)		
Setting	E4	E5
Channel A/channel 1, receive data	13 & 14, 15 & 16	5 & 6, 7 & 8
Channel A/channel 3, transmit data	13 & 14, 15 & 16	5 & 6, 7 & 8
Note: Pins designated should be in the closed position.		

DMA SELECTION (CHANNEL B FULL DUPLEX)		
Setting	E6	E7
Channel B/channel 1, receive data	13 & 14, 15 & 16	5 & 6, 7 & 8
Channel B/channel 3, transmit data	13 & 14, 15 & 16	5 & 6, 7 & 8
Note: Pins designated should be in the closed position.		

DMA SELECTION (NO DMA SELECTED)				
Setting	E4	E5	E6	E7
No DMA	Open	Open	Open	Open

TRANSMIT CLOCK SELECTION	
Setting	E8
Channel A RTS enabled (RS-485)	Pins 1 & 2 closed
Transmit clock output channel A	Pins 3 & 4 closed
Transmit clock input channel A	Pins 5 & 6 closed
Channel B RTS enabled (RS-485)	Pins 7 & 8 closed
Transmit clock output channel B	Pins 9 & 10 closed
Transmit clock input channel B	Pins 11 & 12 closed