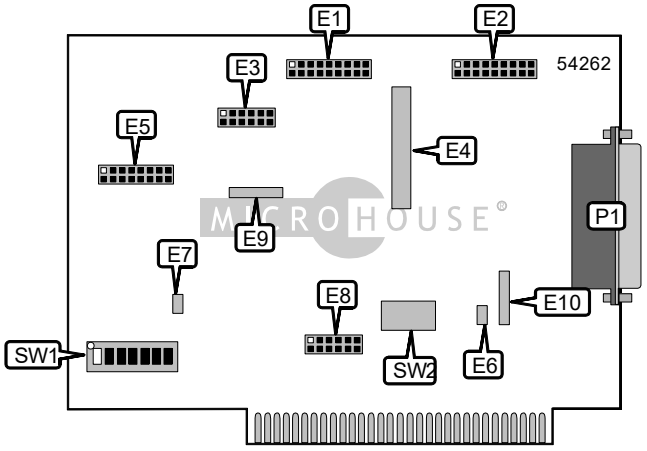
SEA LEVEL SYSTEMS, INC.
A C B - I V

Card Type	Serial card
Chip Set	Unidentified
I/O Options	Serial port
Data Bus	8-bit ISA



CONNECTIONS	
Function	**Label**
Serial port	P1

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured – do not alter	E4	Unidentified
í Factory configured – do not alter	E6	Unidentified
í Factory configured – do not alter	E7	Unidentified
í Factory configured – do not alter	E9	Unidentified
í Factory configured – do not alter	E10	Unidentified
í Factory configured – do not alter	SW2	Unidentified

SERIAL PORT 1 SELECTION	
Setting	**E2**
Enabled (RS-485 only)	Pins 1 & 2 closed
Transmit clock input (TXC in on pins 12 & 15 of P1)	Pins 3 & 4 closed
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 5 & 6 closed
Receive clock input (RXC in on pins 9 & 17)	Pins 7 & 8 closed
Not used	Pins 9 & 10 open
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 11 & 12 closed
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 13 & 14 closed
Not used	Pins 15 & 16 open
Not used	Pins 17 & 18 open

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SERIAL PORT 2 SELECTION	
Setting	**E1**
Enabled (RS-485 only)	Pins 1 & 2 closed
Transmit clock input (TXC in on pins 12 & 15 of P1)	Pins 3 & 4 closed
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 5 & 6 closed
Receive clock input (RXC in on pins 9 & 17)	Pins 7 & 8 closed
Not used	Pins 9 & 10 open
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 11 & 12 closed
Transmit clock output (TXC out on pins 12 & 15 of P1)	Pins 13 & 14 closed
Not used	Pins 15 & 16 open
Not used	Pins 17 & 18 open

SERIAL PORT INTERRUPT SELECTION	
IRQ	**E8**
2/9	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
Normal IRQ mode	Pins 9 & 10 closed
Multi IRQ mode	Pins 11 & 12 closed

BASE I/O ADDRESS SELECTION							
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**	**SW1/7**
238	Off	On	On	On	Off	Off	Off
280	Off	On	Off	On	On	On	On
2A0	Off	On	Off	On	Off	On	On
2E8	Off	On	Off	Off	Off	On	Off
300	Off	Off	On	On	On	On	On
328	Off	Off	On	On	Off	On	Off
3E8	Off	Off	Off	Off	Off	On	Off

DMA CHANNEL SELECTION	
Channel	**E3**
1 – two channel mode	Pins 1 & 2 closed
3 – two channel mode	Pins 1 & 2 closed
Two channel A/B mode A3B1	Pins 3 & 4 closed
Two channel A/B mode A1B3	Pins 5 & 6 closed
Closed = channel A, Open = channel B	Pins 7 & 8 closed
3 – acknowledges channel 3	Pins 9 & 10 closed
1 – acknowledges channel 1	Pins 11 & 12 closed

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DMA SELECTION	
Setting	**E5**
SCC channel A/B uses channel 3	Pins 1 & 2 closed
SCC channel A uses channel 3	Pins 3 & 4 closed
SCC channel A/B uses channel 1	Pins 5 & 6 closed
SCC channel A uses channel 3	Pins 7 & 8 closed
SCC channel B enabled for half duplex DMA transfers	Pins 9 & 10 closed
SCC channel A – DMA channel 1/3 for full duplex transfers	Pins 11 & 12 closed
DMA tri state drivers permanently enabled	Pins 13 & 14 closed
DMA tri state drivers enabled by status control port bit 7	Pins 15 & 16 closed