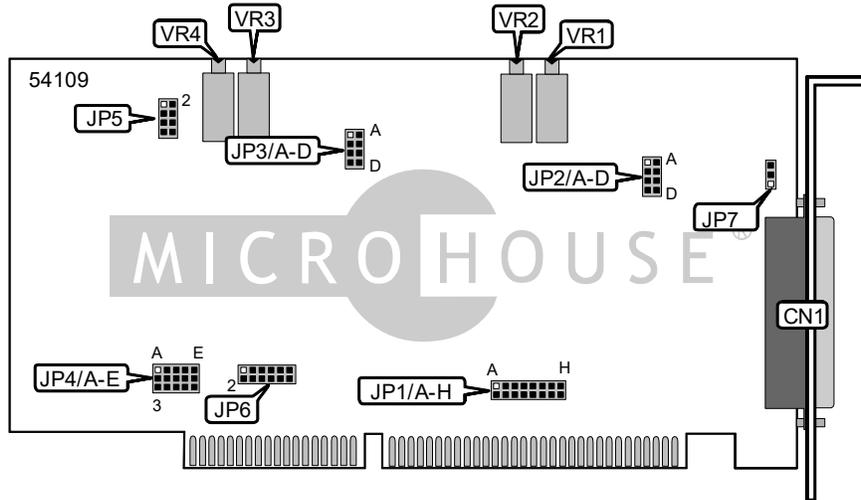


AQUTEK CORPORATION PA-MD12, PA-MD16

Card Type	Data Acquisition
Chip Set	None
Maximum Onboard Memory	Unidentified
I/O Options	Analog/digital I/O
Data Bus	16-bit ISA
Card Size	Three-quarter length



CONNECTIONS			
Function	Label	Function	Label
37-pin analog/digital signals connector	CN1	D/A 1 converter range selector	VR2
External clock input	JP7	D/A 2 converter offset selector	VR3
D/A 1 converter offset selector	VR1	D/A 2 converter range selector	VR4

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	JP5	Pins 1 & 2, 3 & 4 closed

DMA CHANNEL SELECTION	
Channel	JP6
0	Pins 9 & 10, 11 & 12 closed
1	Pins 5 & 6, 7 & 8 closed
3	Pins 1 & 2, 3 & 4 closed

D/A 1 OUTPUT VOLTAGE RANGE SELECTION				
Voltage	JP2/A	JP2/B	JP2/C	JP2/D
í 0V to 5V	Open	Open	Closed	Open
0V to 10V	Open	Closed	Open	Open
-5V to 5V	Closed	Open	Closed	Open
-10V to 10V	Closed	Closed	Open	Open

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D/A 2 OUTPUT VOLTAGE RANGE SELECTION				
Voltage	JP3/A	JP3/B	JP3/C	JP3/D
í 0V to 5V	Open	Open	Closed	Open
0V to 10V	Open	Closed	Open	Open
-5V to 5V	Closed	Open	Closed	Open
-10V to 10V	Closed	Closed	Open	Open

BASE I/O ADDRESS SELECTION								
Setting	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F	JP1/G	JP1/H
000h	Closed							
010h	Closed	Open						
020h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Closed
030h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Open
040h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
í 2E0h	Closed	Closed	Open	Closed	Open	Open	Open	Closed
FB0h	Open	Open	Open	Open	Open	Closed	Open	Open
FC0h	Open	Open	Open	Open	Open	Open	Closed	Closed
FD0h	Open	Open	Open	Open	Open	Open	Closed	Open
FE0h	Open	Closed						
FF0h	Open							

Note: A total of 256 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. JP1/A is the Most Significant Bit and switch JP1/H is the Least Significant Bit. The switches have the following decimal values: JP4/A=2048, JP1/B=1024, JP1/C=512, JP1/D=256, JP1/E=128, JP1/F=64, JP1/G=32, JP1/H=16. Turn off the switches and add the values of the switches to obtain the correct memory address. (Open=1, Closed=0)

IRQ INTERRUPT SELECTION					
IRQ	JP4/A	JP4/B	JP4/C	JP4/D	JP4/E
IRQ2	Open	Open	Open	Open	Pins 1 & 2
IRQ3	Open	Open	Open	Pins 1 & 2	Open
IRQ4	Open	Open	Pins 1 & 2	Open	Open
í IRQ5	Open	Pins 1 & 2	Open	Open	Open
IRQ7	Pins 1 & 2	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Pins 2 & 3
IRQ11	Open	Open	Open	Pins 2 & 3	Open
IRQ12	Open	Open	Pins 2 & 3	Open	Open
IRQ14	Open	Pins 2 & 3	Open	Open	Open
IRQ15	Pins 2 & 3	Open	Open	Open	Open
Disabled	Open	Open	Open	Open	Open

Note: Pins designated are in the closed position. IRQ10, IRQ11, IRQ12, IRQ14 & IRQ15 are for AT compatible buses; PC buses will not support them.