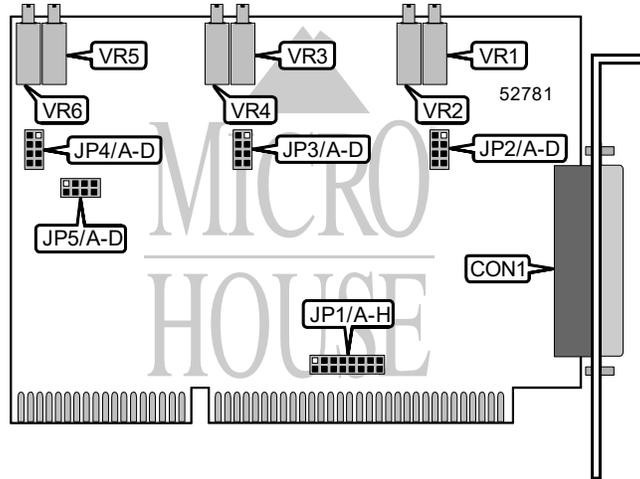


# ACQUTEK CORPORATION

## PA-DA12, PA-DA16

**Card Type** Data acquisition  
**Chipset** Unidentified  
**I/O Options** Analog output  
**Data Bus** 8/16 bit ISA



CONNECTIONS			
Function	Label	Function	Label
Analog output (see pinout below)	CON1	D/A converter 2 offset selector	VR4
D/A converter 1 range selector	VR1	D/A converter 3 range selector	VR5
D/A converter 1 offset selector	VR2	D/A converter 3 offset selector	VR6
D/A converter 2 range selector	VR3		

CON1 PINOUT			
Function	Pin	Function	Pin
D/A 1 voltage ground	1	D/A 1 voltage output	20
D/A 2 voltage ground	2	D/A 2 voltage output	21
D/A 3 voltage ground	3	D/A 3 voltage output	22
Not used	4-10	Not used	23-28
D/A 1 current ground	11	D/A 1 current output	29
D/A 2 current ground	12	D/A 2 current output	30
D/A 3 current ground	13	D/A 3 current output	31
Not used	14-19	Not used	32-37

USER CONFIGURABLE SETTINGS			
Setting	Label	Position	
í Factory configured - do not alter	JP2/D	Open	
í Factory configured - do not alter	JP3/D	Open	
í Factory configured - do not alter	JP4/D	Open	
í Factory configured - do not alter	JP5/A	Unidentified	
í Factory configured - do not alter	JP5/B	Unidentified	
í Factory configured - do not alter	JP5/C	Unidentified	
í Factory configured - do not alter	JP5/D	Unidentified	

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# ACQUTEK CORPORATION

## PA-DA12, PA-DA16

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BASE I/O ADDRESS SELECTION								
Setting	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F	JP1/G	JP1/H
000h	Closed							
010h	Closed	Open						
020h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Closed
030h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Open
040h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
0E0h	Closed	Closed	Open	Closed	Open	Open	Open	Closed
FB0h	Open	Open	Open	Open	Open	Closed	Open	Open
FC0h	Open	Open	Open	Open	Open	Open	Closed	Closed
FD0h	Open	Open	Open	Open	Open	Open	Closed	Open
FE0h	Open	Closed						
FF0h	Open							

**Note:** A total of 256 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. JP1/A is the Most Significant Bit and jumper JP1/H is the Least Significant Bit. The jumpers have the following decimal values: JP1/A=2048, JP1/B=1024, JP1/C=512, JP1/D=256, JP1/E=128, JP1/F=64, JP1/G=32, JP1/H=16. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0)

OUTPUT VOLTAGE 1 SELECTION			
Setting	JP2/A	JP2/B	JP2/C
0V to 5V	Open	Open	Closed
0V to 10V	Open	Closed	Open
-5V to 5V	Closed	Open	Closed
-10V to 10V	Closed	Closed	Open

OUTPUT VOLTAGE 2 SELECTION			
Setting	JP3/A	JP3/B	JP3/C
0V to 5V	Open	Open	Closed
0V to 10V	Open	Closed	Open
-5V to 5V	Closed	Open	Closed
-10V to 10V	Closed	Closed	Open

OUTPUT VOLTAGE 3 SELECTION			
Setting	JP4/A	JP4/B	JP4/C
0V to 5V	Open	Open	Closed
0V to 10V	Open	Closed	Open
-5V to 5V	Closed	Open	Closed
-10V to 10V	Closed	Closed	Open