EICON TECHNOLOGY CORPORATION
EICON/G. DIEHL QUADRO

Card Type	ISDN TA
Chip Set	Unidentified
Transfer Rate	64Kbps x 4
Data Bus	16-bit ISA
ISDN Protocol	1TR6, ATEL, AT&T, Northern, DMS100, DSS1, ETSI, Q.931, other country specific
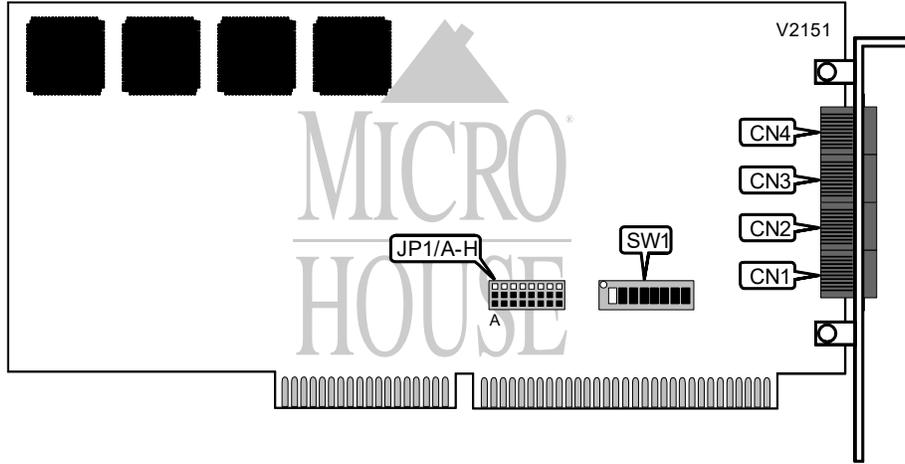
CONNECTIONS			
Function	**Label**	**Function**	**Label**
ISDN line 1	CN1	ISDN line 3	CN3
ISDN line 2	CN2	ISDN line 4	CN4

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	SW1/1	On
í Factory configured - do not alter	SW1/2	On

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BASE I/O ADDRESS SELECTION						
Setting	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**	**SW1/7**	**SW1/8**
8000h	On	On	On	On	On	On
8200h	Off	On	On	On	On	On
8400h	On	Off	On	On	On	On
8600h	Off	Off	On	On	On	On
8800h	On	On	Off	On	On	On
D000h	On	On	On	Off	On	Off
F600h	Off	Off	On	Off	Off	Off
F800h	On	On	Off	Off	Off	Off
FA00h	Off	On	Off	Off	Off	Off
FC00h	On	Off	Off	Off	Off	Off
FE00h	Off	Off	Off	Off	Off	Off

Note: A total of 64 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/8 is the Most Significant Bit and switch SW1/3 is the Least Significant Bit. The switches have the following decimal values: SW1/8=16384, SW1/7=8192, SW1/6=4096, SW1/5=2048, SW1/4=1024, SW1/3=512. Turn switches off and add their values to a *base value* of 32768. Convert to hexadecimal to obtain the correct memory address. (Off=1, On=0)

INTERRUPT								
IRQ	**JP1/A**	**JP1/B**	**JP1/C**	**JP1/D**	**JP1/E**	**JP1/F**	**JP1/G**	**JP1/H**
2	Open	Open	Open	Open	Open	Open	Open	Pins 1 & 2
3	Open	Open	Open	Open	Open	Open	Pins 1 & 2	Open
4	Open	Open	Open	Open	Open	Pins 1 & 2	Open	Open
5	Open	Open	Open	Open	Pins 1 & 2	Open	Open	Open
7	Open	Open	Open	Pins 1 & 2	Open	Open	Open	Open
10	Open	Open	Pins 1 & 2	Open	Open	Open	Open	Open
11	Open	Pins 1 & 2	Open	Open	Open	Open	Open	Open
12	Pins 1 & 2	Open	Open	Open	Open	Open	Open	Open

Note: Pins designated are in the closed position.