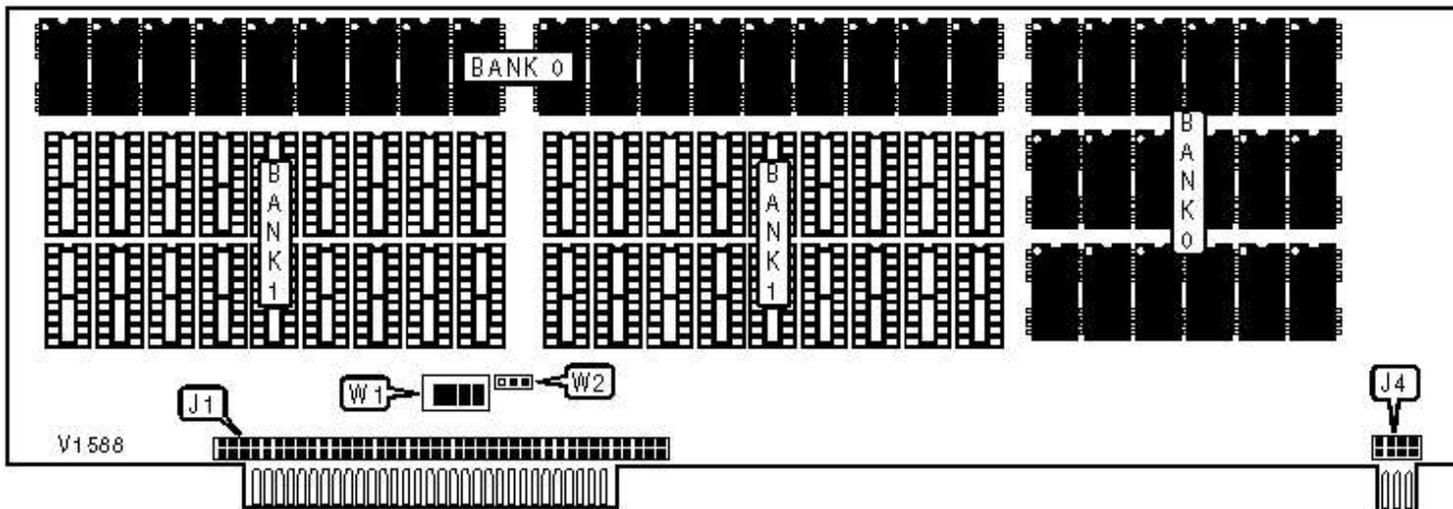


DTK COMPUTER, INC.

PEI-303

Card Type Memory card
Chipset Controller N/A
I/O Options Memory
Maximum DRAM 8MB



CONNECTIONS			
Purpose	Location	Purpose	Location
80-pin memory board interface	J1	8-pin memory board interface	J4

Note: J1 & J2 are for installation of a second memory board to the PEI-303.

DRAM CHIP SIZE CONFIGURATION		
Chip	W1/3	W2
256KB	Off	Pins 2 & 3 closed
1MB	On	Pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(36) 256K x 1	NONE
2MB	(36) 256K x 1	(36) 256K x 1

4MB	(36) 1024K x 1	NONE
8MB	(36) 1024K x 1	(36) 1024K x 1

FACTORY CONFIGURED - DO NOT ALTER	
Switch	Position
W1/1	Off
W1/2	Off
W1/4	Off

MISCELLANEOUS TECHNICAL NOTE
The second memory board is restricted to use only 1MB DRAM chips when connected to the PEI-303 board.