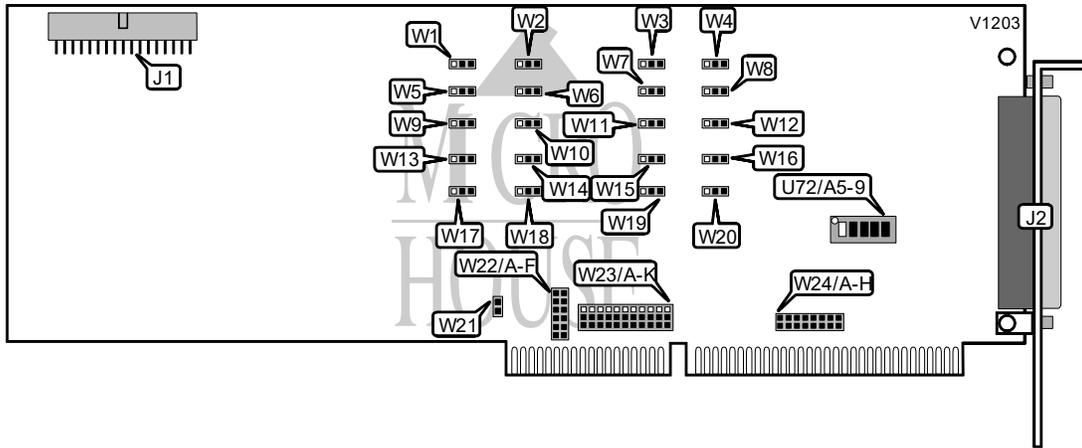


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Card Type Analog to digital I/O converter
Chipset Controller Unidentified
I/O Options Analog input, analog output, digital input, digital output
Maximum DRAM N/A



CONNECTIONS			
Position	Location	Position	Location
34-pin interface connector	J1	50-pin I/O connector	J2

BASE I/O ADDRESS SELECTION					
Address	U72/A5	U72/A6	U72/A7	U72/A8	U72/A9
01C0h	On	Off	Off	Off	On
000h	On	On	On	On	On
020h	Off	On	On	On	On
040h	On	Off	On	On	On
060h	Off	Off	On	On	On
080h	On	On	Off	On	On
360h	Off	Off	On	Off	Off
380h	On	On	Off	Off	Off
3A0h	Off	On	Off	Off	Off
3C0h	On	Off	Off	Off	Off
3E0h	Off	Off	Off	Off	Off

Note: A total of 255 base address settings are available. The switches are a binary representation of the decimal memory addresses. Switch A9 is the Most Significant Bit and switch A5 is the Least Significant Bit. The switches have the following decimal values: switch A9=512, A8=256, A7=128, A6=64, A5=32. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

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DMA CHANNEL SELECTION (TABLE 1)						
DMA	W22/A	W22/B	W22/C	W22/D	W22/E	W22/F
5	Closed	Closed	Open	Open	Open	Open
6	Open	Open	Closed	Closed	Open	Open
7	Open	Open	Open	Open	Closed	Closed

Note: DMA channel selection is for EISA or AT bus.

DMA CHANNEL SELECTION (TABLE 2)								
DMA	W24/A	W24/B	W24/C	W24/D	W24/E	W24/F	W24/G	W24/H
0	Closed	Closed	Open	Open	Open	Open	Open	Open
1	Open	Open	Closed	Closed	Open	Open	Open	Open
2	Open	Open	Open	Open	Closed	Closed	Open	Open
3	Open	Open	Open	Open	Open	Open	Closed	Closed

Note: DMA channel selection is for EISA bus only.

INTERRUPT SELECTION (GROUP 1)						
IRQ	W23/A	W23/B	W23/C	W23/D	W23/E	W23/F
11	Open	Open	Open	Open	Open	Open
2/9	Open	Open	Open	Open	Open	Pins 1 & 2
3	Pins 1 & 2	Open	Open	Open	Open	Open
4	Open	Pins 1 & 2	Open	Open	Open	Open
5	Open	Open	Pins 1 & 2	Open	Open	Open
6	Open	Open	Open	Pins 1 & 2	Open	Open
7	Open	Open	Open	Open	Pins 1 & 2	Open
10	Open	Open	Open	Open	Open	Open
12	Open	Open	Open	Open	Open	Open
14	Open	Open	Open	Open	Open	Open
15	Open	Open	Open	Open	Open	Open

Note: Pins designated are in the closed position.

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INTERRUPT SELECTION (GROUP 1 CON'T)						
IRQ	W23/G	W23/H	W23/I	W23/J	W23/K	
i 11	Open	Pins 1 & 2	Open	Open	Open	
2/9	Open	Open	Open	Open	Open	
3	Open	Open	Open	Open	Open	
4	Open	Open	Open	Open	Open	
5	Open	Open	Open	Open	Open	
6	Open	Open	Open	Open	Open	
7	Open	Open	Open	Open	Open	
10	Pins 1 & 2	Open	Open	Open	Open	
12	Open	Open	Pins 1 & 2	Open	Open	
14	Open	Open	Open	Pins 1 & 2	Open	
15	Open	Open	Open	Open	Pins 1 & 2	

Note: Pins designated are in the closed position.

INTERRUPT SELECTION (GROUP 2)						
IRQ	W23/A	W23/B	W23/C	W23/D	W23/E	W23/F
i 12	Open	Open	Open	Open	Open	Open
2/9	Open	Open	Open	Open	Open	2 & 3
3	2 & 3	Open	Open	Open	Open	Open
4	Open	2 & 3	Open	Open	Open	Open
5	Open	Open	2 & 3	Open	Open	Open
6	Open	Open	Open	2 & 3	Open	Open
7	Open	Open	Open	Open	2 & 3	Open
10	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Open	Open	Open
14	Open	Open	Open	Open	Open	Open
15	Open	Open	Open	Open	Open	Open

Note: Pins designated are in the closed position.

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INTERRUPT SELECTION (GROUP 2 CON'T)					
IRQ	W23/G	W23/H	W23/I	W23/J	W23/K
12	Open	Open	2 & 3	Open	Open
2/9	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Open
4	Open	Open	Open	Open	Open
5	Open	Open	Open	Open	Open
6	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Open
10	2 & 3	Open	Open	Open	Open
11	Open	2 & 3	Open	Open	Open
14	Open	Open	Open	2 & 3	Open
15	Open	Open	Open	Open	2 & 3

Note: Pins designated are in the closed position.

ANALOG OUTPUT CONFIGURATION (TABLE 1)					
Type	W1 (0)	W2 (1)	W5 (2)	W6 (3)	W9 (4)
Internal	Pins 2 & 3				
External	Pins 1 & 2				

Note: Numbers in parenthesis are the output channels.
Pins designated are in the closed position.

ANALOG OUTPUT CONFIGURATION (TABLE 1 CON'T)					
Type	W10 (5)	W13 (6)	W14 (7)	W17 (8)	W18 (9)
Internal	Pins 2 & 3				
External	Pins 1 & 2				

Note: Numbers in parenthesis are the output channels.
Pins designated are in the closed position.

ANALOG OUTPUT CONFIGURATION (TABLE 2)					
Type	W3 (0)	W4 (1)	W7 (2)	W8 (3)	W11 (4)
Bipolar	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
Unipolar	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2

Note: Numbers in parenthesis are the output channels.
Pins designated are in the closed position.

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ANALOG OUTPUT CONFIGURATION (TABLE 2 CON'T)					
Type	W12 (5)	W15 (6)	W16 (7)	W19 (8)	W20 (9)
Bipolar	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
Unipolar	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
Note: Numbers in parenthesis are the output channels. Pins designated are in the closed position.					

FACTORY CONFIGURED - DO NOT ALTER	
Jumper	Position
W21	Closed