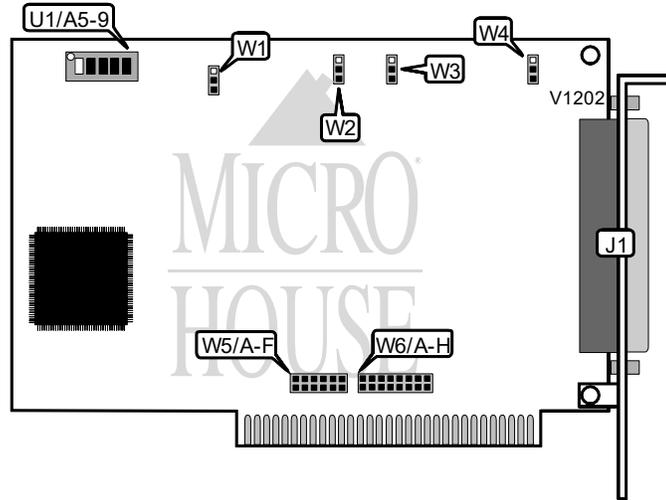


NATIONAL INSTRUMENTS
LAB - PC +

Card Type
Chipset Controller
I/O Options
Maximum DRAM

Analog to digital timing converter
 ACT A1010A
 Analog input, digital input, digital output
 N/A



CONNECTIONS	
Purpose	Location
50-pin I/O interface connector	J1

DMA CHANNEL SELECTION								
DMA	W6/A	W6/B	W6/C	W6/D	W6/E	W6/F	W6/G	W6/H
3	Open	Open	Open	Open	Open	Open	Closed	Closed
1	Open	Open	Closed	Closed	Open	Open	Open	Open
2	Open	Open	Open	Open	Closed	Closed	Open	Open
Disabled	Closed	Closed	Open	Open	Open	Open	Open	Open

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BASE I/O ADDRESS SELECTION					
Address	U1/A5	U1/A6	U1/A7	U1/A8	U1/A9
í 260h	Off	Off	On	On	Off
000h	On	On	On	On	On
020h	Off	On	On	On	On
040h	On	Off	On	On	On
060h	Off	Off	On	On	On
080h	On	On	Off	On	On
360h	Off	Off	On	Off	Off
380h	On	On	Off	Off	Off
3A0h	Off	On	Off	Off	Off
3C0h	On	Off	Off	Off	Off
3E0h	Off	Off	Off	Off	Off

Note: A total of 255 base address settings are available. The switches are a binary representation of the decimal memory addresses. Switch A9 is the Most Significant Bit and switch A5 is the Least Significant Bit. The switches have the following decimal values: switch A9=512, A8=256, A7=128, A6=64, A5=32. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

INTERRUPT SELECTION						
IRQ	W5/A	W5/B	W5/C	W5/D	W5/E	W5/F
í 5	Open	Open	Closed	Open	Open	Open
3	Closed	Open	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open	Open
6	Open	Open	Open	Closed	Open	Open
7	Open	Open	Open	Open	Closed	Open
9	Open	Open	Open	Open	Open	Closed
Disabled	Open	Open	Open	Open	Open	Open

ANALOG OUTPUT CONFIGURATION		
Type	W1 (Channel 0)	W2 (Channel 1)
í Bipolar output	Pins 1 & 2 closed	Pins 1 & 2 closed
Unipolar output	Pins 2 & 3 closed	Pins 2 & 3 closed

ANALOG INPUT CONFIGURATION	
Type	W4
í RSE input	Pins 1 & 2 closed
NRSE/DIFF input	Pins 2 & 3 closed

ANALOG INPUT POLARITY CONFIGURATION	
Type	W3
í Bipolar input	Pins 1 & 2 closed
Unipolar input	Pins 2 & 3 closed