VISIONETICS INTERNATIONAL
VIGA GENLOCK+

Category	Video
Video Types Supported	XVGA
Video Processor	Tseng Labs ET4000 (Exact location is unidentified)
Highest Resolution Supported	1024 x 768
Data Bus Type	16-bit ISA
Memory Type	VRAM
Maximum Onboard Memory	1MB (Exact location of memory is unidentified)
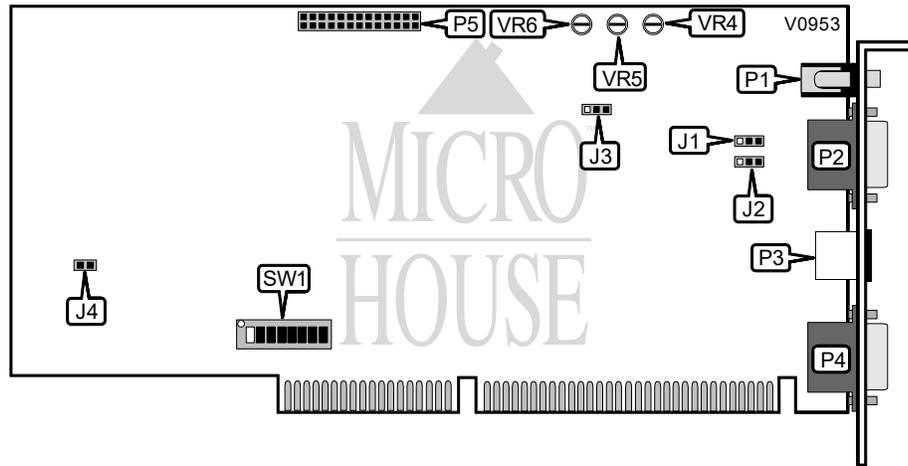
CONNECTIONS			
Purpose	Location	Purpose	Location
RCA connector	P1	15-pin analog video port	P4
9-pin analog video port	P2	VGA feature connector	P5
Minin-Din connector	P3		

P2 (PIN 9) ASSIGNMENT SELECTION	
Setting	J1
C input	Pins 1 & 2 closed
R - Y in	Pins 2 & 3 closed

P2 (PIN 7) ASSIGNMENT SELECTION	
Setting	J2
Overlay out	Pins 1 & 2 closed
B - Y in	Pins 2 & 3 closed

INPUT SIGNAL SELECTION	
Setting	J3
Y/C input	Pins 1 & 2 closed
Composite input	Pins 2 & 3 closed

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INTERRUPT SELECTION	
IRQ2	J4
Enabled	Closed
Disabled	Open

VGA POWER SELECTION	
Setting	SW1/5
í Enabled	Off
Disabled	On

I/O BASE ADDRESS SELECTION				
Address	SW1/1	SW1/2	SW1/3	SW1/4
í 300h	Off	On	On	On
200h	On	On	On	On
220h	On	On	On	Off
240h	On	On	Off	On
260h	On	On	Off	Off
280h	On	Off	On	On
3C0h	Off	Off	Off	On
3E0h	Off	Off	Off	Off

BIOS SELECTION	
Setting	SW1/6
í Enabled	On
Disabled	Off

BIOS BASE ADDRESS SELECTION		
Addre s	SW1/7	SW1/8
í C800h	Off	Off
D000h	Off	On
D800h	On	Off
E000h	On	On

TEXT MODE KEY DELAY ADJUSTMENT	
Setting	VR4
Increase	Clockwise
Decrease	Counter - clockwise

GRAPHICS MODE >800 x 600 KEY DELAY ADJUSTMENT	
Setting	VR5
Increase	Clockwise
Decrease	Counter - clockwise

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GRAPHICS MODE ≤800 x 600 KEY DELAY ADJUSTMENT	
Setting	VR6
Increase	Clockwise
Decrease	Counter - clockwise