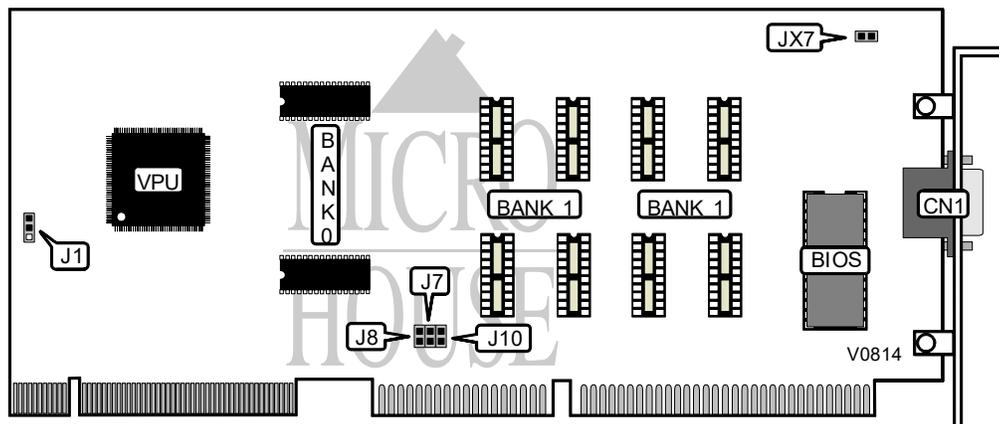


UNIDENTIFIED TGUI 9400CXI-VLB (VERSION 2)

Category	Video
Video Types Supported	XVGA
Video Processor	Trident Microsystems, Inc.
Highest Resolution Supported	1280 x 1024
Data Bus Type	VESA local-bus
Memory Type	DRAM
Maximum Onboard Memory	2MB



CONNECTIONS	
Purpose	Location
15-pin analog video port	CN1

VL-BUS TIMING	
Setting	J1
Standard	Pins 2 & 3 closed
Alternate	Pins 1 & 2 closed

VERTICAL SCAN RATE	
Setting	J7
Monitor scan rate < 48.7KHz	Closed
Monitor scan rate ≥ 48.7KHz	Open

DRAM SPEED	
DRAM Speed	J8
80ns	Closed
60ns or 70ns	Open

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UNIDENTIFIED
 TGUI 9400CXI-VLB (VERSION 2)

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DRAM CLOCK	
Setting	J10
Standard	Closed
Enhanced	Open

VL-BUS SPEED	
Setting	JX7
CPU speed ≤ 33MHz	Open
CPU speed > 33MHz	Closed

DRAM CONFIGURATION		
Size	Bank 1	Bank 2
1MB	(2) 512K x 8	None
2MB	(2) 512K x 8	(8) 256K x 4