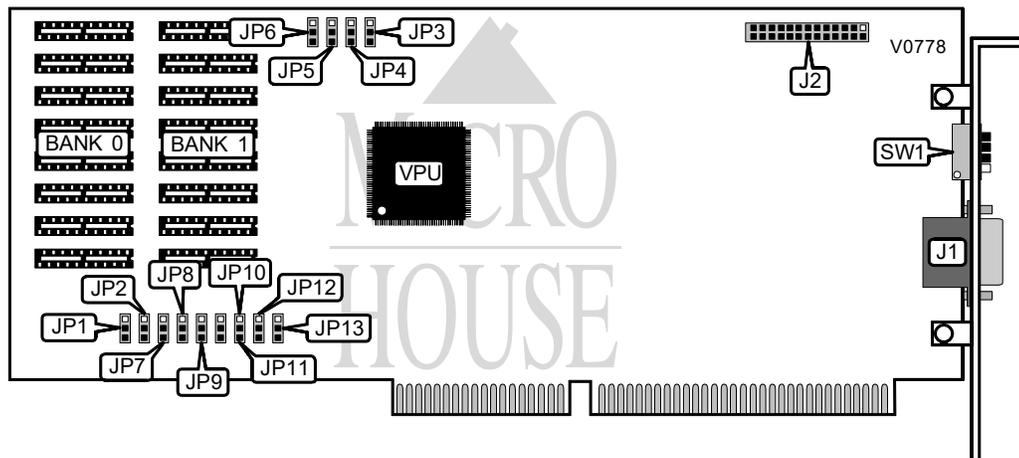# ACTIX SYSTEMS, INC.
GRAPHICS ENGINE ULTRA

Category Video
Video Types Supported XVGA
Video Processor S3 86C928
Highest Resolution 1280 x 1024
Supported
Data Bus Type 16-bit ISA
Memory Type VRAM
Maximum Onboard Memory 2MB

CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	J1	VESA Feature connector	J2

FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP3	Pins 2 & 3 closed
JP4	Pins 2 & 3 closed

INTERRUPT SELECTION				
IRQ	JP7	JP8	JP9	JP10
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
2/9	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
3	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
5	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
15	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
Note: An IRQ channel must only be selected when using a CGA or EGA monitor.				

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MONITOR TYPE SELECT			
Horizontal Retrace ⊇ 1024x768	Vertical Retrace @ 1024x768	SW1/1	SW1/2
31.5KHz - 35.5KHz	43.5Hz (Interlaced)	Off	Off
30KHz - 38KHz	43.5Hz (Interlaced)	Off	On
30KHz - 48KHz	60Hz	On	Off
30KHz - 57KHz	72Hz	On	On

WAIT STATE CONFIGURATION	
Wait States	SW1/3
í >0 wait states	Off
0 wait states	On

BIOS WIDTH SELECTION	
BIOS	SW1/4
í 8-bit	On
16-bit	Off

VIDEO MEMORY CONFIGURATION			
ize	B nk 0	B nk 1	ꞋP13
1MB	(8) 256K x 4	NONE	Pins 1 & 2 closed
2MB	(8) 256K x 4	(8) 256K x 4	Pins 2 & 3 closed

ENGINE SPEED SELECTION		
Mode	JP5	JP6
Fast	Pins 1 & 2 closed	Pins 2 & 3 closed
Slow	Pins 2 & 3 closed	Pins 1 & 2 closed

CLOCK CHIP SELECTION
Jumpers JP1, JP2, JP11, and JP12 are factory configured for the clock chip installed. They should not be changed.