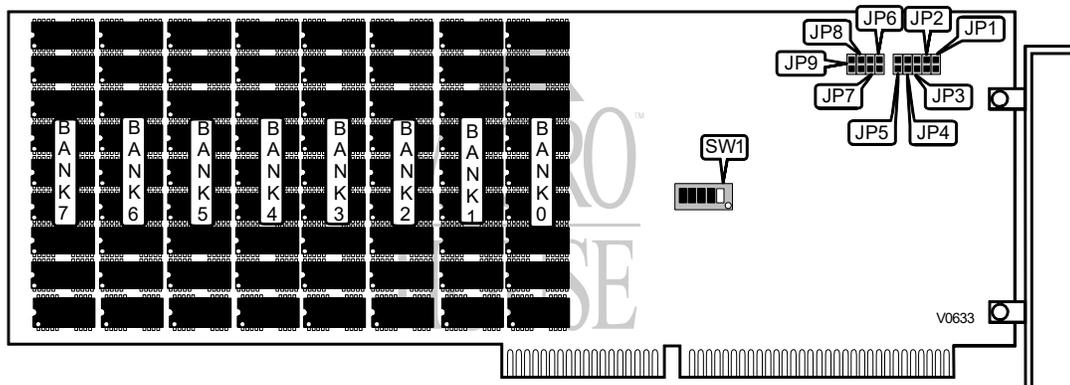# LAVA COMPUTER MFG., INC.
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Card Type   Memory card
Chipset/Controller N/A
I/O Options   N/A
Maximum DRAM   2MB

AT BOARD SELECTION		
Starting Address	System allocated RAM	SW1
640KB	0KB	On
512KB	128KB	Off
Note: This board allows for a section of the RAM to be considered existing system memory to compensate for having a machine with less than 640KB.		

DRAM ADDRESS SELECTION			
Starting Address	SW1/2	SW1/3	SW1/4
0208h	Off	Off	Off
0218h	On	Off	Off
0258h	On	On	Off
0268h	Off	On	Off
0298h	Off	Off	On
02A8h	Off	On	On
02B8h	On	Off	On
02E8h	On	On	On

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DRAM ADDRESS SELECTION	
Base Address	SW1/5
E000h	On
D000h	Off

WAIT STATE SELECT					
Number	JP1	JP2	JP3	JP4	JP5
0	Open	Open	Open	Open	Closed
1	Open	Open	Open	Closed	Open
2	Open	Open	Closed	Open	Open
3	Open	Closed	Open	Open	Open
4	Closed	Open	Open	Open	Open

DRAM SPEED SELECT				
Speed	JP6	JP7	JP8	JP9
100 nsec	Open	Open	Open	Closed
120 nsec	Open	Open	Closed	Open
150 nsec	Open	Closed	Open	Open
200 nsec	Closed	Open	Open	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
256KB	(9) 64K x 4	NONE	NONE	NONE
512KB	(9) 64K x 4	(9) 64K x 4	NONE	NONE
768KB	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4	NONE
1024KB	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4

DRAM CONFIGURATION (CONTINUED)				
Size	Bank 4	Bank 5	Bank 6	Bank 7
1280KB	(9) 64K x 4	NONE	NONE	NONE
1536KB	(9) 64K x 4	(9) 64K x 4	NONE	NONE
1792KB	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4	NONE
2048KB	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4	(9) 64K x 4