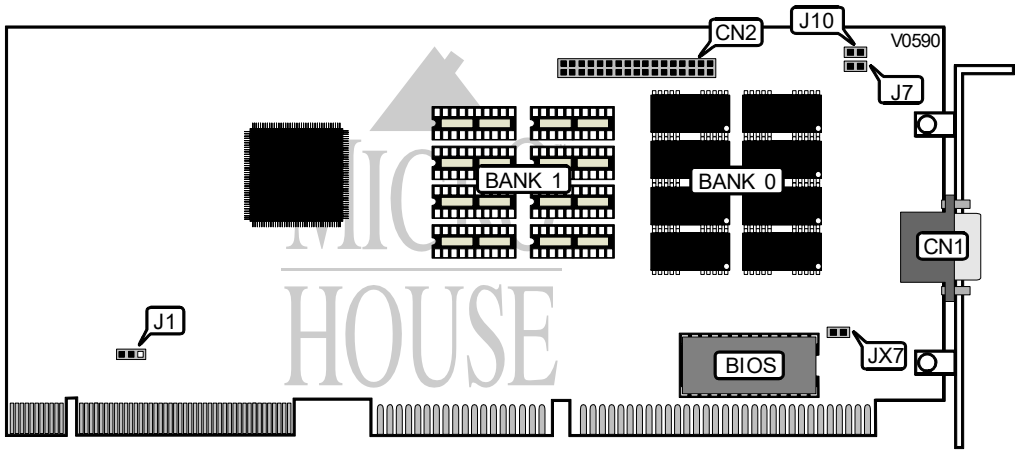
UNIDENTIFIED
TVGA9400CXI (DIP DRAM VERSION)

Category	Video
Video Types Supported	XVGA
Video Processor	Trident
Highest Resolution Supported	1280 x 1024
Data Bus Type	VESA local bus
Memory Type	DRAM
Maximum Onboard Memory	2048KB



CONNECTIONS			
Purpos ¡	Location	Purpos ¡	Location
15-pin analog video port	CN1	VESA feature connector	CN2

MONITOR INTERLACE CONFIGURATION	
Setting	J7
í Interlaced	Closed
Non-interlaced	Open

CPU CLOCK CONFIGURATION	
Speed(MHz)	JX7
í <=33MHz	Open
>33MHz	Closed

VESA LOCAL BUS TIMING	
Setting	J1
í Standard	Pins 2 & 3 closed
Alternate	Pins 1 & 2 closed

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DMCLK CONFIGURATION	
Frequency	J10
í 80MHz	Open
MHz	Closed

DRAM CONFIGURATION		
Siz	Bank 0	Bank 1
1024KB	(8) 256K x 4	NONE
2048KB	(8) 256K x 4	(8) 256K x 4