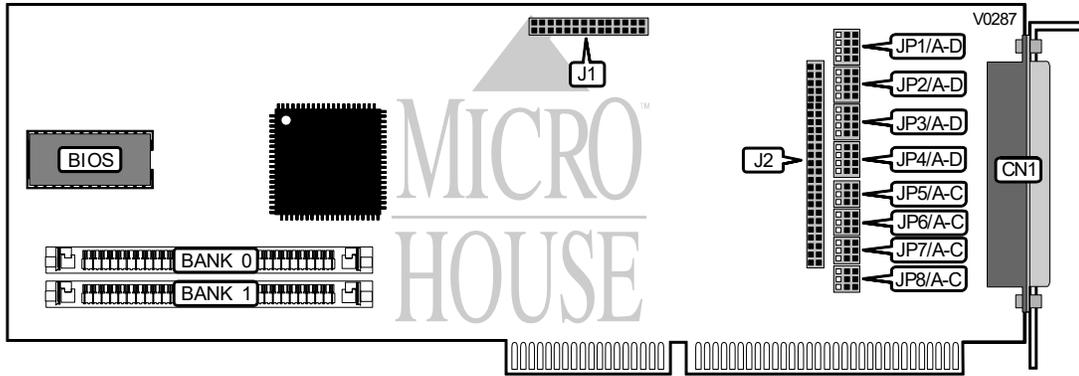


# CONTROL CORPORATION HOSTESS I/MC

**Card Type** I/O controller  
**Chipset/Controller** NEC Corporation  
**I/O Options** Serial ports (8 or 16)  
**Maximum DRAM** 8MB



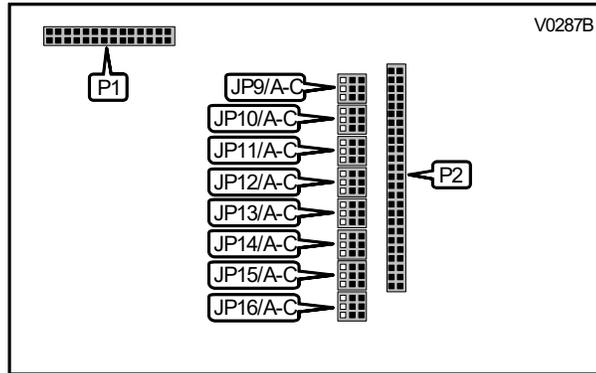
CONNECTIONS			
Purpose :	Location	Purpose :	Location
100 Pin interface connector	CN1	Upgrade connector 3	P1
Upgrade connector 1	J1	Upgrade connector 4	P2
Upgrade connector 2	J2		
Note: Connectors J1 and J2 interface with connectors P1 and P2.			

DRAM CONFIGURATION		
Size	Bank (	Bank :
2MB	(1)1Mx36	(1)1Mx36
8MB	(1)4Mx36	(1)4Mx36

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# CONTROL HOSTESS I/MC

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HOSTESS i/MC(upgrade)

RS-232 MODE SELECTION	
Jumper	RS-232
JP1/A-D	Pins 1 & 2 closed
JP2/A-D	Pins 1 & 2 closed
JP3/A-D	Pins 1 & 2 closed
JP4/A-D	Pins 1 & 2 closed
JP5/A-C	Pins 1 & 2 closed
JP6/A-C	Pins 1 & 2 closed
JP7/A-C	Pins 1 & 2 closed
JP8/A-C	Pins 1 & 2 closed
JP9/A-C	Pins 1 & 2 closed
JP10/A-C	Pins 1 & 2 closed
JP11/A-C	Pins 1 & 2 closed
JP12/A-C	Pins 1 & 2 closed
JP13/A-C	Pins 1 & 2 closed
JP14/A-C	Pins 1 & 2 closed
JP15/A-C	Pins 1 & 2 closed
JP16/A-C	Pins 1 & 2 closed

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# CONTROL HOSTESS I/MC

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RS-422 MODE SELECTION	
Jumper	RS-422
JP1/A-C	Pins 2 & 3 closed
JP1/D	Pins 1 & 2 closed
JP2/A-C	Pins 2 & 3 closed
JP2/D	Pins 1 & 2 closed
JP3/A-C	Pins 2 & 3 closed
JP3/D	Pins 1 & 2 closed
JP4/A-C	Pins 2 & 3 closed
JP4/D	Pins 2 & 3 closed
JP5/A-C	Pins 2 & 3 closed
JP6/A-C	Pins 2 & 3 closed
JP7/A-C	Pins 2 & 3 closed
JP8/A-C	Pins 2 & 3 closed
JP9/A-C	Pins 2 & 3 closed
JP10/A-C	Pins 2 & 3 closed
JP11/A-C	Pins 2 & 3 closed
JP12/A-C	Pins 2 & 3 closed
JP13/A-C	Pins 2 & 3 closed
JP14/A-C	Pins 2 & 3 closed
JP15/A-C	Pins 2 & 3 closed
JP16/A-C	Pins 2 & 3 closed

**MISCELLANEOUS TECHNICAL NOTE**  
Serial ports are located on an interface box which connects to the Hostess I/MC via the 100-pin interface connector.