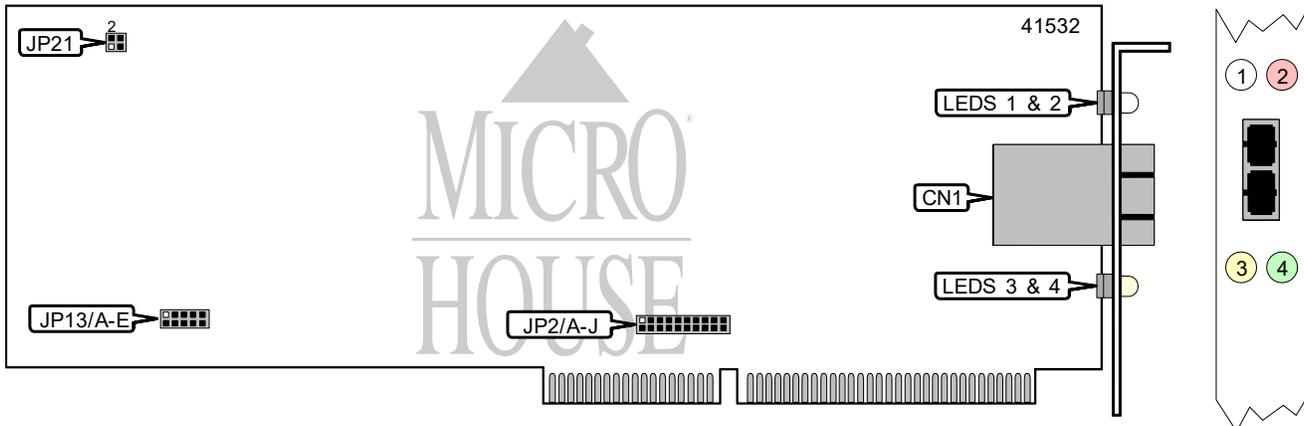


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CS-FDDI ISA-MMF-SAS

Card Type	Network Interface Card
NIC Type	FDDI
Maximum Onboard Memory	Unidentified
Boot ROM	Available
Network Transfer Rate	100Mbps
Topology	Ring
Wiring Type	Fiber optic cable
Data Bus	16-bit ISA



CONNECTIONS	
Function	Label
Fiber-optic connector via MIC	CN1

USER CONFIGURABLE SETTINGS			
Function	Label	Position	
í Boot ROM disabled	JP13/A	Open	
Boot ROM enabled	JP13/A	Closed	
í Factory configured - do not alter	JP13/E	Unidentified	
í Factory configured - do not alter	JP21/pins 1 & 2	Unidentified	
Hard configuration mode selected	JP21/pins 3 & 4	Open	
Soft configuration mode selected	JP21/pins 3 & 4	Closed	

BASE I/O ADDRESS SELECTION				
Address	JP13/B	JP13/C	JP13/D	
200h-21Fh	Closed	Closed	Closed	
220h-23Fh	Closed	Closed	Open	
240h-25Fh	Closed	Open	Closed	
280h-29Fh	Closed	Open	Open	
í 300h-31Fh	Open	Closed	Closed	
320h-33Fh	Open	Closed	Open	
340h-35Fh	Open	Open	Closed	

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INTERRUPT SELECTION										
IRQ	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E	JP2/F	JP2/G	JP2/H	JP2/I	JP2/J
3	Open	Closed								
4	Open	Closed	Open							
5	Open	Closed	Open	Open						
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
12	Open	Open	Closed	Open						
14	Open	Closed	Open							
15	Closed	Open								

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
LED1	Unidentified	On	See conditions in note below
LED1	Unidentified	Off	See conditions in note below
LED2	Red	On	Cable fault (SDDI only)
LED2	Red	Off	Cable active
LED3	Yellow	On	Data is being transmitted/received
LED3	Yellow	Off	Data is not being transmitted/received
LED4	Green	On	Ring connection is good
LED4	Green	Off	Ring connection is broken

TECHNICAL NOTE

Note: When the board is operated in soft configuration mode, jumpers JP13 and JP2 must be open. JP21/pins 1 & 2 function to toggle I/O address selection. If an I/O address collision is detected during installation, the shunt on JP21/pins 1 & 2 can be changed (while the PC is running) to force the software to try the next larger I/O address. For example, if a collision is detected using I/O address 300h, removing shunt on JP21/pins 1 & 2 will force the software to try I/O address 320h.