TIARA COMPUTER SYSTEMS, INC.
A R C N E T   U N I V E R S A L   L A N C A R D

NIC Type   ARCnet
Transfer Rate   2.5Mbps
Data Bus   16-bit ISA
Topology   Linear Bus, Star
Wiring Type   RG--58A/U 50ohm coaxial
                      Unshielded twisted pair
Boot ROM   Available
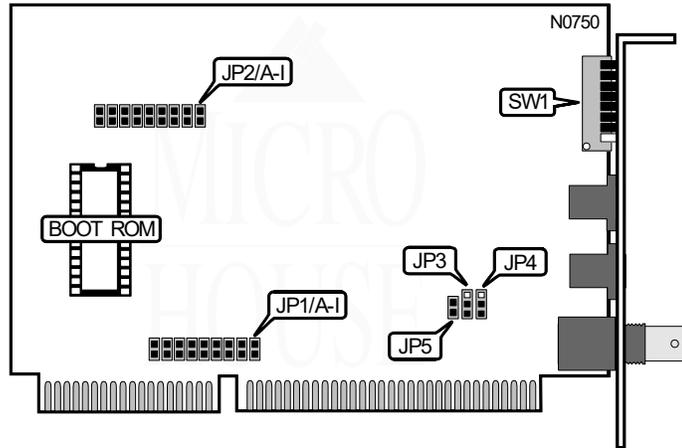
BOOT ROM	
Setting	JP2/I
í Disabled	Open
Enabled	Closed

CABLE TYPE		
Type	JP3	JP4
í Thin Ethernet	Pins 1 & 2 closed	Pins 1 & 2 closed
Unshielded twisted pair	Pins 2 & 3 closed	Pins 2 & 3 closed

I/O ADDRESS SELECT			
Address	JP2/A	JP2/B	JP2/C
í 2E0h	Closed	Open	Open
Note: All other address configurations are unspecified in manufacturer documentation. Alternate address settings should be silk-screened on the board itself.			

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INTERRUPT SELECT									
IRQ	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F	JP1/G	JP1/H	JP1/I
í IRQ2/9	Closed	Open	Open	Open	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Closed	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Open	Open	Open	Open	Closed

NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	On	Off	Off	Off	Off	Off	Off	Off
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	On	On	On	On	On	On	Off
255	On	On	On	On	On	On	On	On

Note: Node address 0 is used for messaging between nodes and must not be used.
  A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=1, 2=2, 3=4, 4=8, 5=16, 6=32, 7=64 8=128. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, Off=0)

RIM BUFFER ADDRESS					
Address	JP2/D	JP2/E	JP2/F	JP2/G	JP2/H
í D0000h	Closed	Open	Open	Closed	Closed

Note: All other address configurations are unspecified in manufacturer documentation. Alternate address settings should be silk-screened on the board itself.

TOPOLOGY	
Topology	JP5
í Star	Closed
Linear Bus	Open