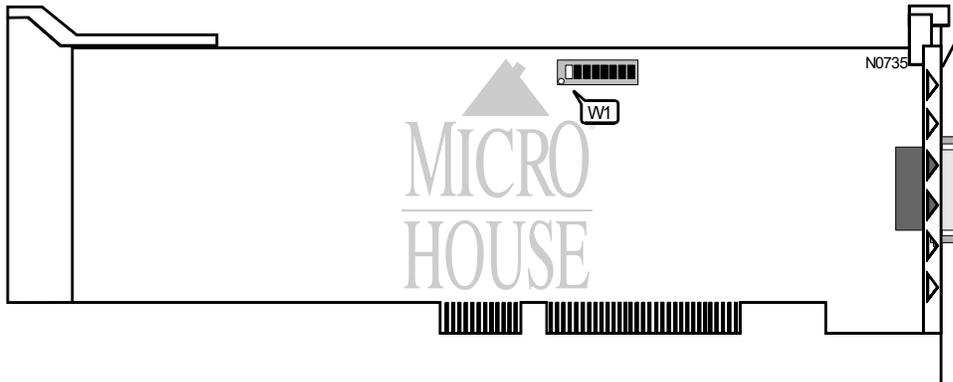


**SYSKONNECT, INC.**  
**SK-NET TR/MC 2**

**NIC Type**                      Token Ring  
**Transfer Rate**                4/16Mbps  
**Data Bus**                      MCA  
**Topology**                      Ring  
**Wiring Type**                  Shielded twisted pair via DB-9 port  
**Boot ROM**                      Available (Location unidentified)



BOOT ROM	
Setting	W1/2
Disabled	Off
Enabled	On

BASE MEMORY ADDRESS SELECT			
Address	\ '1/3	\ '1/4	\ '1/5
C000h	On	On	On
C400h	On	On	Off
C800h	On	Off	On
CC00h	On	Off	Off
D000h	Off	On	On
D400h	Off	On	Off
D800h	Off	Off	On
DC00h	Off	Off	Off

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I/O ADDRESS SELECT			
Address	\ '1/6	\ '1/7	\ '1/8
0A20h	Off	Off	Off
1A20h	Off	Off	On
0B20h	Off	On	Off
1B20h	Off	On	On
0980h	On	Off	Off
1980h	On	Off	On
0900h	On	On	Off
1900h	On	On	On

FACTORY CONFIGURED SETTINGS	
Switch	Setting
W1/1	On
Note: This setting should be altered only if the system's motherboard uses address lines SA17 to SA19.	