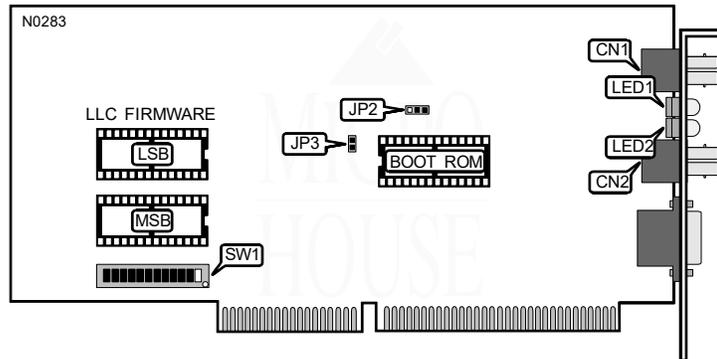


ZENITH DATA SYSTEMS
LAN 16 TRF - A T

NIC Type Token-Ring
Transfer Rate 4/16Mbps
Data Bus 16-bit ISA
Topology Ring
Wiring Type 50/62.5/100µ Fiber optic cable
Boot ROM Available



CONNECTIONS	
Purpose	Location
ST Receive Connector (gray)	CN1
ST Transmit Connector (black)	CN2

I/O BASE ADDRESS		
Address	SW1/1	SW1/2
A00 - A0Fh	On	On
A20 - A2Fh	Off	On
A40 - A4Fh	On	Off
A60 - A6Fh	Off	Off

INTERRUPT REQUEST		
IRQ	SW1/3	SW1/4
3	On	On
9	Off	On
10	On	Off
11	Off	Off

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ZENITH DATA SYSTEMS
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BOOT ROM ADDRESS		
Address	SW1/5	SW1/6
CC000 - CFFFFh	On	On
D0000 - D3FFFh	Off	On
D8000 - D8FFFh	Off	Off
DC000 - DFFFFh	On	Off

DMA CHANNEL		
Channel	SW1/7	SW1/8
DMA0	On	On
DMA5	Off	On
DMA6	On	Off
DMA7	Off	Off

BOOT ROM	
Setting	SW1/9
Disabled	On
Enabled	Off

NETWORK SPEED	
Speed	SW1/10
4Mbps	Off
16Mbps	On

Note: All cards on the network segment must have this option set the same.

LLC EPROM CONFIGURATION	
Setting	SW1/11
ROM-based LLC	On
RAM-based LLC	Off

Note: The LLC EPROMs contain the Logic Link Control program. With the LLC ROMs disabled, the LLC logic can be loaded into RAM. The card will function properly with the LLC chips disabled.

CABLE TYPE	
Type	SW1/12
50/62.5/100μ fiber optic cable	On
Shielded Twisted Pair (DB-9 Connector)	Off

ALTERNATE CONCENTRATOR ENABLE	
Setting	JP2
Default concentrator enabled (IEEE 802.5j)	Pins 2 & 3 Closed
Alternate concentrator enabled	Pins 1 & 2 Closed

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LAN16TRF-A T

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BOOT ROM MEMORY	
Setting	JP3
iBoot ROM memory space required	Closed
Boot ROM memory space not required	Open

DIAGNOSTIC LED(S)		
LED	Status	Condition
LED1	On	Card is properly connected to MAU
LED1	Off	Card is not properly connected to MAU
LED2	On	Card is inserted into the ring
LED2	Off	Card is not inserted into the ring