ZENITH DATA SYSTEMS
L A N 1 6 T R - X T

NIC Type	Token-Ring
Transfer Rate	4/16Mbps
Data Bus	16-bit ISA
Topology	Ring
Wiring Type	Unshielded twisted pair
	Shielded twisted pair
Boot ROM	Available
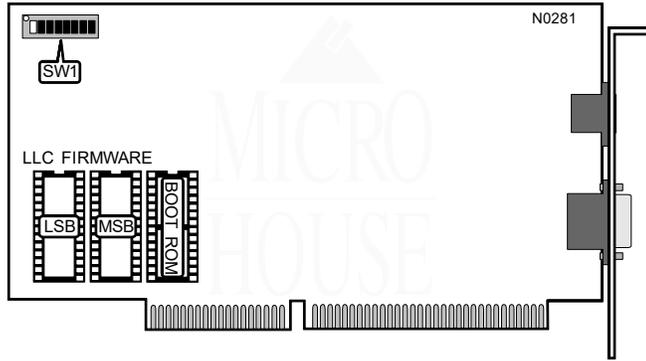
I/O BASE ADDRESS	
Address	SW1/1
A20 - A4Fh	On
A60 - A6Fh	Off

INTERRUPT REQUEST	
IRQ	SW1/2
2	Off
3	On

BOOT ROM ADDRESS	
Address	SW1/3
CC000 - CFFFFh	On
DC000 - DFFFFh	Off

BOOT ROM	
Setting	SW1/4
íDisabled	On
Enabled	Off

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LLC EPROM ENABLED	
Setting	SW1/5
íRAM-based LLC	On
ROM-based LLC	Off
Note: The LLC EPROMs contain the Logic Link Control program. With the LLC ROMs disabled, the LLC logic can be loaded into RAM. The card will function properly with the LLC chips disabled.	

NETWORK SEGMENT SPEED	
Speed	SW1/6
í16Mbps	On
4Mbps	Off
Note: All cards on the network segment must have this option set the same.	

CABLE TYPE	
Type	SW1/7
Unshielded twisted pair (RJ-45 Jack)	Off
Shielded twisted pair (DB-9 Connector)	On

FACTORY CONFIGURED SETTINGS	
Switch	Setting
SW1/8	Unused