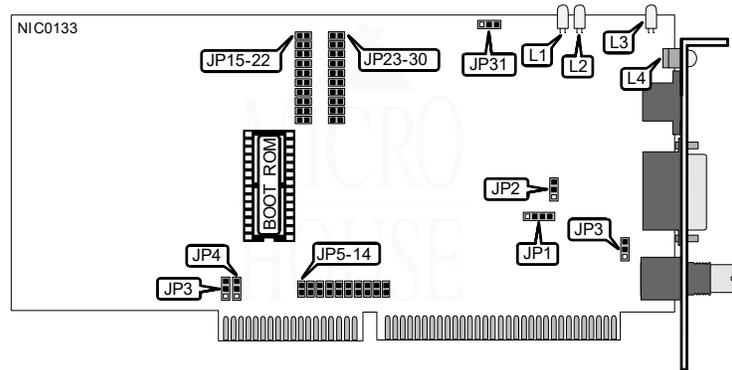


TIARA COMPUTER SYSTEMS, INC.

10BASE-T LanCard 2003

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Linear Bus
Wiring Type	RG-58A/U 50ohm coaxial Shielded twisted pair Unshielded twisted pair
Boot ROM	AUI transceiver via DB-15 port Available



BOOT ROM ADDRESS								
Address	JP23	JP24	JP25	JP26	JP27	JP28	JP29	JP30
Disabled	Closed	Open						
C400h	Open	Closed						
C800h	Open	Closed	Open	Open	Open	Open	Open	Open
CC00h	Open	Open	Closed	Open	Open	Open	Open	Open
D000h	Open	Open	Open	Closed	Open	Open	Open	Open
D400h	Open	Open	Open	Open	Closed	Open	Open	Open
D800h	Open	Open	Open	Open	Open	Closed	Open	Open
DC00h	Open	Open	Open	Open	Open	Open	Closed	Open

INTERRUPT REQUEST										
IRQ	JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13	JP14
i3	Open	Closed	Open							
2	Open	Closed								
4	Open	Closed	Open	Open						
5	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
7	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
9	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
10	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
11	Open	Open	Closed	Open						
12	Open	Closed	Open							
15	Closed	Open								

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10BASE-T LanCard 2003

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CABLE TYPE	
Cable	JP1
RG-58A/U 50ohm coaxial	Pins 1 & 2 Closed
AUI transceiver via DB-15 port	Pins 2 & 3 Closed
Shielded/Unshielded twisted pair	Pins 3 & 4 closed

I/O BASE ADDRESS								
Address	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
f300h	Closed	Open						
280h	Open	Open	Open	Open	Closed	Open	Open	Open
2A0h	Open	Open	Open	Open	Open	Closed	Open	Open
2C0h	Open	Open	Open	Open	Open	Open	Closed	Open
2E0h	Open	Closed						
320h	Open	Closed	Open	Open	Open	Open	Open	Open
340h	Open	Open	Closed	Open	Open	Open	Open	Open
360h	Open	Open	Open	Closed	Open	Open	Open	Open

COMPATIBILITY MODE		
Mode	JP3	JP4
Default	Pins 1 & 2 closed	Pins 1 & 2 closed
Special	Pins 2 & 3 closed	Pins 2 & 3 closed

Note: Use the special mode on the PS/2 Model 30-286 if the computer hangs after running IPX.

DB-15 PORT GROUND CONFIGURATION	
DB-15 Standard	JP2
AUI	Pins 1 & 2 closed
DIX	Pins 2 & 3 closed

Note: JP3 should remain with pins 1 & 2 Closed.

LINK INTEGRITY TEST	
Setting	JP31
Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

Note: Link Integrity is only used with 10Base-T wiring. When using other than 10BaseT, the test should be disabled.

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10BASE-T LanCard 2003

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LINK BEAT CONFIGURATION	
Setting	JP31
Link beat enabled (10Base-T default)	Pins 1 & 2 closed
Link beat disbaled (Non-10Base-T application)	Pins 2 & 3 closed

DIAGNOSTIC LED(S)		
LED	Status	Condition
L1	On	Collision is detected on network
L2	On	twisted pair link is good
L3	On	Power on
L4	On	Data is being transmitted/received