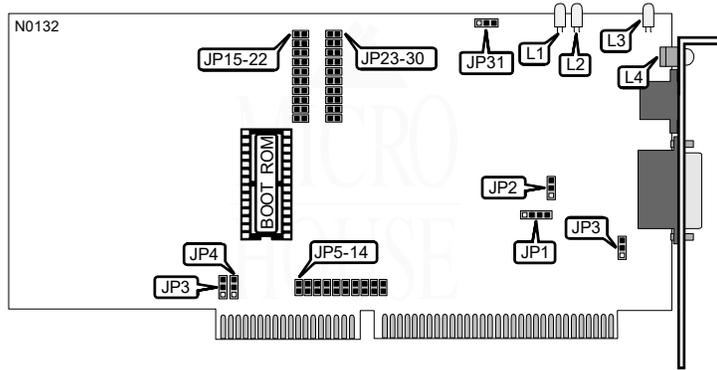


TIARA COMPUTER SYSTEMS, INC.
10BASE-T LanCard 2002

NIC Type Ethernet
Transfer Rate 10Mbps
Data Bus 16-bit ISA
Topology Linear Bus
Wiring Type Shielded twisted pair
 Unshielded twisted pair
 AUI transceiver via DB-15 port
Boot ROM Available



BOOT ROM ADDRESS								
Address	JP23	JP24	JP25	JP26	JP27	JP28	JP29	JP30
Disabled	Closed	Open						
C400h	Open	Closed						
C800h	Open	Closed	Open	Open	Open	Open	Open	Open
CC00h	Open	Open	Closed	Open	Open	Open	Open	Open
D000h	Open	Open	Open	Closed	Open	Open	Open	Open
D400h	Open	Open	Open	Open	Closed	Open	Open	Open
D800h	Open	Open	Open	Open	Open	Closed	Open	Open
DC00h	Open	Open	Open	Open	Open	Open	Closed	Open

INTERRUPT REQUEST										
IRQ	JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13	JP14
i3	Open	Closed	Open							
2	Open	Closed								
4	Open	Closed	Open	Open						
5	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
7	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
9	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
10	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
11	Open	Open	Closed	Open						
12	Open	Closed	Open							
15	Closed	Open								

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CABLE TYPE	
Type	JP1
Shielded/Unshielded twisted pair	Pins 3 & 4 closed
AUI transceiver via DB-15 port	Pins 2 & 3 Closed

I/O BASE ADDRESS								
Address	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
i300h	Closed	Open						
280h	Open	Open	Open	Open	Closed	Open	Open	Open
2A0h	Open	Open	Open	Open	Open	Closed	Open	Open
2C0h	Open	Open	Open	Open	Open	Open	Closed	Open
2E0h	Open	Closed						
320h	Open	Closed	Open	Open	Open	Open	Open	Open
340h	Open	Open	Closed	Open	Open	Open	Open	Open
360h	Open	Open	Open	Closed	Open	Open	Open	Open

COMPATIBILITY MODE		
Mode	JP3	JP4
Default	Pins 1 & 2 Closed	Pins 1 & 2 closed
Special	Pins 2 & 3 Closed	Pins 2 & 3 closed

Note: Use the special mode on the PS/2 Model 30-286 if the computer hangs after running IPX.

DB-15 PORT GROUND CONFIGURATION	
DB-15 Standard	JP2
AUI	Pins 1 & 2 closed
DIX	Pins 2 & 3 closed

Note: JP3 should remain with pins 1 & 2 closed.

LINK INTEGRITY TEST	
Setting	JP31
Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

Note: Link Integrity is only used with 10Base-T wiring. When using an AUI transceiver, the test should be disabled.

DIAGNOSTIC LED(S)		
LED	Status	Condition
L1	On	Collision detected on network
L1	Off	No collision detected on network
L2	On	Twisted pair network connection is good
L2	Off	Twisted pair network connection is broken
L3	On	Power on
L3	Off	Power off
L4	On	Data is being transmitted/received
L4	Off	Data is not being transmitted/received