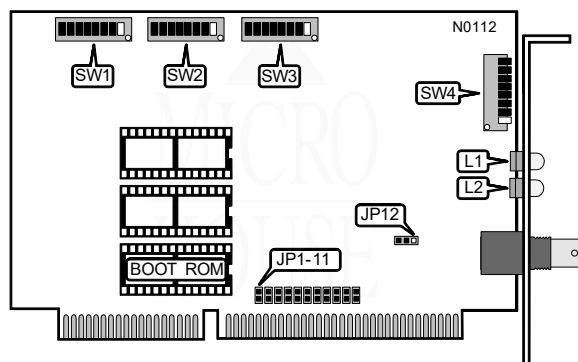


TIARA COMPUTER SYSTEMS, INC.

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NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 16-bit ISA
Topology Star
Wiring Type RG-62A/U 93ohm coaxial
Boot ROM Available



NODE ADDRESS								
Node	SW4/1	SW4/2	SW4/3	SW4/4	SW4/5	SW4/6	SW4/7	SW4/8
0	-	-	-	-	-	-	-	-
1	Off	Off	Off	Off	Off	Off	Off	On
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	On	On	On	On	On	On	Off
255	On	On	On	On	On	On	On	On

Note: Node address 0 is used for messaging between nodes and must not be used.
 A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 8=1, 7=2, 6=4, 5=8, 4=16, 3=32, 2=64, 1=128. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, Off=0)

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INTERRUPT REQUEST											
IRQ	JP1	JP2	JP3	JP4	JP5	JP6	JP7	JP8	JP9	JP10	JP11
i2	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed
4	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
5	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
6	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
7	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
11	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open
12	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
14	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open
15	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open

DIAGNOSTIC CONFIGURATION	
Setting	JP12
iEnhanced diagnostics disabled	Pins 1 & 2 Closed
Enhanced diagnostics enabled	Pins 2 & 3 Closed
Note: The enhanced diagnostics are compatible only with the driver that was shipped with this card. When the diagnostics are enabled, features such as an ARCnet duplicate Node ID search, are activated.	

BOOT ROM	
Setting	SW1/1
iDisabled	Off
Enabled	On

RIM BUFFER ADDRESS CONFIGURATION							
Buffer Address	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
xC000h	Off	On	On	Off	Off	Off	Off
xC400h	Off	On	On	Off	Off	Off	On
xC800h	Off	On	On	Off	Off	On	On
xD000h	Off	On	On	Off	On	Off	Off
xD400h	Off	On	On	Off	On	Off	On
xD800h	Off	On	On	Off	On	On	Off
xDC00h	Off	On	On	Off	On	On	On
xE000h	Off	On	On	On	Off	Off	Off

Notes: Place one digit from the next table in front of the address given above to get the complete Address.
Address xC000h must be used when the RIM buffer is set to use 128KB, rather than 16KB.
SW3/switch 4 should be set to On for 128KB, or OFF for 16KB.

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EXTENDED TIMEOUT CONFIGURATION				
Distance	Response Time	Reconfiguration	SW2/1	SW2/2
i4.8 miles	74.7 μ s	840ms	On	On
21.0 miles	283.4 μ s	1680ms	On	Off
42.5 miles	561.8 μ s	1680ms	Off	On
85.6 miles	1118.6 μ s	1680ms	Off	Off

Note: The distance given is the maximum distance between the two furthest nodes on the network.

I/O BASE ADDRESS						
Address	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
i2E0h	On	Off	On	On	On	Off
260h	On	Off	Off	On	On	Off
290h	On	Off	On	Off	Off	On
2B0h	On	Off	On	Off	On	On
300h	On	On	Off	Off	Off	Off
350h	On	On	Off	On	Off	On
380h	On	On	On	Off	Off	Off
3E0h	On	On	On	On	On	Off

RIM BUFFER EXTENDED ADDRESS CONFIGURATION							
Segment	SW3/1	SW3/2	SW3/3	SW3/5	SW3/6	SW3/7	SW3/8
i0xxx	On	Off	Off	Off	Off	Off	Off
1xxx	On	Off	Off	Off	Off	Off	On
2xxx	On	Off	Off	Off	Off	On	Off
3xxx	On	Off	Off	Off	Off	On	On
4xxx	On	Off	Off	Off	On	Off	Off
5xxx	On	Off	Off	Off	On	Off	On
6xxx	On	Off	Off	Off	On	On	Off
7xxx	On	Off	Off	Off	On	On	On
8xxx	On	Off	Off	On	Off	Off	Off
9xxx	On	Off	Off	On	Off	Off	On
Axxx	On	Off	Off	On	Off	On	Off
Bxxx	On	Off	Off	On	Off	On	On
Cxxx	On	Off	Off	On	On	Off	Off
Dxxx	On	Off	Off	On	On	Off	On
Exxx	On	Off	Off	On	On	On	Off
Fxxx	On	Off	Off	On	On	On	On

Note: Place the digit found in this table in front of the address given on the previous table to get the complete RIM buffer address. A setting other than zero in this table will place the buffer into extended memory at the aforementioned address. For example, a setting of 2 will place the buffer into the 2nd megabyte of extended memory, a setting of C will place the buffer into the 12th megabyte of extended memory. Starting from the base address, make sure that the next 16 to 128KB of memory (see above notes) are not used.

Important: The software drivers supplied with this card do not support extended memory.

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DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
L1	Red	On	Data is being received
L1	Red	Off	Data is not being received
L1	Red	Blinking	Card is reconfiguring
L2	Green	On	Data is being transmitted
L2	Green	Off	Data is not being transmitted
L2	Green	Blinking	Card is reconfiguring