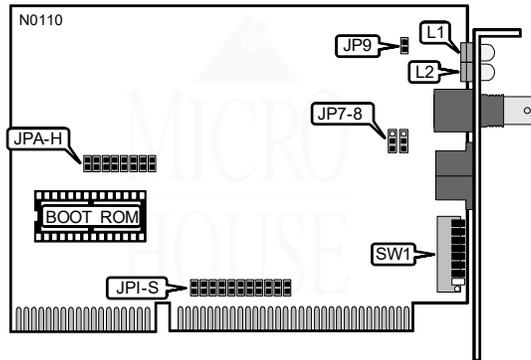


TIARA COMPUTER SYSTEMS, INC.  
ARCNET LanCard AT

<b>NIC Type</b>	ARCnet
<b>Transfer Rate</b>	2.5Mbps
<b>Data Bus</b>	16-bit ISA
<b>Topology</b>	Star
	Linear Bus
<b>Wiring Type</b>	Shielded twisted pair
	Unshielded twisted pair
	RG-58A/U 50ohm coaxial
<b>Boot ROM</b>	Available



NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	On						
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	Off						
255	On							

Note: Node address 0 is used for messaging between nodes and must not be used.  
A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 8=1, 7=2, 6=4, 5=8, 4=16, 3=32, 2=64, 1=128. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, Off=0)

CABLE TYPE	
Type	JP7 & JP8
RG-58A/U 50ohm coaxial	Pins 1 & 2 closed
Shielded/Unshielded twisted pair	Pins 2 & 3 closed

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TOPOLOGY CONFIGURATION	
Topology	JP9
iLinear Bus	Open
Star	Closed

RIM BUFFER ADDRESS/BOOT ROM ADDRESS CONFIGURATION						
Buffer	Boot ROM	JPA	JPB	JPC	JPD	JPE
CC00h	CE00h	Closed	Closed	Closed	Open	Closed
iD000h	D200h	Closed	Closed	Open	Open	Closed
D400h	D600h	Closed	Closed	Closed	Closed	Open
D800h	DA00h	Closed	Closed	Open	Closed	Open
DC00h	DE00h	Closed	Closed	Closed	Open	Open
E000h	E200h	Closed	Closed	Open	Open	Open

I/O BASE ADDRESS			
Address	JPF	JPG	JPH
i2E0h	Closed	Open	Closed
260h	Closed	Closed	Closed
290h	Open	Closed	Closed
2F0h	Open	Open	Closed
300h	Closed	Closed	Open
350h	Open	Closed	Open
380h	Closed	Open	Open
3E0h	Open	Open	Open

INTERRUPT REQUEST									
IRQ	JPI	JPJ	JKP	JPL	JPM	JPN	JPO	JPP	JPQ
i2/9	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Open	Open	Open	Open	Open	Closed	Open	Open	Open
4	Open	Open	Open	Open	Open	Open	Closed	Open	Open
5	Open	Closed	Open						
7	Open	Closed							
10	Open	Open	Open	Closed	Open	Open	Open	Open	Open
11	Open	Open	Closed	Open	Open	Open	Open	Open	Open
12	Open	Closed	Open						
15	Closed	Open							

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I/O OPTION CONFIGURATION			
I/O CHRDY	Zero Wait State	JPR	JPS
Disabled	Disabled	Open	Open
Disabled	Enabled	Closed	Open
Enabled	Disabled	Open	Closed

Note: Zero wait state operation and I/O CHRDY signal cannot be enabled at the same time.

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
L1	Red	On	Data is being received
L1	Red	Off	Data is not being received
L1	Red	Blinking	Card is reconfiguring
L2	Green	On	Data is being transmitted
L2	Green	Off	Data is not being transmitted
L2	Green	Blinking	Card is reconfiguring