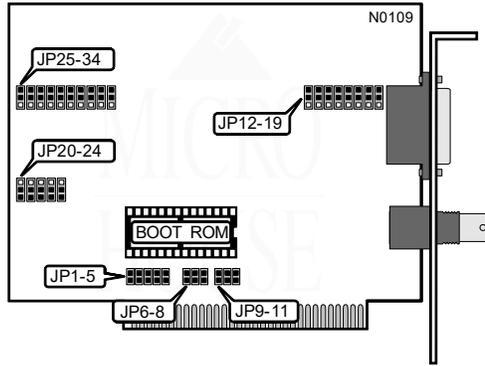


TIARA COMPUTER SYSTEMS, INC.

LanCard/E \* 1000

**NIC Type** Ethernet  
**Transfer Rate** 10Mbps  
**Data Bus** 8-bit ISA  
**Topology** Linear Bus  
**Wiring Type** RG-58A/U 50ohm coaxial  
 AUI transceiver via DB-15 port  
**Boot ROM** Available



INTERRUPT REQUEST					
IRQ	JP1	JP2	JP3	JP4	JP5
3	Open	Closed	Open	Open	Open
2	Closed	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed

DMA CHANNEL						
Channel	JP6	JP7	JP8	JP9	JP10	JP11
iDisabled	Open	Open	Closed	Open	Open	Closed
DMA1	Closed	Open	Open	Closed	Open	Open
DMA3	Open	Closed	Open	Open	Closed	Open

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LanCard/E \* 1000

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CABLE TYPE	
Type	JP12 - JP19
RG-58A/U 50ohm coaxial	Pins 2 & 3 closed
Shielded/Unshielded twisted pair	Pins 2 & 3 closed
AUI transceiver via DB-15 port	Pins 1 & 2 closed

I/O BASE ADDRESS					
Address	JP20	JP21	JP22	JP23	JP24
i300h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
000h	Pins 1 & 2				
020h	Pins 2 & 3	Pins 1 & 2			
040h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
060h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
080h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
0A0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
0C0h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
0E0h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
100h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
120h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
140h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
160h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
180h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
1A0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
1C0h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
1E0h	Pins 2 & 3	Pins 1 & 2			
200h	Pins 1 & 2	Pins 2 & 3			
220h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
240h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
260h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
280h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
2A0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
2C0h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
2E0h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
320h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
340h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
360h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
380h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
3A0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
3C0h	Pins 1 & 2	Pins 2 & 3			
3E0h	Pins 2 & 3				

Note: Pins designated should be in the closed position.

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BASE MEMORY ADDRESS - FIRST DIGIT				
Address Segment	JP25	JP26	JP27	JP28
0h	Pins 2 & 3 closed			
1h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
2h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
3h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
4h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
5h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
6h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
7h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
8h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
9h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
Ah	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
Bh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
Ch	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
Dh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
Eh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
Fh	Pins 1 & 2 closed			

Note: The Address Segment is the first digit in the Base Memory Address. Refer to the following table for the remaining three digits in the address.

BASE MEMORY ADDRESS - LAST THREE DIGITS		
Address	JP29	JP30
x000-x3FFh	Pins 2 & 3 closed	Pins 2 & 3 closed
x400-x7FFh	Pins 2 & 3 closed	Pins 1 & 2 closed
x800-xBFEh	Pins 1 & 2 closed	Pins 2 & 3 closed
xC00-xFFEh	Pins 1 & 2 closed	Pins 1 & 2 closed

Note: Place the three digit address given here behind the single digit given in the previous table to get the complete base memory address.

BOOT ROM	
Setting	JP31
iDisabled	Pins 2 & 3 closed
Enabled	Open

FACTORY CONFIGURED SETTINGS - DO NOT ALTER	
Jumper	Setting
JP32	Open
JP33	Open
JP34	Open