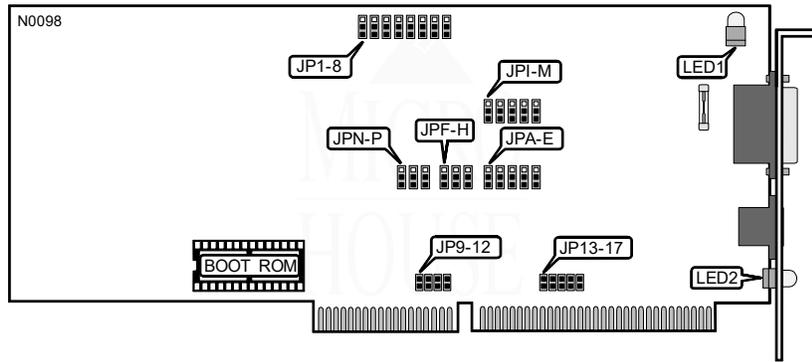


TIARA COMPUTER SYSTEMS, INC.
LanCard/E * AT LTP

NIC Type	Ethernet
Transfer Rate	10Mbps
Data Bus	16-bit ISA
Topology	Linear Bus
	Star
Wiring Type	Shielded twisted pair
	Unshielded twisted pair
	AUI transceiver via DB-15 port
Boot ROM	Available



CABLE TYPE	
Type	JP1 -JP8
Shielded/Unshielded twisted pair	Pins 1 & 2 closed
AUI transceiver via DB-15 port	Pins 2 & 3 closed

INTERRUPT REQUEST									
IRQ	JP9	JP10	JP11	JP12	JP13	JP14	JP15	JP16	JP17
2/9	Open	Open	Open	Open	Closed	Open	Open	Open	Open
i3	Open	Open	Open	Open	Open	Closed	Open	Open	Open
4	Open	Open	Open	Open	Open	Open	Closed	Open	Open
5	Open	Closed	Open						
7	Open	Closed							
10	Open	Open	Open	Closed	Open	Open	Open	Open	Open
11	Open	Open	Closed	Open	Open	Open	Open	Open	Open
12	Open	Closed	Open						
15	Closed	Open							

Continued on next page . . .

TIARA COMPUTER SYSTEMS, INC.
LanCard/E * AT LTP

... continued from previous page

I/O BASE ADDRESS					
Address	JPA	JPB	JPC	JPD	JPE
000h	Pins 2 & 3				
020h	Pins 1 & 2	Pins 2 & 3			
040h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
060h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
080h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0E0h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
100h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
120h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
140h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
160h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
180h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1E0h	Pins 1 & 2	Pins 2 & 3			
200h	Pins 2 & 3	Pins 1 & 2			
220h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
240h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
260h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
280h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2E0h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
i300h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
320h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
340h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
360h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
380h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
3A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2

Note: Pins designated should be in the closed position.

BOOT ROM SIZE			
Size	JPF	JPG	JPH
i2764 (8KB)	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
27128 (16KB)	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
27256 (32KB)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
27512 (64KB)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

Continued on next page ...

TIARA COMPUTER SYSTEMS, INC.
LanCard/E * AT LTP

... continued from previous page

BASE MEMORY ADDRESS					
Base Address	ROM Size	JPI	JPJ	JPK	JPL
C000h	8KB	Pins 2 & 3			
C200h	8KB	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
C400h	8KB	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
C600h	8KB	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
C800h	8KB	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
CA00h	8KB	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
CC00h	8KB	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
CE00h	8KB	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
D000h	8KB	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
D200h	8KB	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
D400h	8KB	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D600h	8KB	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D800h	8KB	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
DA00h	8KB	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
DC00h	8KB	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
DE00h	8KB	Pins 1 & 2			
C000h	16KB	N/A	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
C400h	16KB	N/A	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
C800h	16KB	N/A	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
CC00h	16KB	N/A	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
D000h	16KB	N/A	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
D400h	16KB	N/A	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
D800h	16KB	N/A	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
DC00h	16KB	N/A	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
C000h	32KB	N/A	N/A	Pins 2 & 3	Pins 2 & 3
C800h	32KB	N/A	N/A	Pins 1 & 2	Pins 2 & 3
D000h	32KB	N/A	N/A	Pins 2 & 3	Pins 1 & 2
D800h	32KB	N/A	N/A	Pins 1 & 2	Pins 1 & 2
C000h	64KB	N/A	N/A	N/A	Pins 2 & 3
D000h	64KB	N/A	N/A	N/A	Pins 1 & 2

Note: Pins designated should be in the closed position.

BOOT ROM	
Setting	JPM
Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

Note: Jumpers JPN, JPO, and JPP are reserved for future Boot ROM options, and Pins 1 & 2 should remain closed.

DIAGNOSTIC LED(S)		
LED	Status	Condition
LED1	On	Power on
LED1	Off	Power off
LED2	On	Network connection is good
LED2	Off	Network connecton is broken