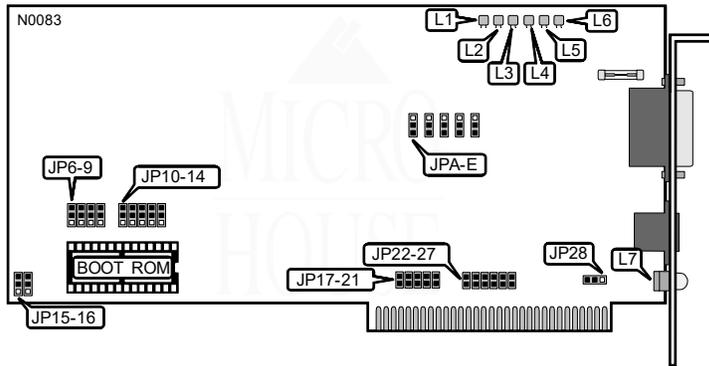


TIARA COMPUTER SYSTEMS, INC.
 LanCard/E * PC10TP

NIC Type Ethernet
Transfer Rate 10Mbps
Data Bus 8-bit ISA
Topology Linear bus
 Star
Wiring Type Shielded twisted pair
 Unshielded twisted pair
 AUI transceiver via DB-15 port
Boot ROM Available



BOOT ROM	
Setting	JP9
iDisabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

BASE MEMORY ADDRESS			
Address	JP6	JP7	JP8
iD0000h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
C0000h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
C4000h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
C8000h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
CC000h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
D4000h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
D8000h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
DC000h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

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LanCard/E*PC10TP

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I/O BASE ADDRESS					
Address	JP10	JP11	JP12	JP13	JP14
I300h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
000h	Pins 2 & 3				
020h	Pins 1 & 2	Pins 2 & 3			
040h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
060h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
080h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
0E0h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
100h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
120h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
140h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
160h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
180h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
1E0h	Pins 1 & 2	Pins 2 & 3			
200h	Pins 2 & 3	Pins 1 & 2			
220h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
240h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
260h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
280h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2C0h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
2E0h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
320h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
340h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
360h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
380h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
3A0h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
3C0h	Pins 2 & 3	Pins 1 & 2			
3E0h	Pins 1 & 2				

Note: Pins designated should be in the closed position.

INTERRUPT REQUEST					
IRQ	JP17	JP18	JP19	JP20	JP21
i3	Open	Closed	Open	Open	Open
2/9	Closed	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed

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DMA CHANNEL						
Setting	JP22	JP23	JP24	JP25	JP26	JP27
iDisabled	Closed	Open	Open	Closed	Open	Open
DMA1	Open	Closed	Open	Open	Closed	Open
DMA3	Open	Open	Closed	Open	Open	Closed

WAIT STATE	
Setting	JP28
iZero wait states disabled	Pins 2 & 3 closed
Zero wait states enabled	Pins 1 & 2 closed

CABLE TYPE	
Type	JPA - JPE
Shielded/Unshielded twisted pair	Pins 1 & 2 closed
AUI transceiver via DB-15 port	Pins 2 & 3 closed

FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP15	Pins 1 & 2 closed
JP16	Pins 1 & 2 closed

DIAGNOSTIC LED(S)		
LED	Status	Condition
L1	On	Data is being transmitted
L1	Off	Data is not being transmitted
L2	On	Collision detected on network
L2	Off	No collisions detected on network
L3	On	Card transmitter is disabled
L3	Off	Card transmitter is enabled
L4	N/A	Not used
L5	On	Data is being received
L5	On	Data is not being received
L6	On	Power on
L6	On	Power off or fuse is blown
L7	On	Twisted pair link is good
L7	On	Twisted pair link is broken