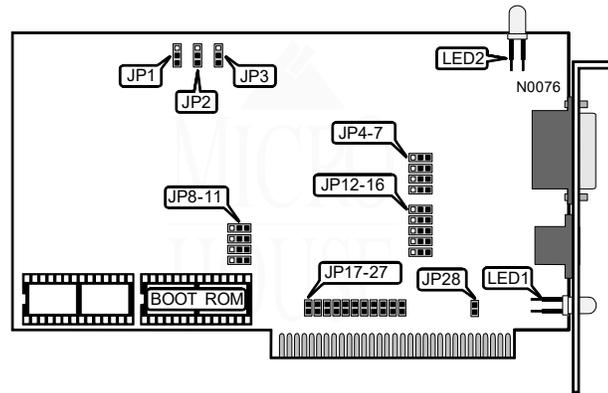


TIARA COMPUTER SYSTEMS, INC.  
LanCard/E\*PC8\*TP

<b>NIC Type</b>	Ethernet
<b>Transfer Rate</b>	10Mbps
<b>Data Bus</b>	8-bit ISA
<b>Topology</b>	Linear Bus
<b>Wiring Type</b>	Shielded/Unshielded twisted pair AUI transceiver via DB-15 port
<b>Boot ROM</b>	Available



CABLE TYPE			
Type	JP1	JP2	JP3
Shielded/Unshielded twisted pair	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
AUI transceiver via DB-15 port	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed

BASE MEMORY ADDRESS			
Address	JP4	JP5	JP7
C0000h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed
C4000h	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
C8000h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed
CC000h	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
D0000h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
D4000h	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
D8000h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
DC000h	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed

BOOT ROM	
Setting	JP6
Disabled	Pins 1 & 2 Closed
Enabled	Pins 2 & 3 Closed

Note: On JP10 and JP11, pins 1 & 2 should remain closed.

Continued on next page . . .

ON-BOARD DRAM CONFIGURATION	
Size	JP8 & JP9
8KB	Pins 2 & 3 Closed
32KB	Pins 1 & 2 Closed

I/O BASE ADDRESS					
Address	JP12	JP13	JP14	JP15	JP16
000h	Pins 1 & 2				
020h	Pins 1 & 2	Pins 2 & 3			
040h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
060h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
080h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
0A0h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
0C0h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
0E0h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
100h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
120h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
140h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
160h	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
180h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
1A0h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
1C0h	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
1E0h	Pins 1 & 2	Pins 2 & 3			
200h	Pins 2 & 3	Pins 1 & 2			
220h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
240h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
260h	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
280h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
2A0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
2C0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
2E0h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
300h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
320h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
340h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
360h	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
380h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
3A0h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
3C0h	Pins 2 & 3	Pins 1 & 2			
3E0h	Pins 2 & 3				

Note: Pins designated should be in the closed position.

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INTERRUPT REQUEST					
IRQ	JP17	JP18	JP19	JP20	JP21
2	Closed	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed

DMA CHANNEL						
Setting	JP22	JP23	JP24	JP25	JP26	JP27
Disabled	Closed	Open	Open	Closed	Open	Open
DMA1	Open	Closed	Open	Open	Closed	Open
DMA3	Open	Open	Closed	Open	Open	Closed

WAIT STATE	
Setting	JP28
Zero wait states disabled	Open
Zero wait states enabled	Closed

DIAGNOSTIC LED(S)		
LED	Status	Condition
LED1	On	Network connection is good
LED1	Off	Network connection is broken
LED2	On	Card is receiving power
LED2	Off	Card is not receiving power