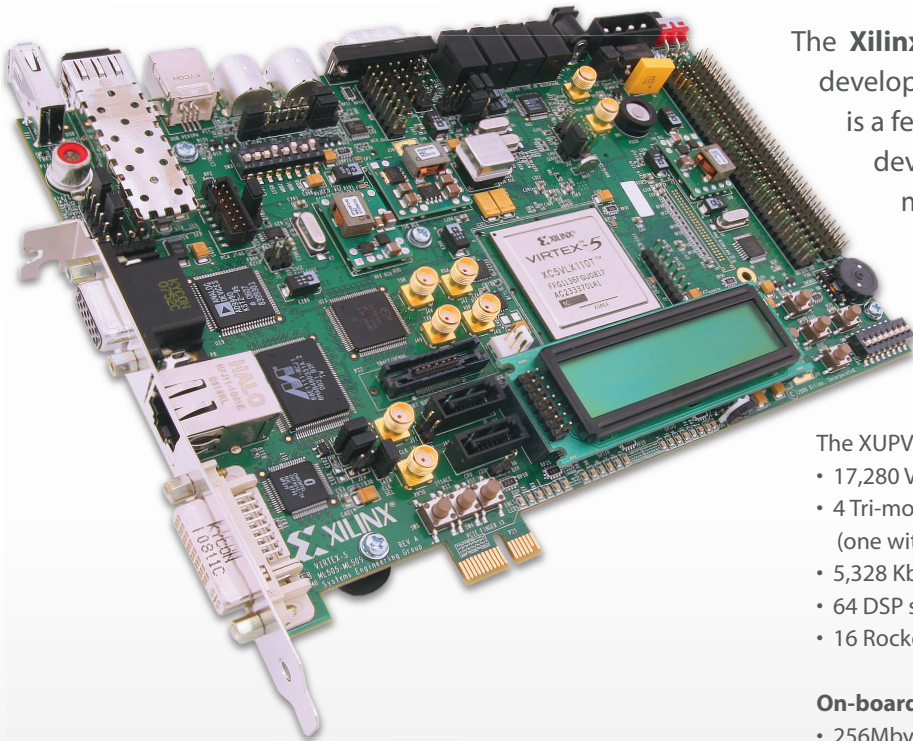


Xilinx XUPV5-LX110T Evaluation Platform

Bringing the Throughput of OpenSPARC™ Chip Multi-Threading to an FPGA



The **Xilinx XUPV5-LX110T** is a versatile general purpose development board powered by the Virtex®-5 FPGA. It is a feature-rich general purpose evaluation and development platform, includes on-board memory and industry standard connectivity interfaces, and delivers a versatile development platform for embedded applications.

The XUPV5-LX110T features the **Virtex-5 XC5VLX110T** device with:

- 17,280 Virtex-5 Logic Slices
- 4 Tri-mode Ethernet MACs (one with on-board Gbit PHY)
- 5,328 Kbits of Block RAM
- 64 DSP slices
- 16 Rocket I/O transceivers

On-board Memory

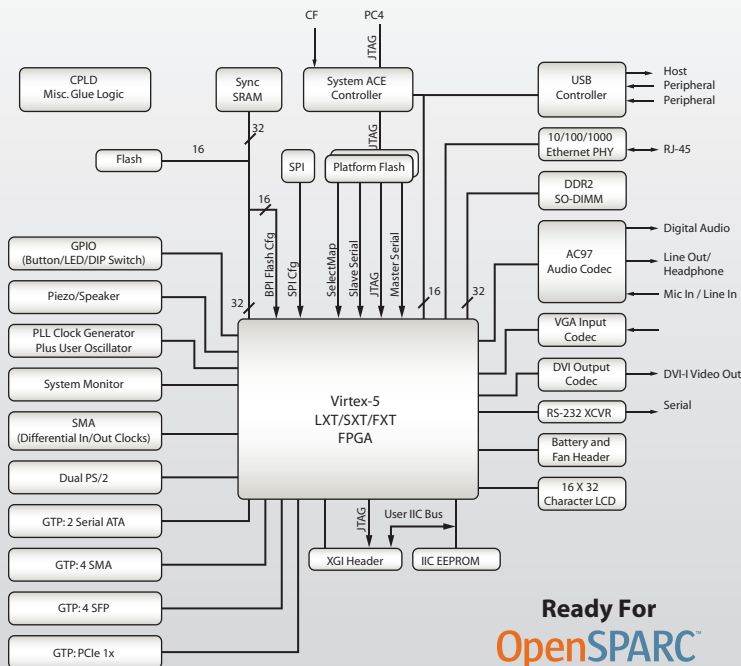
- 256Mbyte Double Data Rate SDRAM SODIMM
- 1Mbyte ZBT SRAM
- Removable 1Gbyte Compact Flash Card
- 32Mbyte Numonyx P30 StrataFlash
- Non-volatile Platform Flash

Configuration

- Platform USB/JTAG download
- Compact Flash

Peripherals

- 10/100/1000 Ethernet PHY (embedded MAC)
- USB2 Host and Peripheral Ports
- Stereo AC-97 Audio CODEC
- Integrated DVI/VGA Output and Video Input
- RS232 & PS/2 Ports
- Debug support includes Mictor Trace, BDM, and SoftTouch ports
- GTP Clock Synthesis Chips
- 16 Character x 2 Line Display
- Rocket I/O Transceivers Connectors
 - 2 Serial ATA Connector for use with Aurora Protocol
 - SMA (Rx and Tx Differential Pairs)
 - Small Form Factor Pluggable (SFP) Connector
 - SGMII
 - PCI Express (PCIe) Edge Connector (x1 End Point)



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