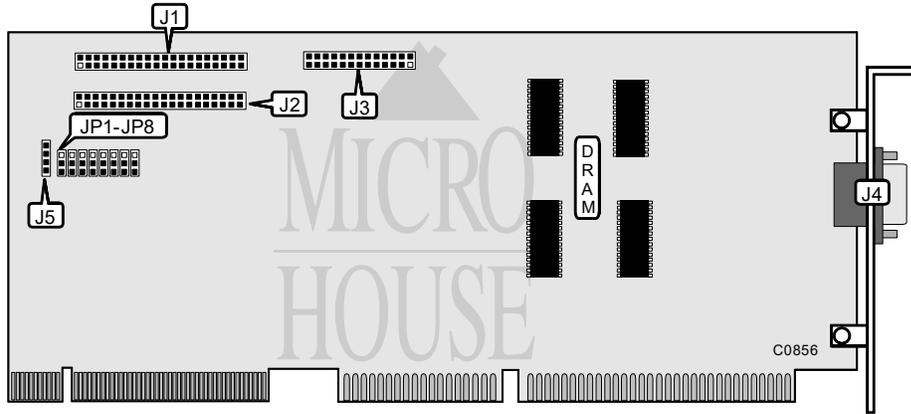


CHAINTECH COMPUTER COMPANY, LTD. CL-9026

Q4/95

Data bus: 32-bit VL-bus
 Size: Three-quarter length, full-height card
 Hard drive supported: Four IDE(AT) drives
 Floppy drives supported: None



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) connector	J1
40-pin secondary IDE(AT) connector	J2
VESA feature connection	J3
15-pin VGA connector	J4
4-pin connector - drive active LED	J5

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
System clock is >33MHz	JP2/pins 2 & 3	closed
System clock is <=33MHz	JP2/pins 1 & 2	closed
IOCHRDY signal for primary IDE disabled	JP5/pins 2 & 3	closed
IOCHRDY signal for primary IDE enabled	JP5/pins 1 & 2	closed
IOCHRDY signal for secondary IDE disabled	JP6/pins 2 & 3	closed
IOCHRDY signal for secondary IDE enabled	JP6/pins 1 & 2	closed
IRQ 9 disabled	JP8/pins 2 & 3	closed
IRQ 9 enabled	JP8/pins 1 & 2	closed

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IDE CONFIGURATION			
Primary IDE (J1)	Secondary IDE (J2)	JP1	JP7
Disabled	Disabled	pins 1 & 2 closed	pins 2 & 3 closed
Enabled	Disabled	pins 2 & 3 closed	pins 1 & 2 closed
Enabled	Enabled	pins 2 & 3 closed	pins 2 & 3 closed

IDE DEVICE TIMING MODE SELECTION		
Mode	JP3	JP4
0 (600ns. low speed device)	pins 2 & 3 closed	pins 2 & 3 closed
0+ (500ns. normal speed device)	pins 2 & 3 closed	pins 1 & 2 closed
1 (383ns. fast speed device)	pins 1 & 2 closed	pins 2 & 3 closed
2 (240ns. turbo speed device)	pins 1 & 2 closed	pins 1 & 2 closed

MISCELLANEOUS TECHNICAL NOTES
The amount of Video DRAM is unidentified.