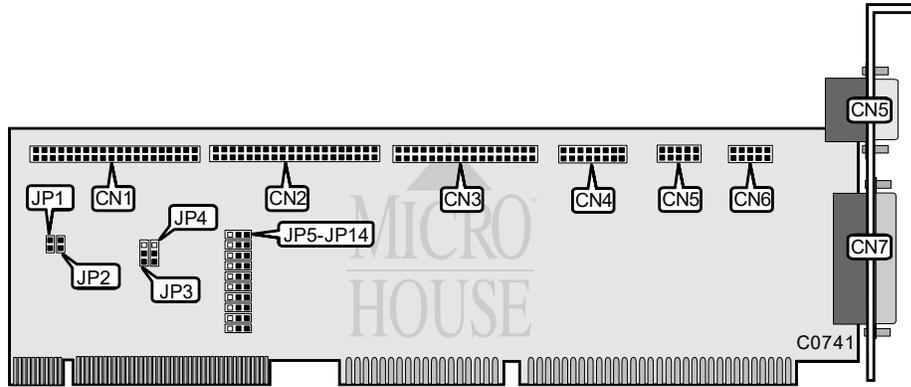


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CA8972

Q3/95

Data bus: 32-bit, VL-Bus  
 Size: Three quarter-length, half-height card  
 Hard drive supported: Two IDE (AT) interface drives  
 Floppy drives supported: 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) interface	CN1
40-pin secondary IDE(AT) interface	CN2
34-pin control cable connector - floppy drive	CN3
16-pin game port	CN4
10-pin serial port 1 - internal & 9-pin serial port 1 - external	CN5
10-pin serial port 2 - internal	CN6
25-pin parallel port	CN7
2-pin drive active LED for primary IDE	JP1
2-pin drive active LED for secondary IDE	JP2

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
Primary IDE interface enabled	JP5	pins 1 & 2 closed
Primary IDE interface disabled	JP5	pins 2 & 3 closed
Secondary IDE interface enabled	JP6	pins 1 & 2 closed
Secondary IDE interface disabled	JP6	pins 2 & 3 closed
Floppy drive interface enabled	JP8	pins 1 & 2 closed
Floppy drive interface disabled	JP8	pins 2 & 3 closed
Game port enabled	JP14	pins 1 & 2 closed
Game port disabled	JP14	pins 2 & 3 closed

PARALLEL PORT CONFIGURATION		
LPT	JP7	JP13
LPT2	pins 1 & 2 closed	pins 1 & 2 closed
LPT3	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	n/a

SERIAL PORT 1 CONFIGURATION		
COM	JP9	JP10
COM1	pins 1 & 2 closed	pins 1 & 2 closed
COM3	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	n/a

SERIAL PORT 2 CONFIGURATION		
COM	JP11	JP12
COM2	pins 1 & 2 closed	pins 1 & 2 closed
COM4	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	n/a

HDD SPEED CONFIGURATION		
CPU Clock Speed	JP3	JP4
> 50MHz	pins 1 & 2 closed	pins 1 & 2 closed
40-50 MHz	pins 1 & 3 closed	pins 1 & 3 closed
20-33 MHz	pins 1 & 3 closed	pins 1 & 2 closed
< 20MHz	pins 1 & 2 closed	pins 1 & 3 closed
Note: The HDD speed is configured according to the CPU clock.		