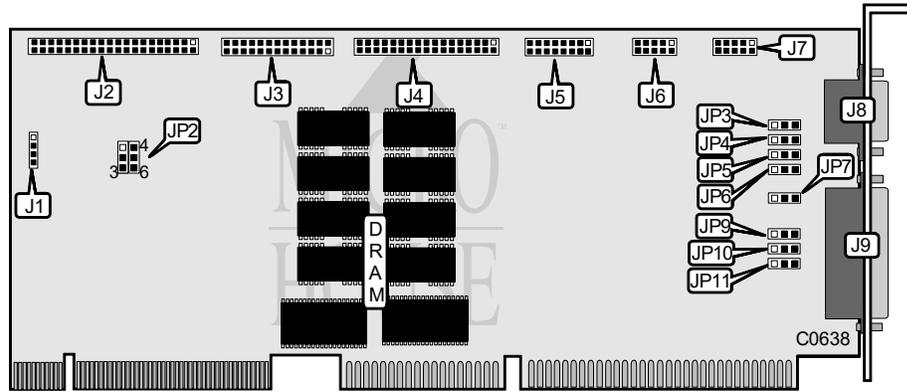


TMC RESEARCH CORPORATION

CP326, CP328 (VER. 1.0)

Q3/95

Data bus: 32-bit, VL-Bus
Size: Three/quarter-length, full-height card
Hard drive supported: Two IDE (AT) interface drives
Floppy drives supported: Two 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS	
Function	Location
4-pin connector - drive active LED	J1
40-pin IDE(AT) interface connector	J2
26-pin feature connector	J3
34-pin control cable connector - floppy drive	J4
16-pin game port - internal	J5
10-pin serial port 2 - internal	J6
10-pin serial port 1 - internal	J7
15-pin VGA connector - external	J8
25-pin parallel port - external	J9

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TMC RESEARCH CORPORATION

CP326, CP328 (VER. 1.0)

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
IDE speed is 0 (lowest)	JP2/pins 1 & 2, 4 & 5	closed
IDE speed is 1 (average)	JP2/pins 2 & 3, 4 & 5	closed
IDE speed is 2 (highest)	JP2/pins 2 & 3, 5 & 6	closed
Parallel port enabled	JP7/pins 1 & 2	closed
Parallel port disabled	JP7/pins 2 & 3	closed
Floppy controller enabled	JP9/pins 1 & 2	closed
Floppy controller disabled	JP9/pins 2 & 3	closed
Parallel port is LPT2	JP10/pins 1 & 2	closed
Parallel port is LPT3	JP10/pins 2 & 3	closed
Game port enabled	JP11/ pins 1 & 2	closed
Game port disabled	JP11/ pins 2 & 3	closed

SERIAL PORT 1 CONFIGURATION		
COM	JP5	JP6
COM1	pins 1 & 2 closed	pins 1 & 2 closed
COM3	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	N/A	pins 2 & 3 closed

SERIAL PORT 2 CONFIGURATION		
COM	JP3	JP4
COM2	pins 1 & 2 closed	pins 1 & 2 closed
COM4	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	N/A	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0/1	Bank 0/1
2MB	(8) 256K x 4	(2) 512K x 8
Note: The orientation of the memory banks is unidentified.		