

SWITCH SETTINGS

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+-----+
|1|2|3|4|5|6|7|8|   SW1:  ON = 1, OFF = 0
+-----+

| | | | | | | | | +--- Split Memory Option +-----+
| | | | | | | | | 1   Enabled           | On-board memory may be | |
| | | | | | | | | 0   Disabled*          | disabled by setting SW1-1 |
| | | | | | | | |   |                       | through SW1-4 ON and SW1-5 |
| | | | | | | | |   |                       | through SW1-8 OFF.       |
+-----+

| | | | | | | | | +----- On-board Memory
| | | | | | | | | 0 0 0   64K
| | | | | | | | | 1 0 0   128K
| | | | | | | | | 0 1 0   192K-256K
| | | | | | | | | 1 1 0   320K-384K
| | | | | | | | | 0 0 1   448K
| | | | | | | | | 1 0 1   512K - Used only with split memory
| | | | | | | | | 0 1 1   576K - Used with/without split memory
| | | | | | | | | 1 1 1   640K-704K - Used only with split memory
| | | | | | | | |
+-----+

Starting Memory Address
0 0 0 0   64K
1 0 0 0   128K - With split memory enabled
0 1 0 0   192K
1 1 0 0   256K - With split memory enabled
0 0 1 0   320K
1 0 1 0   384K
0 1 1 0   448K
1 1 1 0   512K
0 0 0 1   576K
1 0 0 1   640K

+-----+
|1|2|3|4|   SW2:  1=ON, 0=OFF
+-----+

| | | | | +--- Calendar/Clock
| | | | | 1   Disabled
| | | | | 0   Enabled

| | | | | +--- Serial Port
| | | | | 1   COM1 - 3F8H & Jumper E23-E25 for IRQ4
| | | | | 0   COM2 - 2F8H & Jumper E25-E27 for IRQ3

| | | | | +--- Calendar/Clock Address
| | | | | 1   Primary (350H-35FH & 750H-75FH)
| | | | | 0   Secondary (250H-25FH & 650H-65FH)

+--- Parallel Printer Port
1   LPT1 - 378H & Jumper E21-E22 for IRQ7
0   LPT2 - 278H & Jumper E21-E22 for IRQ7 or E22-E24 for IRQ5
    
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JUMPER SETTINGS

Device	Description	Configuration
Serial Port	RS-232-C interface	E29-E30*
	TTL interface	E29-E30 OUT
	+5V connected to pin 9	E32-E33
	Ring Indicator to pin 9	E31-E32*
Parallel Port	Centronics interface	E17-E20*
	Dataproducts interface	E17-E14
	Unidirectional port control	E15-E18*
	Bidirectional port control	E12-E15
	External directional control	E12-E15 & E13-E16
	Internal directional control	E12-E15 & E16-E19

Memory	Zero wait state enabled	E1-E2*
	Zero wait state disabled	E2-E3
Memory Parity	Enabled	E4-E5*
	Disabled	E5-E6

#### SPLIT MEMORY CONFIGURATION

The SB384L supports a split memory addressing option. When this option is selected, it enables an extra 128K of memory in an area normally reserved for system use from 832K-960K or D000H-EFFFH. Split memory can be enabled only if the starting address on the board is 128K or 256K. Also, this memory must be accessed directly; it is not available to the operating system. The table below shows the four memory configurations supported with split memory. One of them (576K) is a standard configuration and may be used with or without the split memory option.

Bank #	512K	576K	640K	704K
0	256K	256K	256K	256K
1	64K	64K	256K	256K
2	64K	64K	EMPTY	64K
3	EMPTY	64K	EMPTY	EMPTY
4	64K	64K	64K	64K
5	64K	64K	64K	64K

#### Split Memory Configuration

#### EXTERNAL EQUIPMENT CONNECTIONS

DB25 FEMALE Parallel Printer Port connector  
 DB9 MALE Serial Port connector  
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