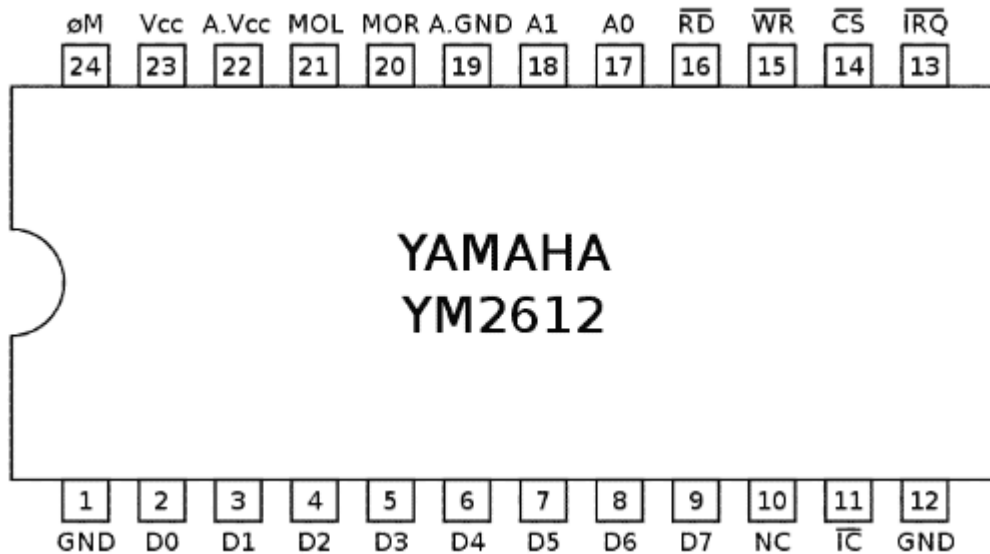


Yamaha YM2612 (OPN) Sound Chip



No.	Pin Name	I/O	Function					
1	GND	—	Ground pin.					
2	D ₀	I/O	8-bit bidirectional data bus. Communicates data with the processor.					
3	D ₁							
4	D ₂							
5	D ₃							
6	D ₄							
7	D ₅							
8	D ₆							
9	D ₇							
10	TEST	I/O	Pin to test this LSI. Do not connect.					
11	IC	I	Initializes the internal register.					
12	GND	—	Ground pin.					
13	IRQ	O	Interrupt signal issued from the two timers. When the time programmed into the timer has elapsed, this goes low. Output with open drain.					
14	CS	I	Control the D0 – D7 data bus.					
			CS	RD	WR	A1	A0	Details
0	1		0	0	0	Writes register addresses of timers, etc.		
						Writes register addresses of channels 1-3.		
0	1		0	0	1	Writes register data of timers, etc.		
						Writes register data of channels 1-3.		
15	WR		0	1	0	1	0	Writes register addresses of channels 4-6.
16	RD		0	1	0	1	1	Writes register data of channels 4-6.
17	A ₀		0	0	1	0	0	Reads statuses.
18	A ₁		1	X	X	X	X	D0 – D7 are set to high-impedance.
19	A GND	—	Ground pin.					
20	MOR	O	Two-channel analog outputs. These are output with a source follower.					
21	MOL							
22	A V _{CC}	—	+5V power supply pins.					
23								
24	φ M V _{CC}	I	Master clock input.					