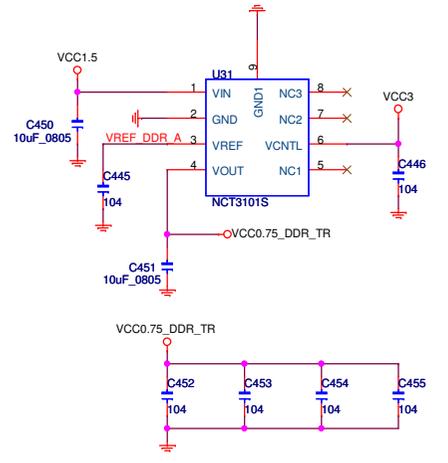
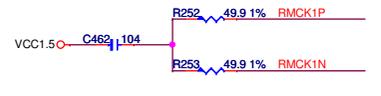
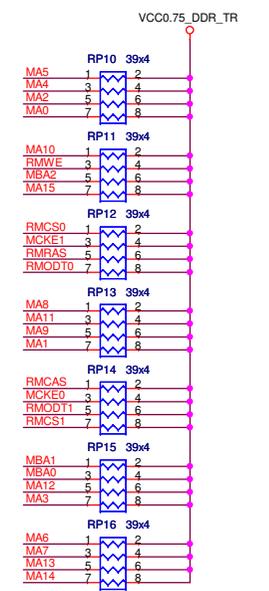
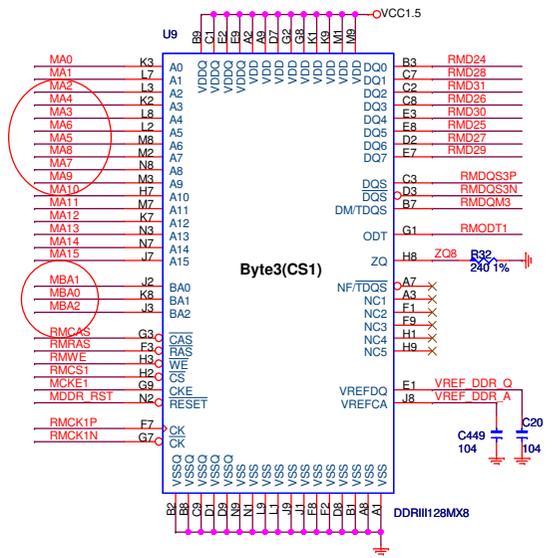
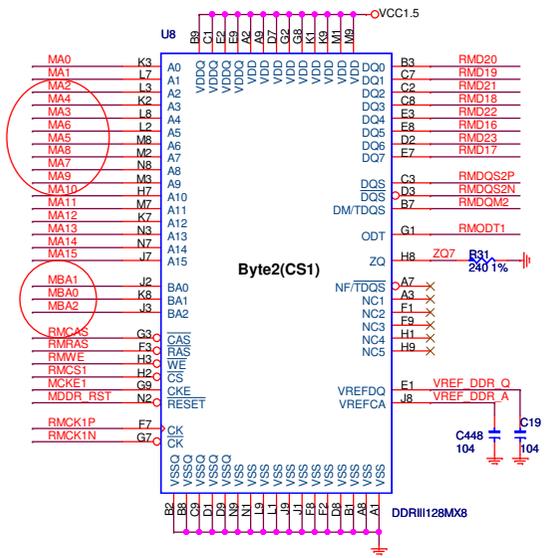
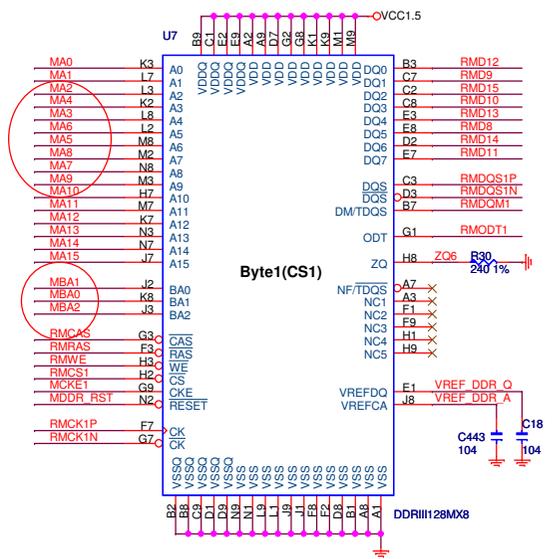
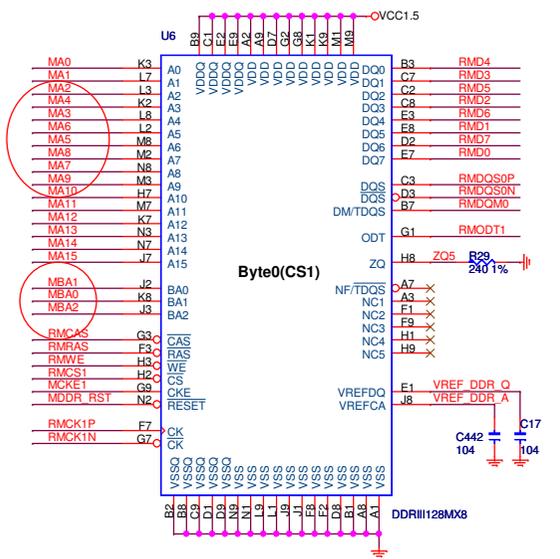
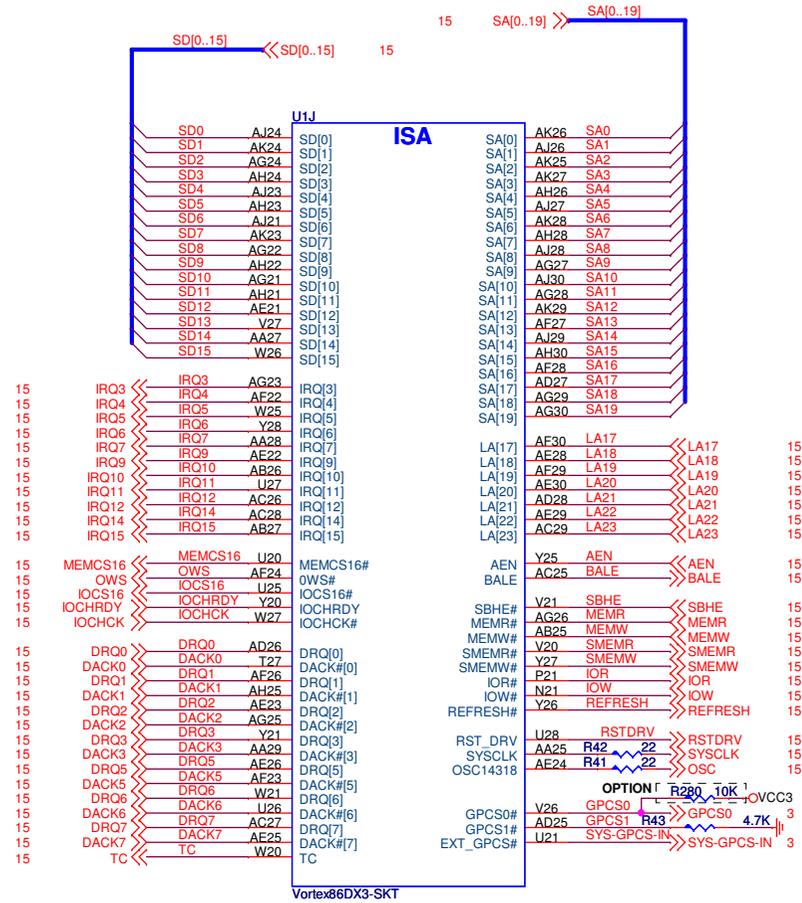


- MBA[0..2] << MBA[0..2] 1
- MA[0..15] << MA[0..15] 1
- RMD[0..31] << RMD[0..31] 1
- MCKE1 << MCKE1 1
- MDDR_RST << MDDR_RST 1
- RMODT1 << RMODT1 1
- RMDQS0N << RMDQS0N 1
- RMDQS0P << RMDQS0P 1
- RMDQS1N << RMDQS1N 1
- RMDQS1P << RMDQS1P 1
- RMDQS2N << RMDQS2N 1
- RMDQS2P << RMDQS2P 1
- RMDQS3N << RMDQS3N 1
- RMDQS3P << RMDQS3P 1
- RMCAS << RMCAS 1
- RMRAS << RMRAS 1
- RMWE << RMWE 1
- RMCS1 << RMCS1 1
- RMDQM0 << RMDQM0 1
- RMDQM1 << RMDQM1 1
- RMDQM2 << RMDQM2 1
- RMDQM3 << RMDQM3 1
- VREF_DDR_Q << VREF_DDR_Q 1
- VREF_DDR_A << VREF_DDR_A 1
- RMCS0 << RMCS0 1
- RMODT0 << RMODT0 1
- MCKE0 << MCKE0 1
- RMCK1P << RMCK1P 1
- RMCK1N << RMCK1N 1



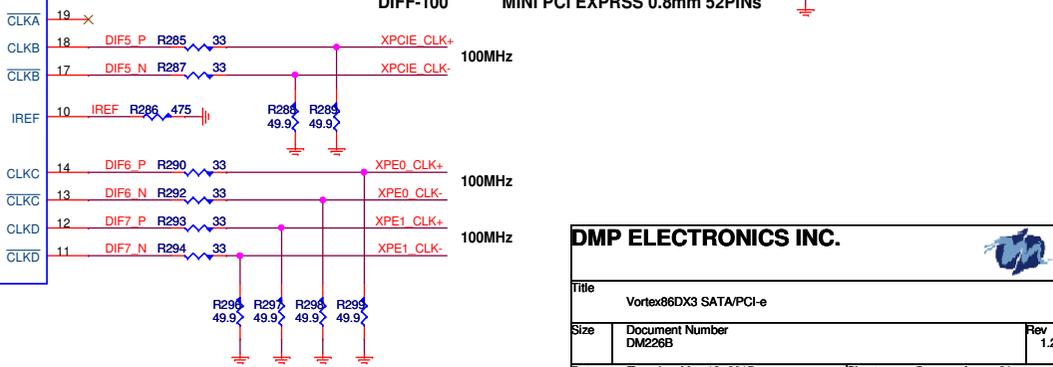
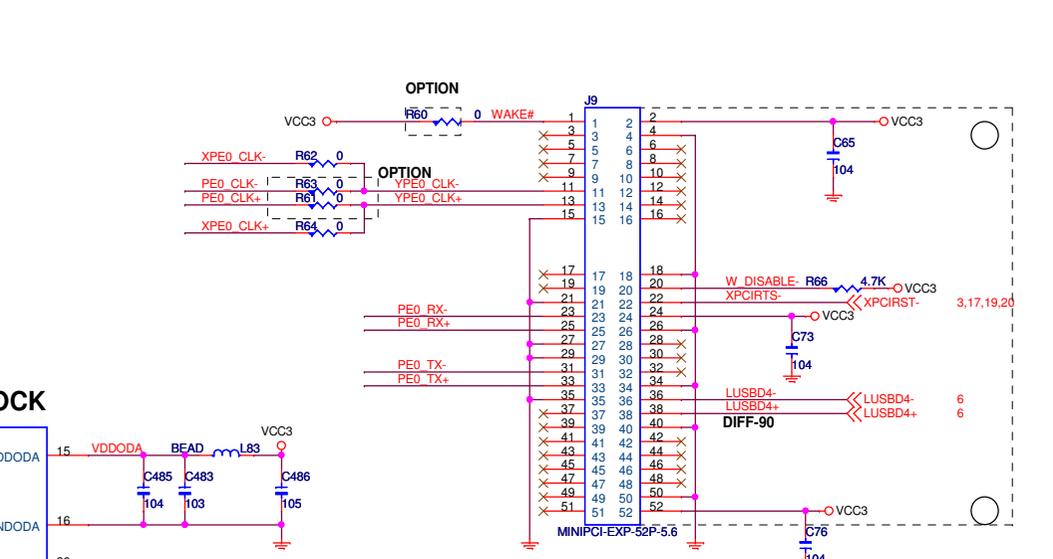
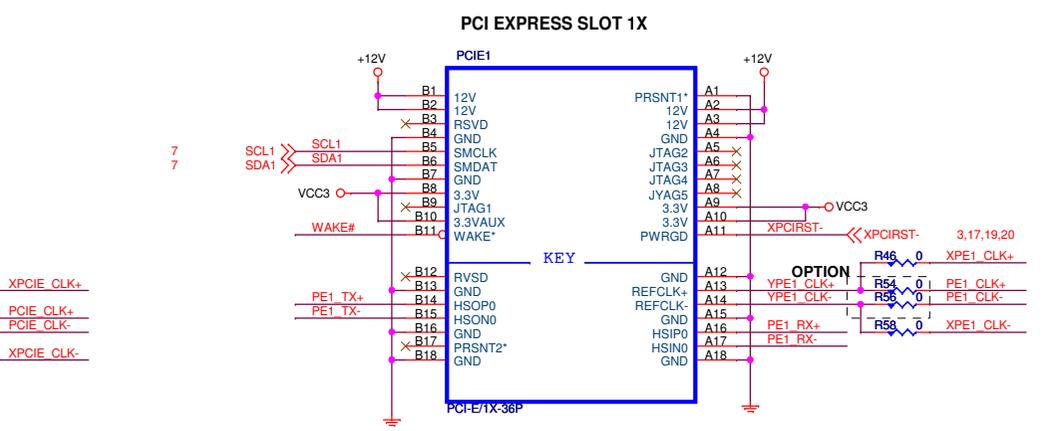
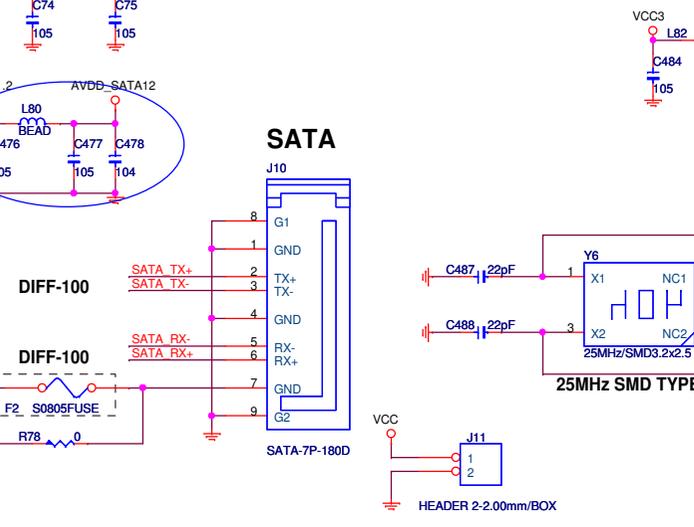
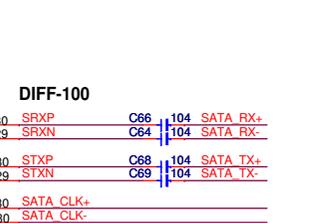
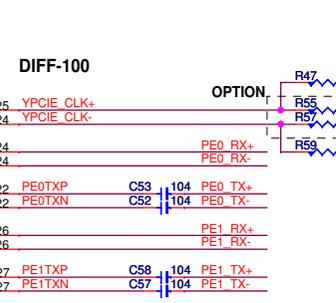
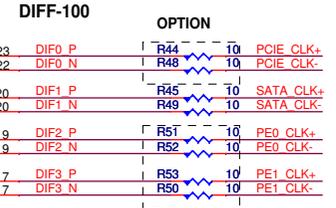
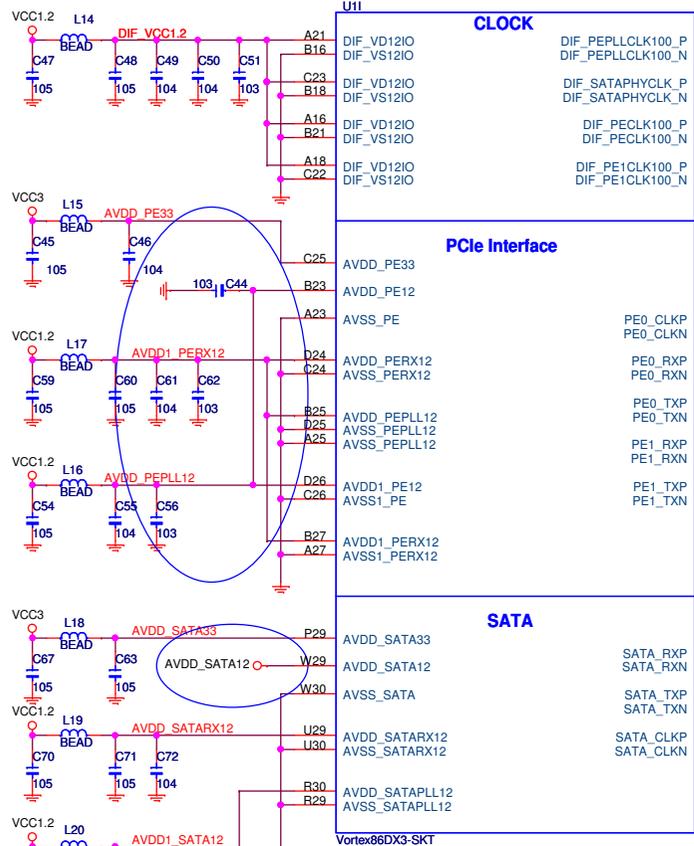
ISA BUS



DMP ELECTRONICS INC.



Title		
Vortex86DX3 ISA BUS		
Size	Document Number	Rev
DM226B		1.2
Date:	Tuesday, May 12, 2015	Sheet 4 of 21

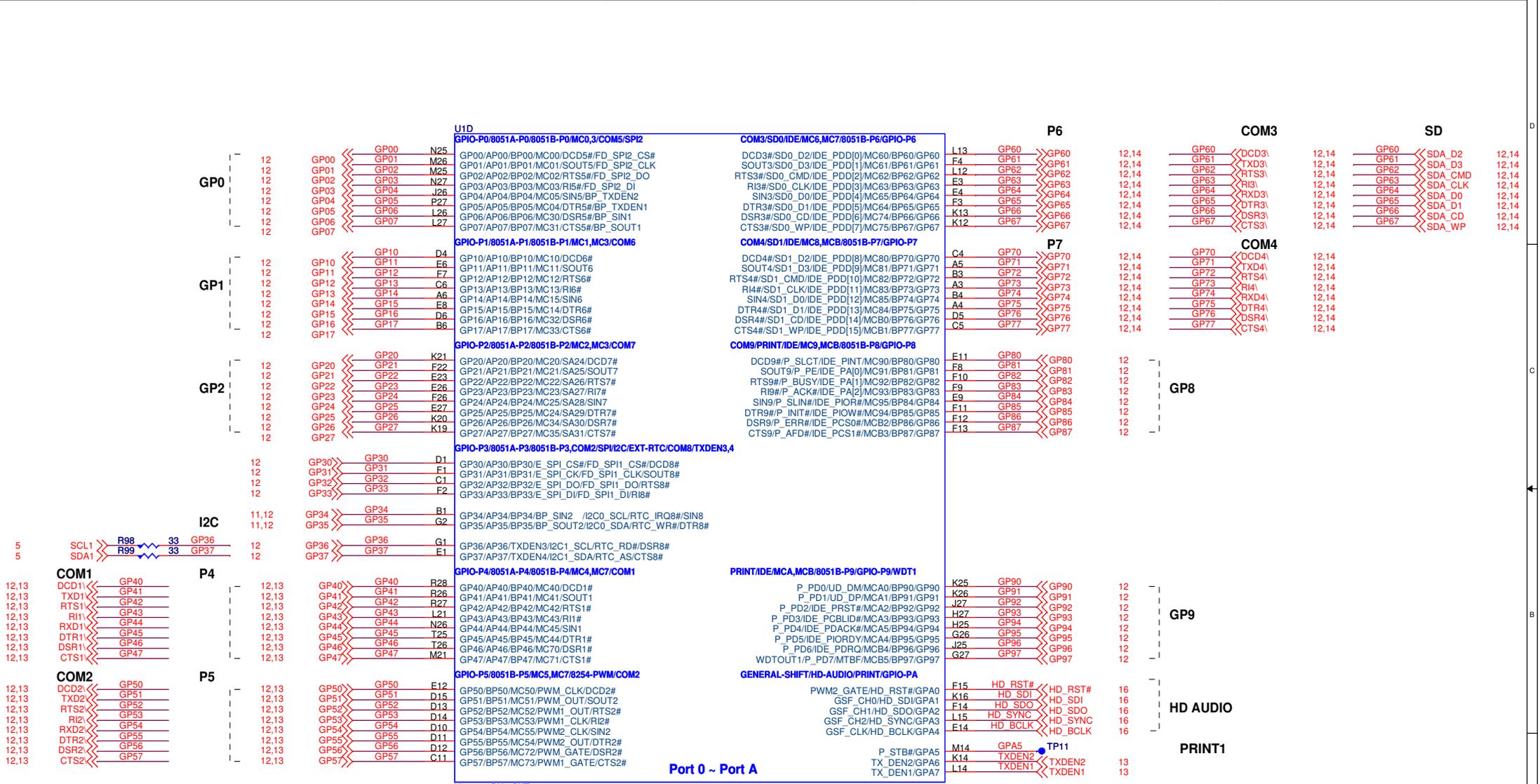


DMP ELECTRONICS INC.

Title: Vortex86DX3 SATA/PCI-e

Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 5 of 21



Vortex86DX3-SKT

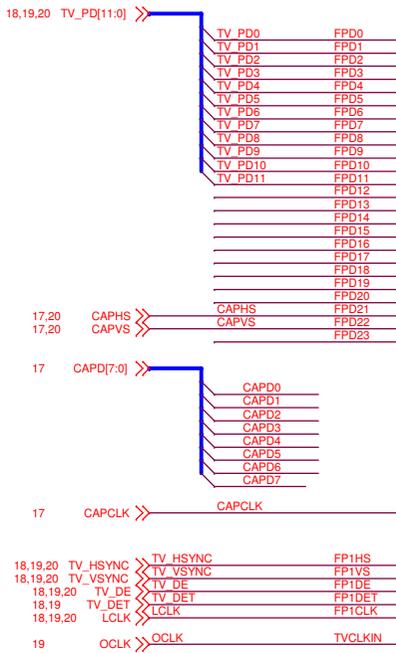
DMP ELECTRONICS INC.

Title: Vortex86DX3 GPIO/COM/PWM

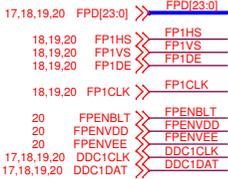
Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 7 of 21

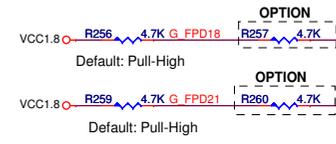
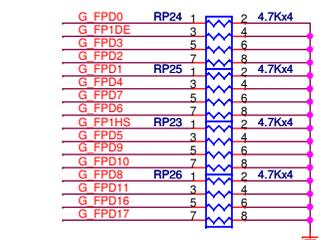
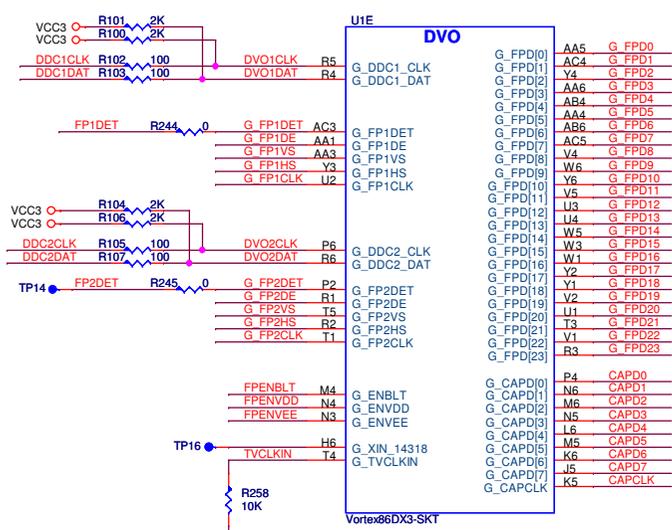
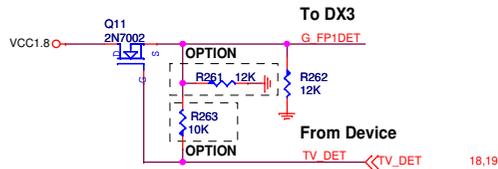
DVO/TV-IN/TV-OUT



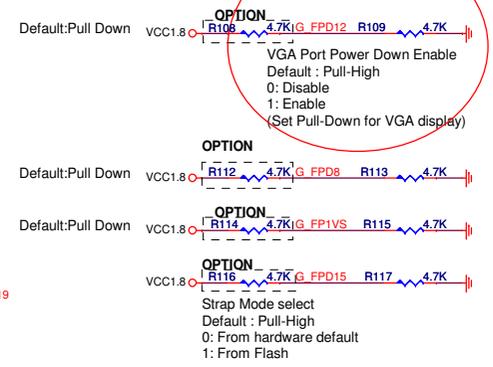
LCD 1



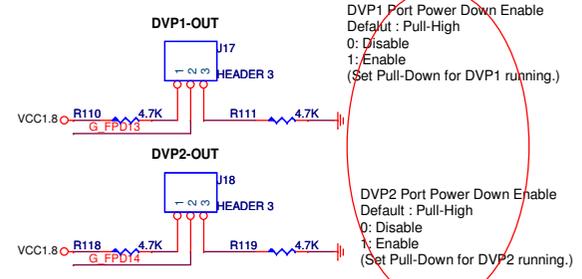
LCD 2



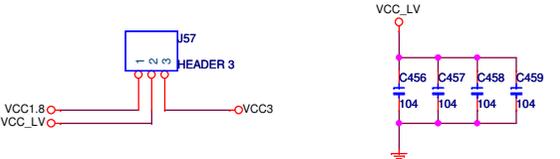
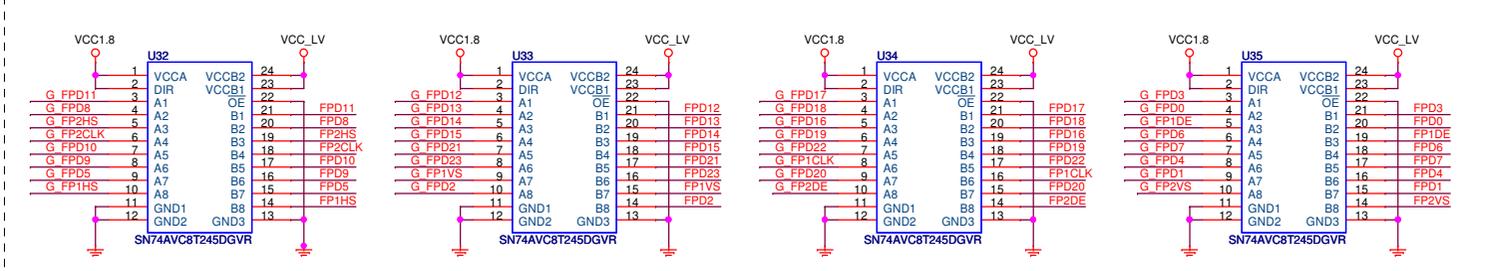
DISPLAY STRAP SETUP:



DISPLAY STRAP SETUP:



LEVEL SHIFT

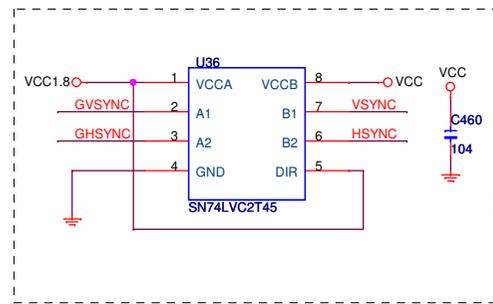
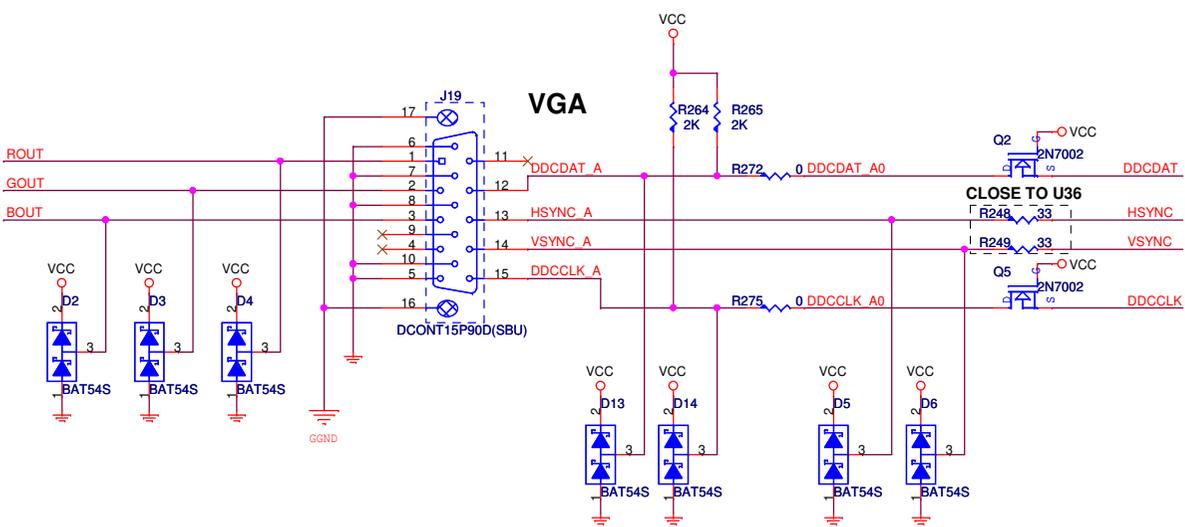
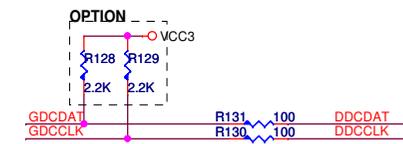
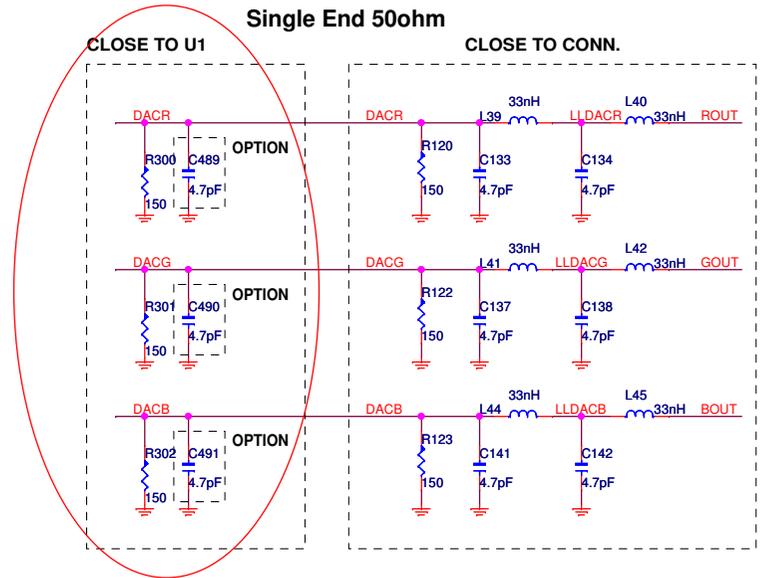
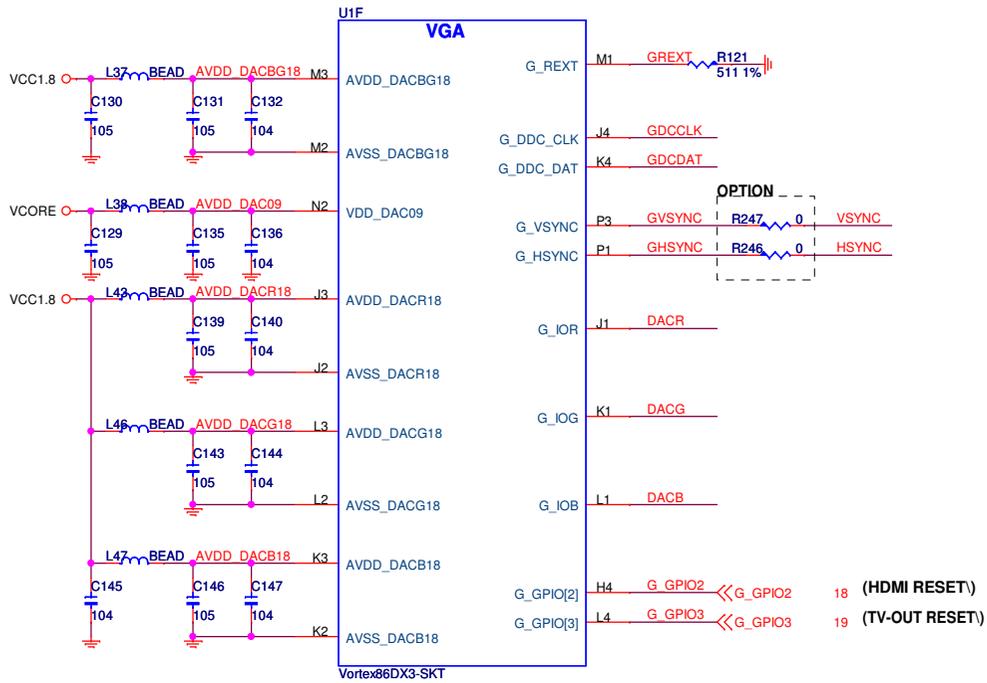


DMP ELECTRONICS INC.

Title: Vortex86DX3 DVO

Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 8 of 21

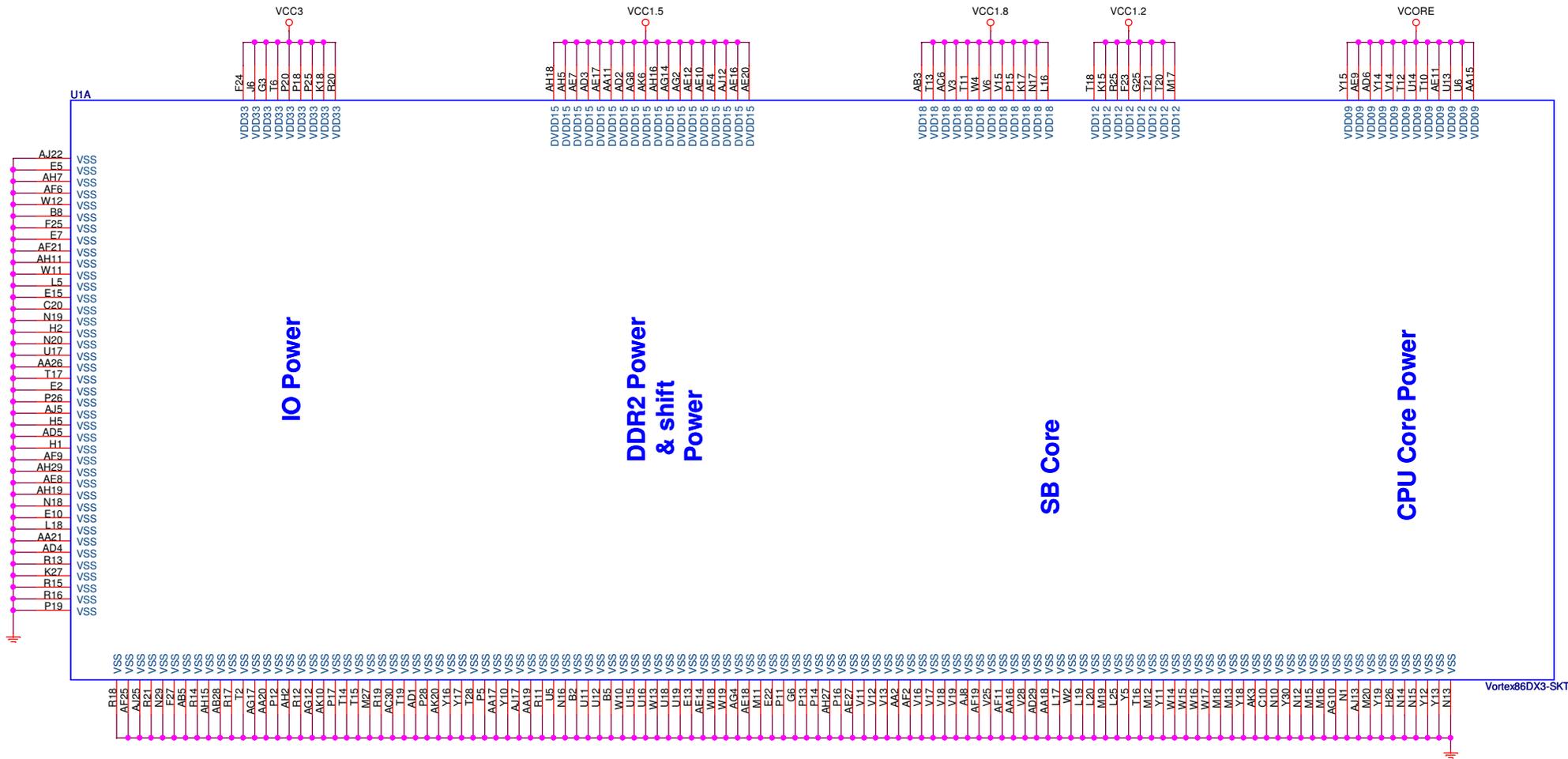


DMP ELECTRONICS INC.

Title: Vortex86DX3 VGA/ PLL

Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 9 of 21



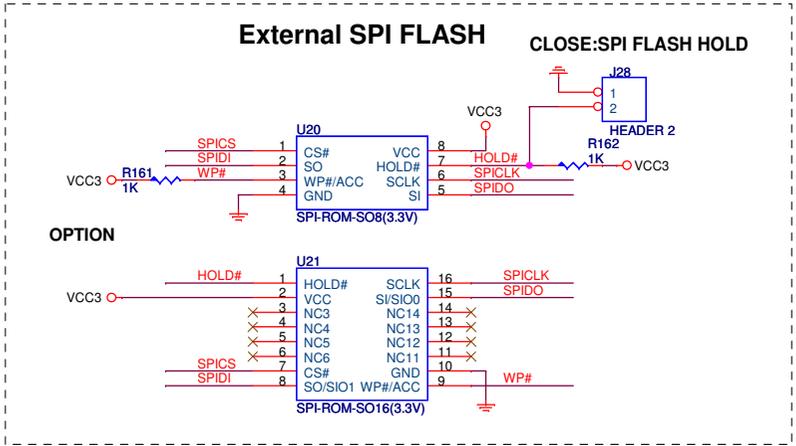
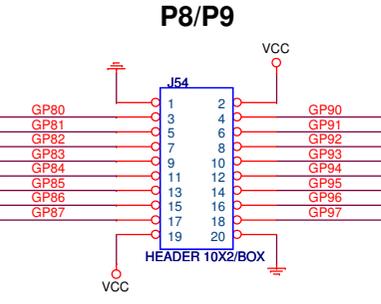
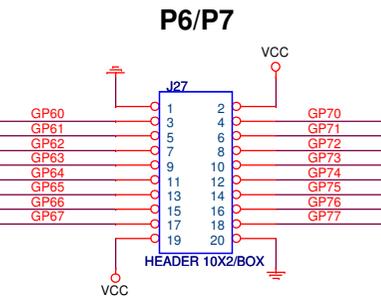
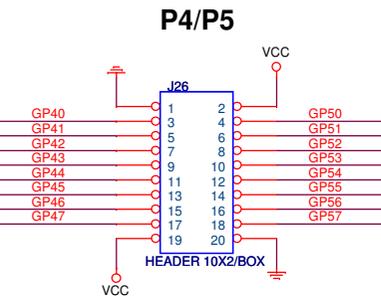
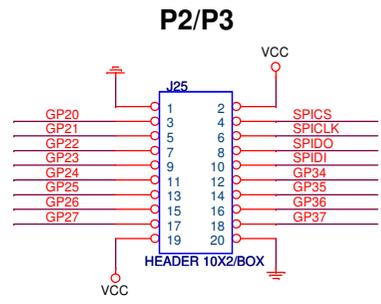
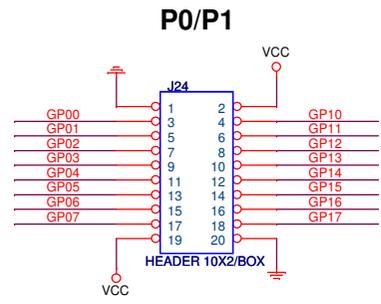
DMP ELECTRONICS INC.

Title: Vortex86DX3 POWER

Size	Document Number DM226B	Rev 1.2
------	---------------------------	------------

Date: Tuesday, May 12, 2015 Sheet 10 of 21

GP0	7	GP00	GP00
	7	GP01	GP01
	7	GP02	GP02
	7	GP03	GP03
	7	GP04	GP04
	7	GP05	GP05
	7	GP06	GP06
GP1	7	GP07	GP07
	7	GP10	GP10
	7	GP11	GP11
	7	GP12	GP12
	7	GP13	GP13
	7	GP14	GP14
	7	GP15	GP15
GP2	7	GP16	GP16
	7	GP17	GP17
	7	GP20	GP20
	7	GP21	GP21
	7	GP22	GP22
	7	GP23	GP23
	7	GP24	GP24
GP3	7	GP25	GP25
	7	GP26	GP26
	7	GP27	GP27
	7	GP30	SPICS
	7	GP31	SPICLK
	7	GP32	SPID0
	7	GP33	SPIDI
GP4	7,11	GP34	GP34
	7,11	GP35	GP35
	7	GP36	GP36
	7	GP37	GP37
	7,13	GP40	GP40
	7,13	GP41	GP41
	7,13	GP42	GP42
GP5	7,13	GP43	GP43
	7,13	GP44	GP44
	7,13	GP45	GP45
	7,13	GP46	GP46
	7,13	GP47	GP47
	7,13	GP50	GP50
	7,13	GP51	GP51
GP6	7,13	GP52	GP52
	7,13	GP53	GP53
	7,13	GP54	GP54
	7,13	GP55	GP55
	7,13	GP56	GP56
	7,13	GP57	GP57
	GP7	7,14	GP60
7,14		GP61	GP61
7,14		GP62	GP62
7,14		GP63	GP63
7,14		GP64	GP64
7,14		GP65	GP65
7,14		GP66	GP66
GP8	7,14	GP67	GP67
	7,14	GP70	GP70
	7,14	GP71	GP71
	7,14	GP72	GP72
	7,14	GP73	GP73
	7,14	GP74	GP74
	7,14	GP75	GP75
GP9	7,14	GP76	GP76
	7,14	GP77	GP77
	7	GP80	GP80
	7	GP81	GP81
	7	GP82	GP82
	7	GP83	GP83
	7	GP84	GP84
GP9	7	GP85	GP85
	7	GP86	GP86
	7	GP87	GP87
	7	GP90	GP90
	7	GP91	GP91
	7	GP92	GP92
	7	GP93	GP93
GP9	7	GP94	GP94
	7	GP95	GP95
	7	GP96	GP96
	7	GP97	GP97



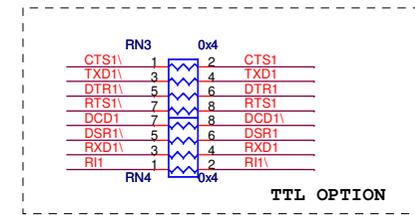
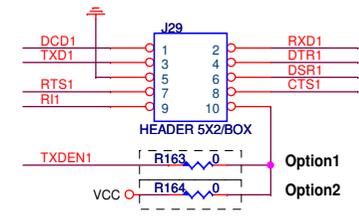
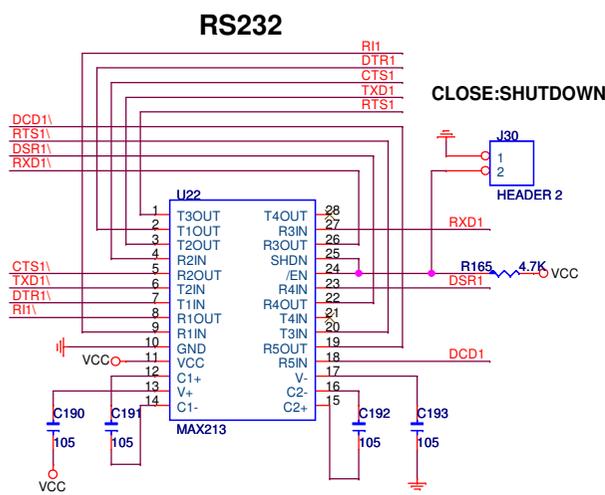
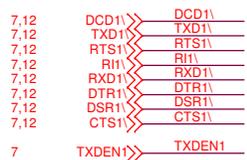
DMP ELECTRONICS INC.

Title: GPIO P0-P7

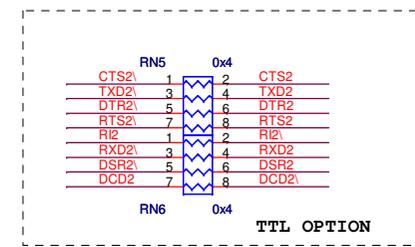
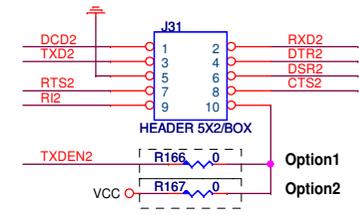
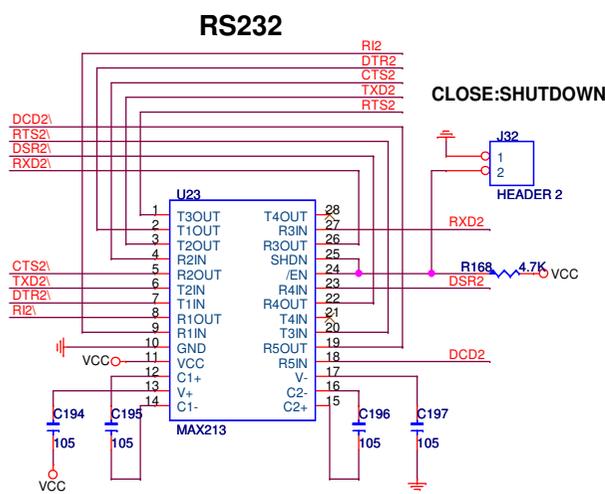
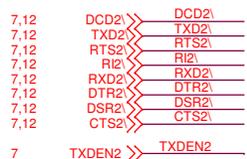
Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 12 of 21

COM1



COM2



DMP ELECTRONICS INC.

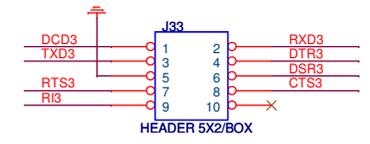
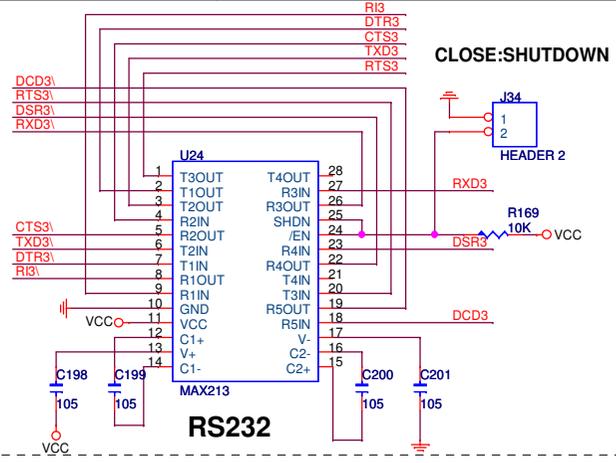
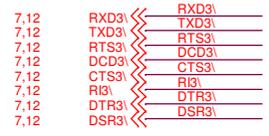


Title: COM1/2

Size: Document Number DM226B Rev 1.2

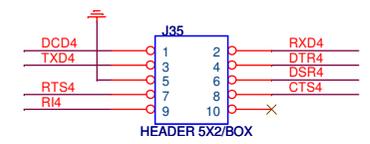
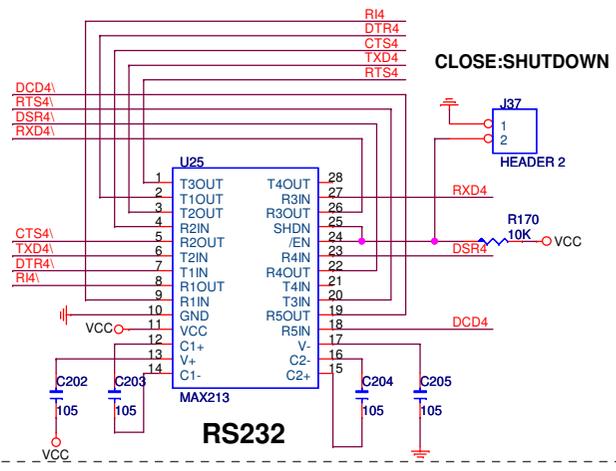
Date: Tuesday, May 12, 2015 Sheet 13 of 21

COM3



COM3 RS232

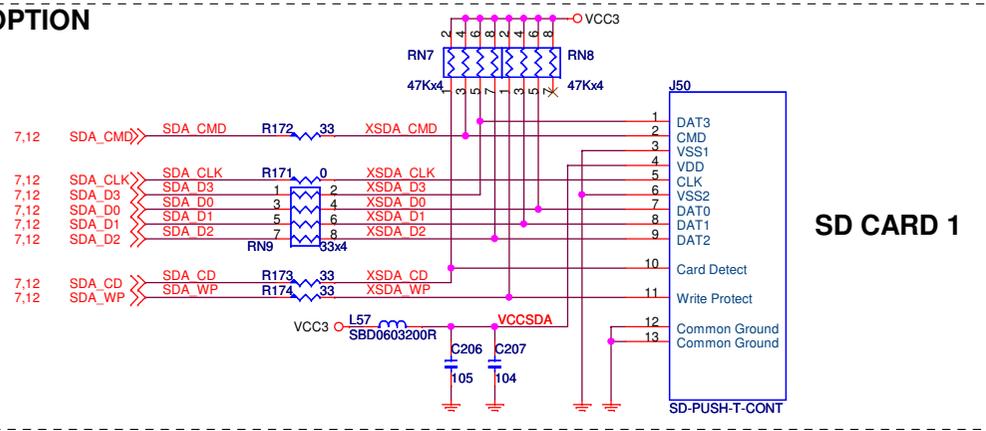
COM4



COM4 RS232

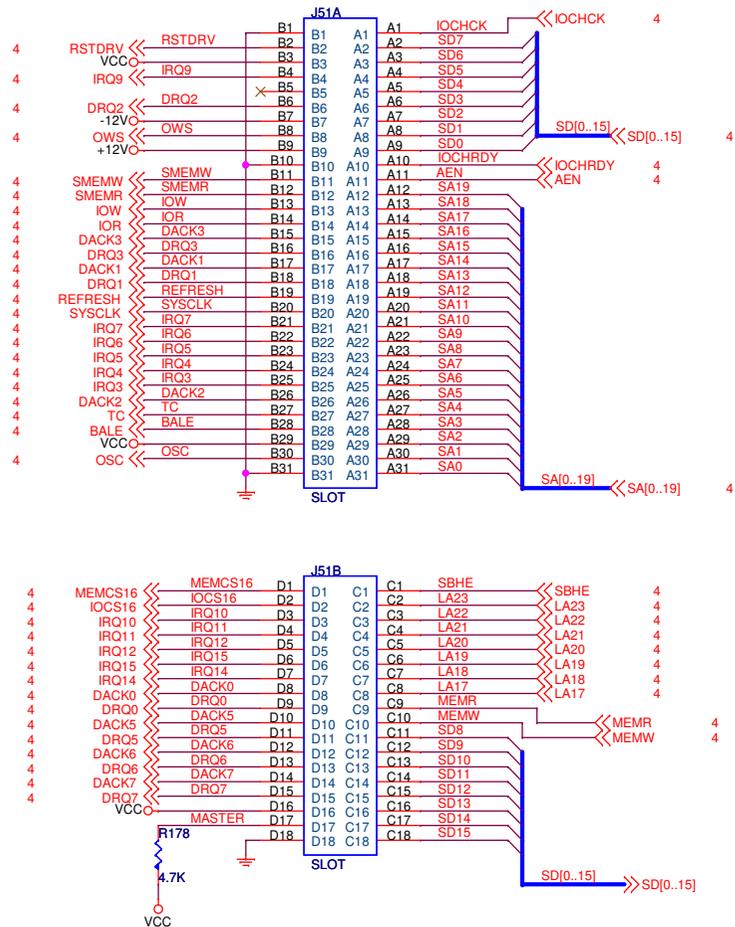
SD

OPTION

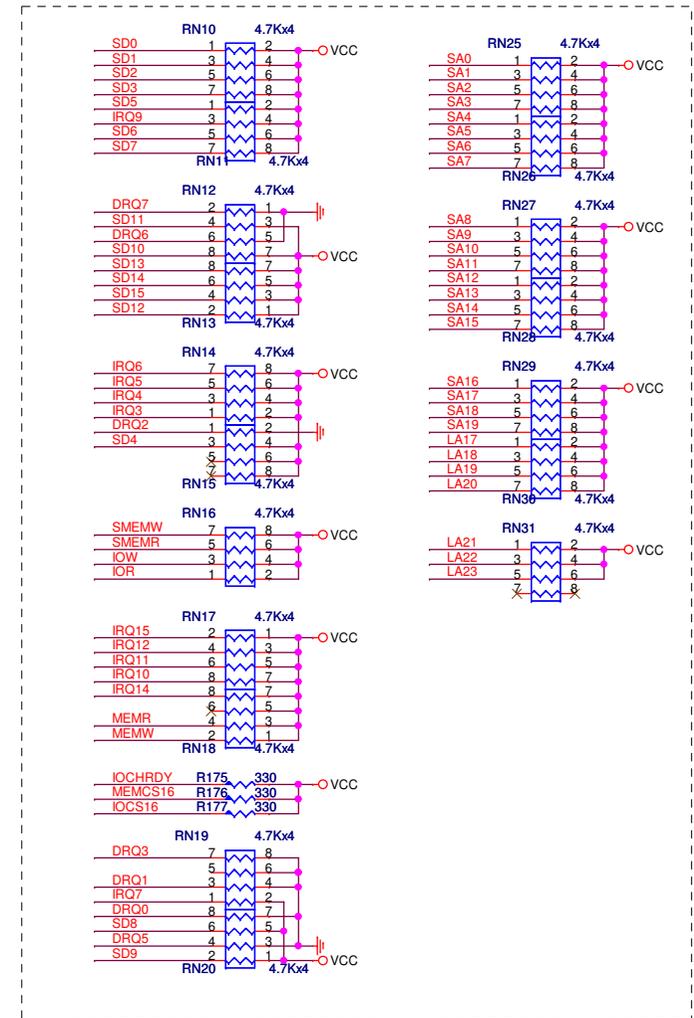


DMP ELECTRONICS INC.		
Title COM3/4/SD		
Size	Document Number DM226B	Rev 1.2
Date:	Tuesday, May 12, 2015	Sheet 14 of 21

ISA 8/16 BIT SLOT



OPTION PULL UP/DOWN

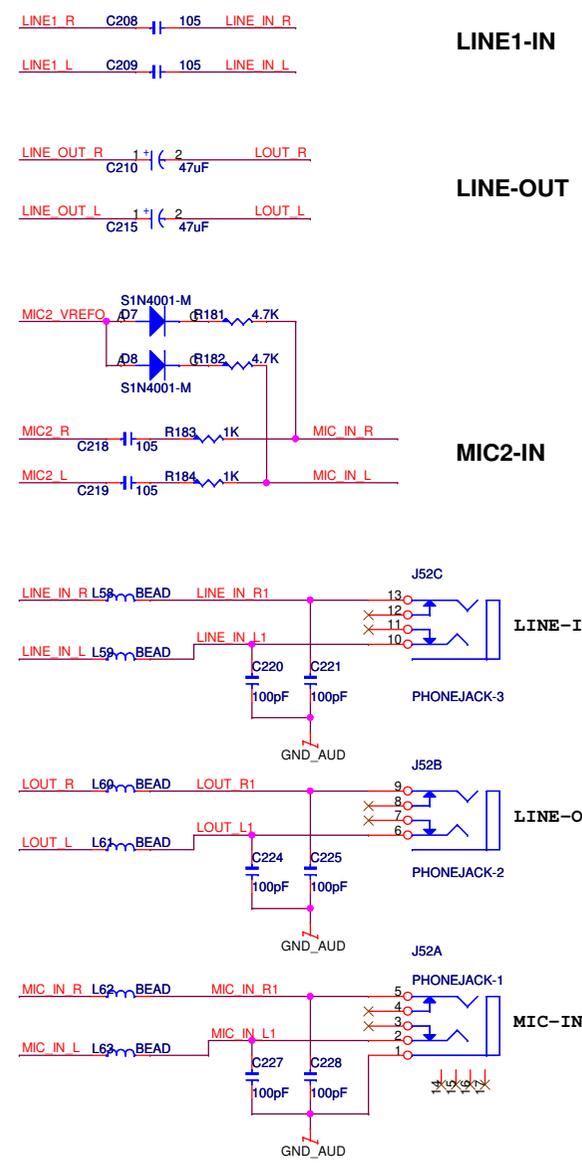
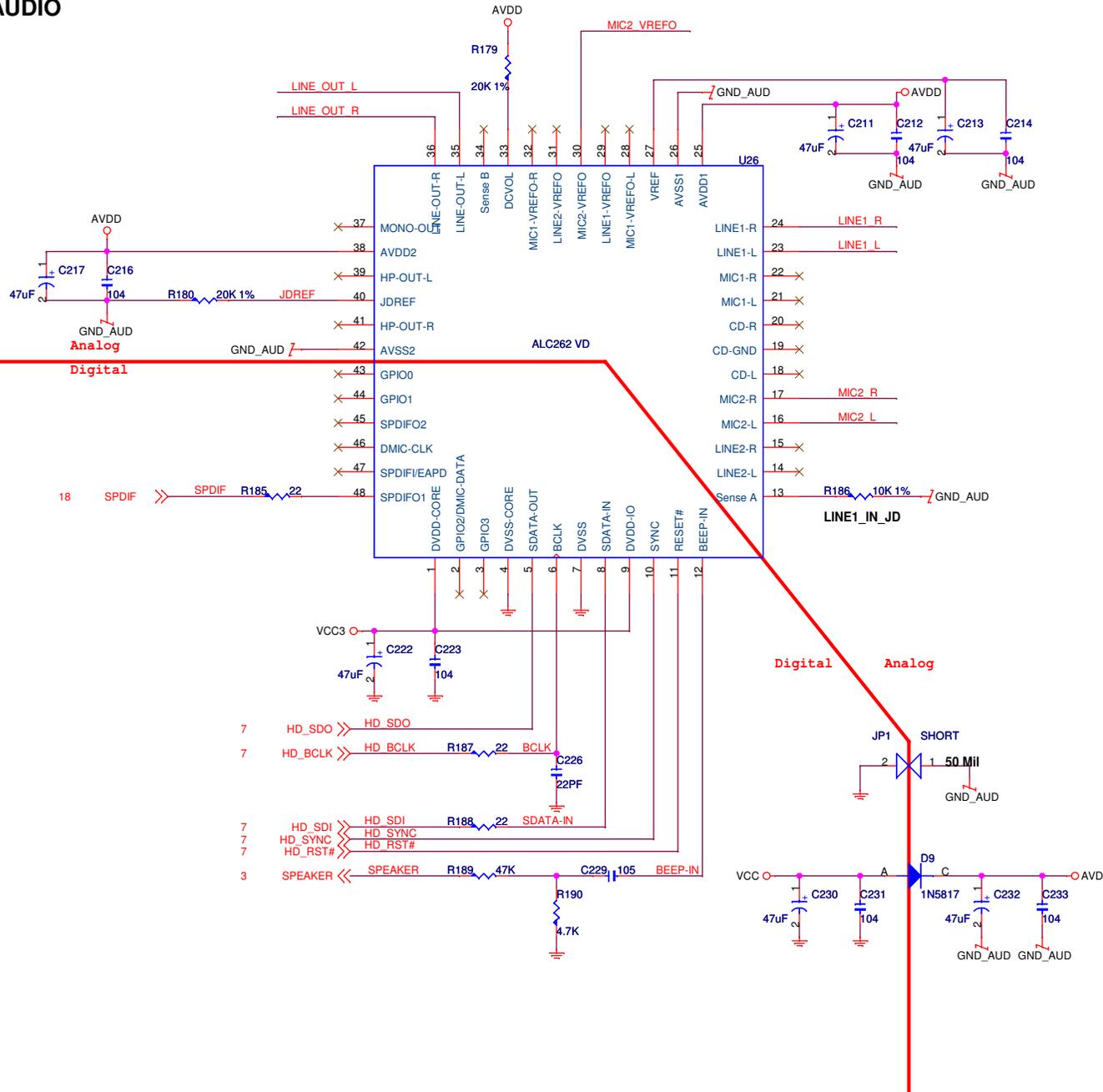


DMP ELECTRONICS INC.



Title ISA SLOT		
Size	Document Number DM226B	Rev 1.2
Date:	Tuesday, May 12, 2015	Sheet 15 of 21

HD AUDIO



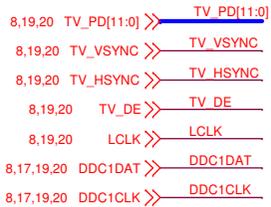
DMP ELECTRONICS INC.

Title: HD AUDIO ALC262

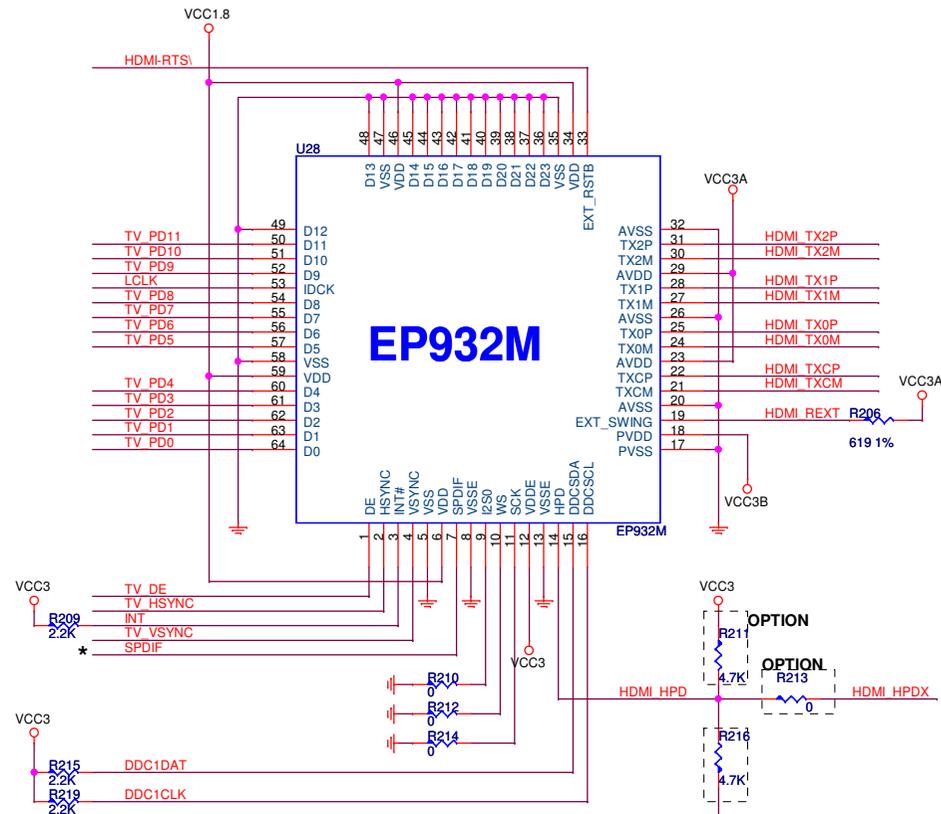
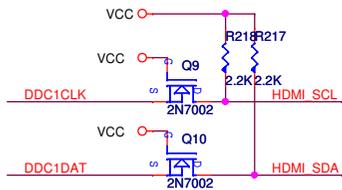
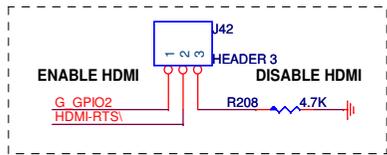
Size	Document Number	Rev
	DM226B	1.2

Date: Tuesday, May 12, 2015 Sheet 16 of 21

HDMI

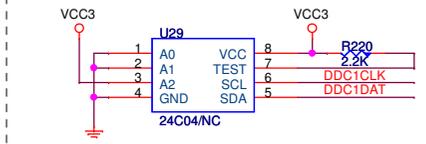


HDMI SETUP

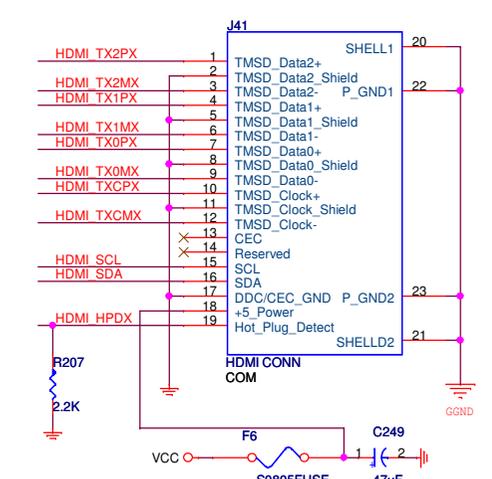


EP932M

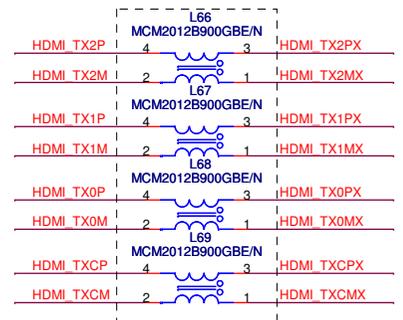
HDCP KEY OPTION



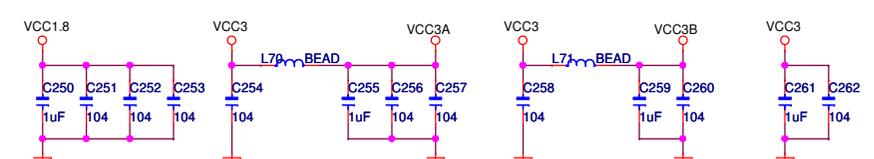
HDMI-OUT



Differential 100 ohm



BY PASS



DMP ELECTRONICS INC.

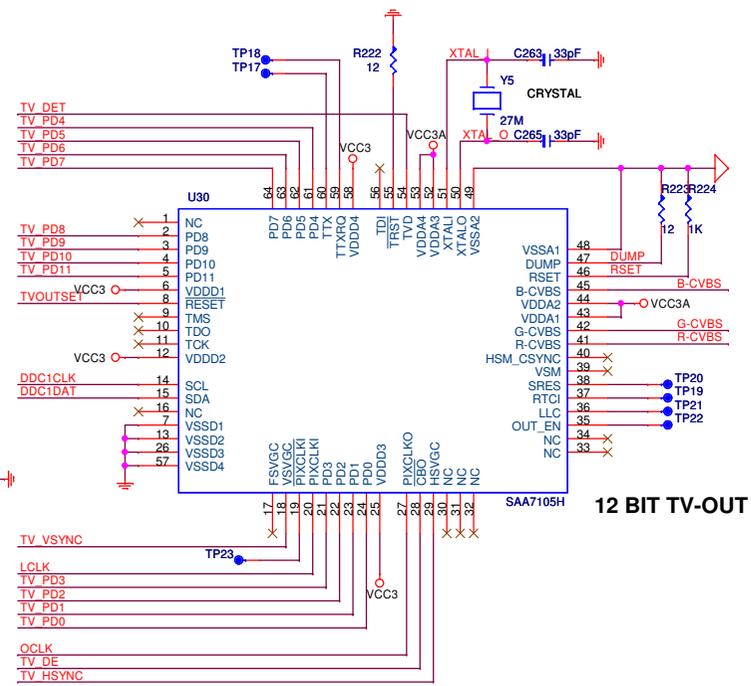
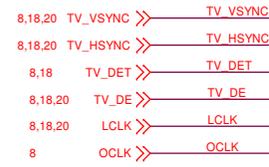
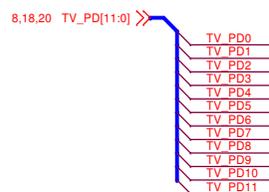
Title: HDMI EP932M

Size: Document Number DM226B Rev 1.2

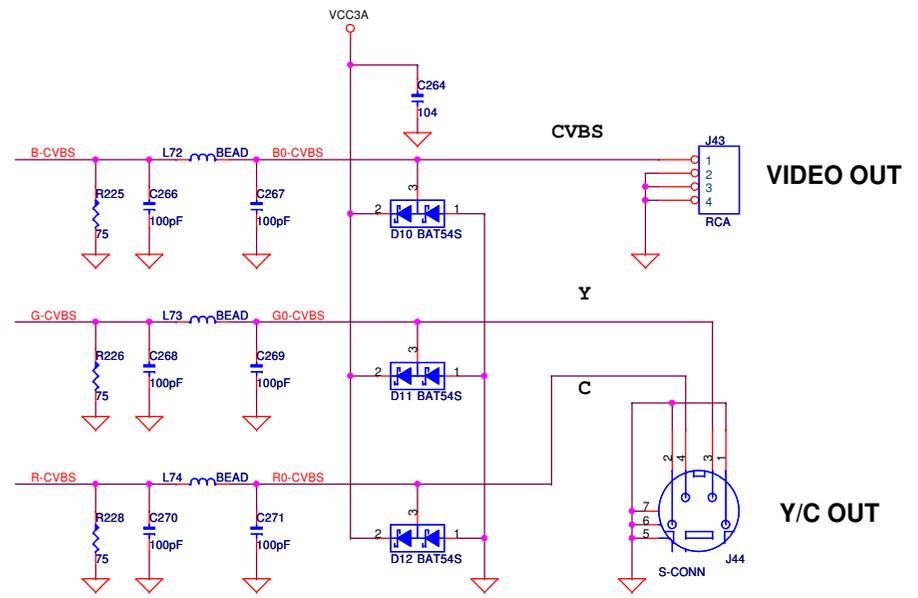
Date: Tuesday, May 12, 2015 Sheet 18 of 21

TV-OUT/HDMI

(TV-OUT RESET)



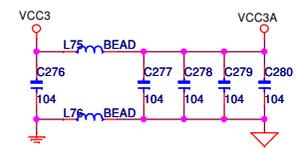
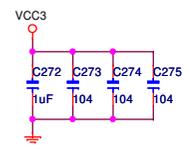
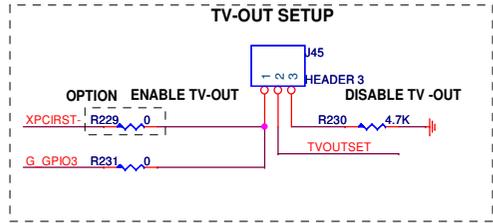
12 BIT TV-OUT



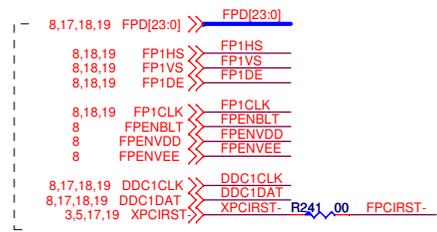
VIDEO OUT

Y/C OUT

TV-OUT SETUP



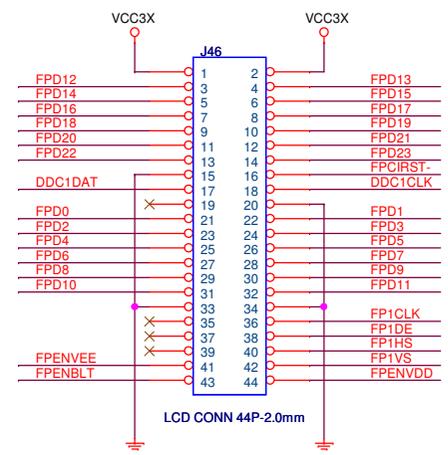
LCD 1



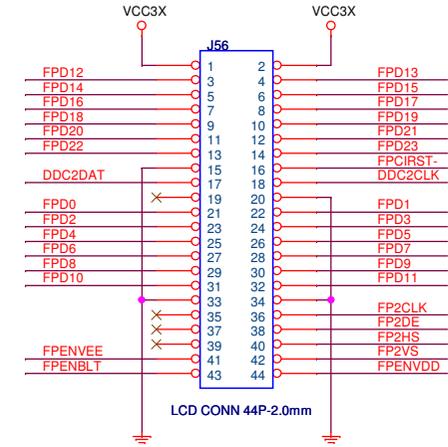
LCD 2



LCD 1 Support 24BIT/18BIT

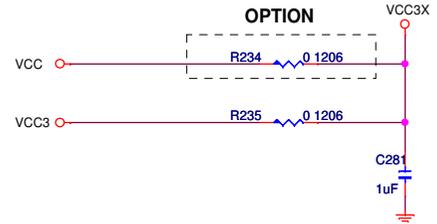


LCD 2 Support 24BIT/18BIT



DVO definition table

Port Name/ DMP LCD pin	LVDS-24bit SDR	TTL-18bit	LVDS-12bit DDR+CAP	CAP+TV-12bit	DSTN	
					Color	Mono
FPD23	LVD23:R7	D17: R5				
FPD22	LVD22:R6	D16: R4	VSYNC	VSYNC		
FPD21	LVD21:R5	D15: R3	HSYNC	HSYNC		
FPD20	LVD20:R4	D14: R2	CAP FILD	CAP FILD		
FPD19	LVD19:R3	D13: R1			LD7	
FPD18	LVD18:R2	D12: R0			LD6	
FPD17	LVD17:R1				LD5	
FPD16	LVD16:R0				LD4	
FPD15	LVD15:G7	D11: G5			LD3	LD3
FPD14	LVD14:G6	D10: G4			LD2	LD2
FPD13	LVD13:G5	D09: G3			LD1	LD1
FPD12	LVD12:G4	D08: G2			LD0	LD0
FPD11	LVD11:G3	D07: G1	LV1D11:G3R7	TVD11		
FPD10	LVD10:G2	D06: G0	LV1D10:G2R6	TVD10		
FPD09	LVD09:G1		LV1D09:G1R5	TVD09		
FPD08	LVD08:G0		LV1D08:G0R4	TVD08		
FPD07	LVD07:B7	D05: B5	LV1D07:B7R3	TVD07	HD7	
FPD06	LVD06:B6	D04: B4	LV1D06:B6R2	TVD06	HD6	
FPD05	LVD05:B5	D03: B3	LV1D05:B5R1	TVD05	HD5	
FPD04	LVD04:B4	D02: B2	LV1D04:B4R0	TVD04	HD4	
FPD03	LVD03:B3	D01: B1	LV1D03:B3G7	TVD03	HD3	HD3
FPD02	LVD02:B2	D00: B0	LV1D02:B2G6	TVD02	HD2	HD2
FPD01	LVD01:B1		LV1D01:B1G5	TVD01	HD1	HD1
FPD00	LVD00:B0		LV1D00:B0G4	TVD00	HD0	HD0
FPD1DE/FPD2DE	LVDE	DE	LV1DE	TVDE		DISP
FPD1HS/FPD2HS	LVHS	HS	LV1HS	TVHS		LOAD
FPD1VS/FPD2VS	LVVS	VS	LV1VS	TVVS		FRM
FPD1CLK/FPD2CLK	LVCLK	CLK	LV1CLK	TVCLK		CP
TVCLKIN				TVCLKIN		
FPD1DET/FPD2DET	DVIDET	DVIDET	DVIDET	TVDET		DVIDET
ENVEE	ENVEE	ENVEE	ENVEE			ENVEE
ENPVDD	EPVDD	ENPVDD	ENPVDD			ENPVDD
ENBLT	ENBLT	ENBLT	ENBLT			ENBLT
CAPD07			CAPD07	CAPD07		
CAPD06			CAPD06	CAPD06		
CAPD05			CAPD05	CAPD05		
CAPD04			CAPD04	CAPD04		
CAPD03			CAPD03	CAPD03		
CAPD02			CAPD02	CAPD02		
CAPD01			CAPD01	CAPD01		
CAPD00			CAPD00	CAPD00		
CAPCLK			CAPCLK	CAPCLK		



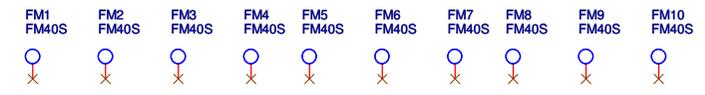
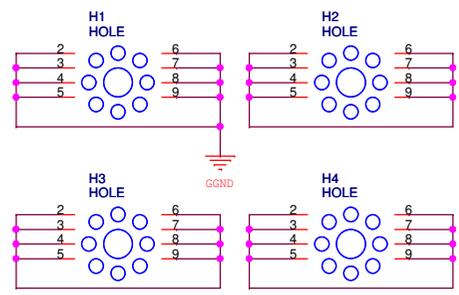
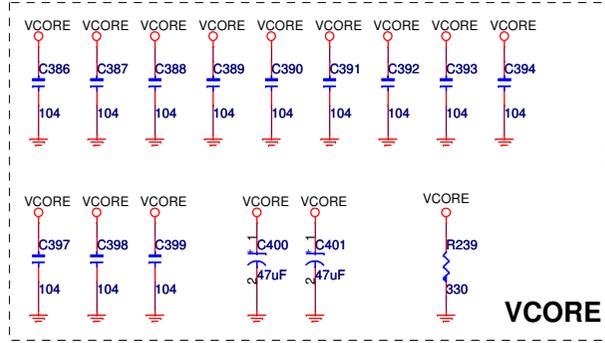
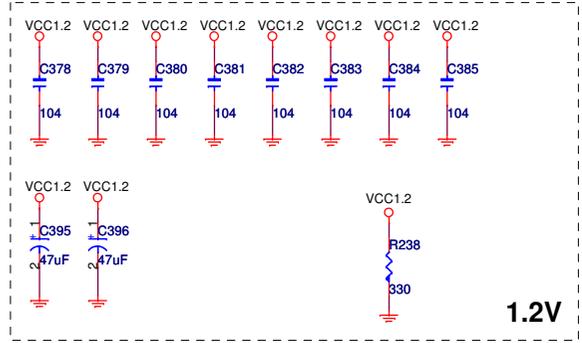
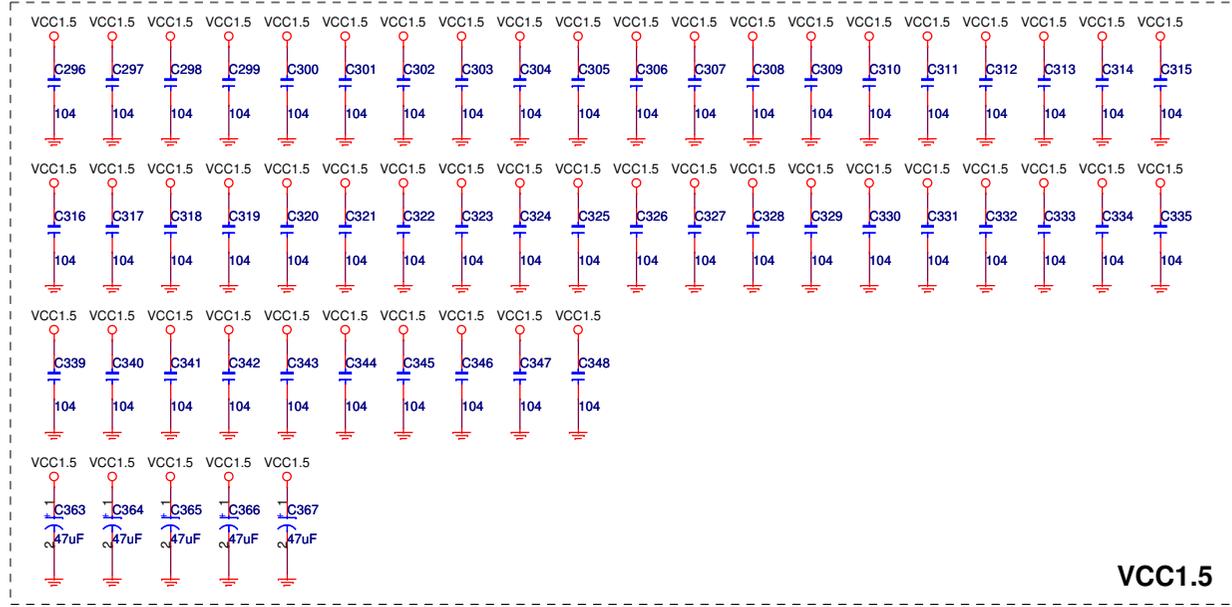
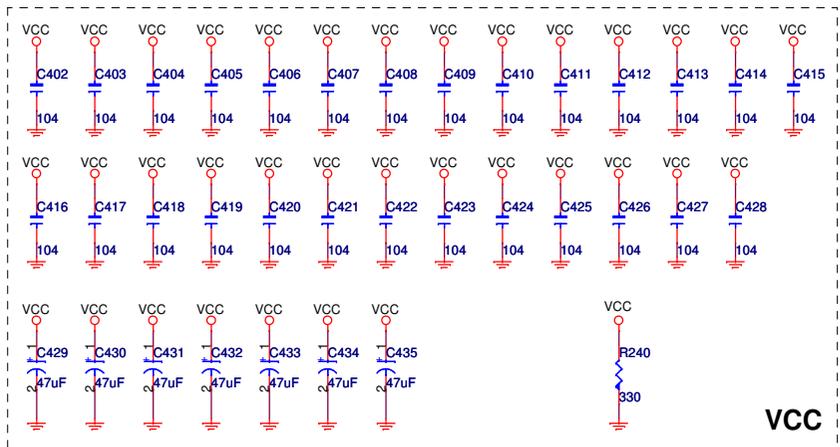
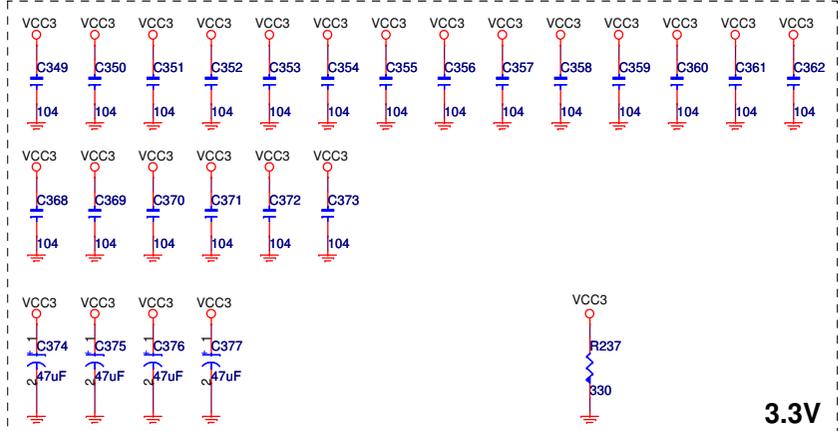
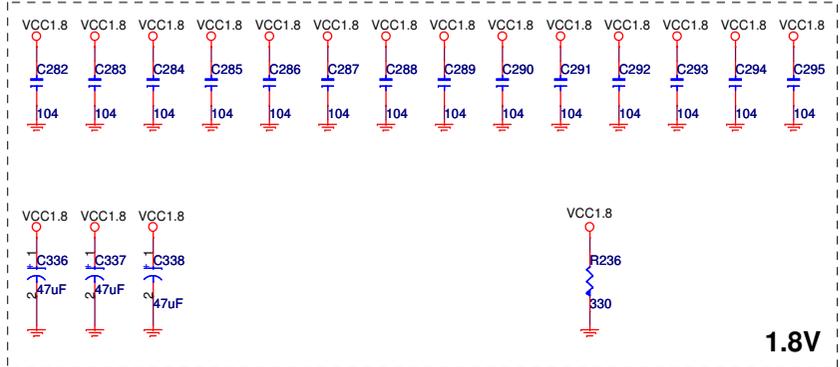
DMP ELECTRONICS INC.

Title: LCD1/2

Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 20 of 21

BYPASS



DMP ELECTRONICS INC.

Title: BYPASS

Size: Document Number DM226B Rev 1.2

Date: Tuesday, May 12, 2015 Sheet 21 of 21