

FEATURES

- 16-bit pixel port, usable as 8-bit port
- Up to 135MHz pipeline operation
- Available clock rates : 110MHz, 100MHz, 94.5MHz, 75MHz, and 50MHz
- Support of TrueColor (8-8-8) Format (16,777,216 colors)
- Support of XGA (5-6-5) Format (65,536 colors)
- Support of TARGA (5-5-5) Format (32,768 colors)
- Support of pseudo Color Format (256 colors)
- 256 x 18 Color Palette RAM
- Triple 8-bit D/A Converters
- Snow-effect free

- Internal/External Voltage Reference Mode (MX86491/492V)
- Standard MPU Interface
- Three on-chip comparators to detect monitor connections
- RS-343A/RS-170 compatible outputs
- +5V CMOS Monolithic (EPL) construction
- Software power-down mode
- Power-on-reset circuit for the command register
- 44-pin PLCC package

GENERAL DESCRIPTION

The MX86491/492 CMOS True-Color RAMDAC comprises 24-bit True-color format, which has 16,777,216 colors, 16-bit XGA format, which has 65,536 colors; 15-bit TARGA format, which has 32,768 colors, and pseudo-color format, which has 256 colors.

The color information in True-Color, XGA, and Hicolor formats are directly passed to the DACs. When the pseudo-color mode is activated, the device works in the same way as Brooktree® 81471™, SIERRA® SC11471™. It has the function of snow-effect free.

The device includes 8-bit video DACs, 258x18 color

palette RAM, on-chip comparator which is output to the SENSE# pin for monitor detection, internal voltage reference, and command register for choosing different color modes.

The MX86491/492 is offered in the industry-standard 44-pin PLCC packages. It is designed to work with internal/external voltage reference or external current reference.

The MX86491/492 generates RS-343A-compatible video signals into a doubly terminated 75Ω load, and RS-170-compatible video signals into a singly terminated 75Ω load, without requiring external buffers.

PIN CONFIGURATION

44-PIN PLCC

