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**MX86200****High Performance 64-Bit GUI / Video Accelerator****FEATURES**

- \* High Performance Architecture
- \* True 64-bit graphics engine.
- \* True 8/16/32 bpp acceleration.
- \* BitBLT, rectangle, pattern fill, line draw, color expansion and complete 256 ROP.
- \* On-chip pattern memory.
- \* Multiple advanced FIFOs.
- \* Memory-mapped I/O.
- \* 64x64x2 bit-mapped hardware cursor.
  
- \* High-quality Video Playback
- \* Support primary surface DCI drivers for smooth and high quality video playback.
- \* Support Microsoft Video For Windows.
  
- \* Advanced Memory Control
- \* 1,2, or 4 MBytes display memory.
- \* True 64-bit display memory access.
- \* 256K x 4, 256K x 8, and 256K x 16 dual CAS or dual WE DRAM.
- \* Fast-page and Hyper-page EDO.
- \* Auto memory size detection.
- \* Linear addressing modes up to 4 MByte.
  
- \* Full Resolution/color support
- \* 1600x1200, 64K colors @ 60 Hz.
- \* 1280x1024, 64K color @ 75 Hz.
- \* 1024x768, 16M color @ 60 Hz.
  
- \* Fully Integrated for Modern Systems
- \* Glueless PCI bus interface.
- \* Full PCI 2.1 compliance.
- \* Integrated RA MDAC.
- \* Integrated programmable 135 MHz clock synthesizer.
- \* Low-Power sub-micron CMOS.
- \* 208 pin PQFP.
  
- \* Engineered for Modern Standards
- \* Plug and Play compliant.

- \* VESA Display Data Channel (DDC2B) protocol support.
- \* VESA standard Feature Connector support.
- \* DPMS and advanced power management support.

## **OVERVIEW**

The MX86200 is an advanced 64-bit GUI and Video Accelerator. Integrating a 64-bit graphics coprocessor, 135 MHz RAMDAC and programmable clock synthesizer, the MX86200 is a complete low-cost and high-performance PC graphics / video solution. Performance of the memory subsystem was a key design consideration of the MX86200. Utilizing the regular DRAM with 65 MHz memory clock, DRAM bandwidth can exceed 250 MB/sec. This bandwidth gives top-end Windows performance and smooth video playback in a low-cost DRAM system.