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CYBER9320(tm)
FLAT PANEL CONTROLLER

FEATURES

Highly Integrated Design

- Requires only one 256Kx16 DRAM for 1024x768-16 color SVGA solution
- Built-in LUT-DAC composed of 256x18 color look-up table
- Dual loop memory and video clock
- Two-chip solution (9320 and 256Kx16) for 512K display memory
- Supports Extended-Data-Out (EDO) Memory
- 208-pin PQFP package

Accelerated Graphics Functions

- Optimized graphic engine for: BitBLTs, line drawing, short stroke vectors, rectangle fills, and text transfer
- Hardware pop-up icon (64x64x2-bit)
- Internal hardware cursor (64x64x2-bit or 32x32x2-bit)

Advanced Power Management

- Independent control for video clock, memory clock and DAC
- All register contents are accessible through the system bus, providing mechanisms for 0 V suspend and hibernation
- Supports self-refresh and slow-refresh DRAM for minimum power consumption

Versatile Display Support

- High resolution TFT, SS-STN, and CRT, including 1280x1024-16, 1024x768-256, 800x600-64K, 800x600-256, 640x480-256, 640x480-64K, and 640x480-16M colors
- High resolution DS-STN, including 1024x768-16, 800x600-256, 640x480-64K, and 640x480-256 colors
- Simultaneous display

Flexible Display Memory Interface

- 8-bit interface to mono SS/DS-STN, 8/16-bit interface to color SS/DS-STN, and 9/12/18/24-bit or analog interface to color TFT panels

Advanced Image Control

- Supports Frame Rate Control and Spatial Dithering for flat panel
- Auto-contrast adjustment
- Auto-fill and centering for high resolution panel

NTSC/PAL Encoder Support

- Provides RGB, composite sync, and carrier clock for NTSC/PAL encoder

Advanced Multimedia Support

- Allows 8/16 bits of external RGB video data to be input and merged with internal data stream

Simple Bus Interface Support

- Flexible Bus Interface Unit for zero wait state 32-bit glueless connection to VESA Local Bus (VL Bus), ISA, or PCI Bus 2.0 with no additional TTL
- Supports VESA DDC, DPMS, and VAFC standards
- Two wire interface to EEPROM or VESA DDC
- Linear display memory addressing

Mixed Voltage Operation and Interface

- Independent VCC for internal logic, host interface, memory interface, and display interface

CYBER9320 Application Diagram

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