

## BE-T1000 REFERENCE MANUAL

### List of Volumes

Volume	Title	Pages	Status
01	General Description	40	Release Candidate
02	Clock Control Unit (CCU)	57	Release Candidate
03	Boot Controller	20	Release Candidate
<b>Memory System</b>			
04	DDR3 Memory Controller		Draft
05	DDR PHY		Draft
06	DMA Controller (DMAC)		Draft
<b>High Speed Peripherals</b>			
07	1Gb Ethernet Controller (GMAC)	134	Release Candidate
08	10Gb Ethernet Controller (XGMAC)	399	Release Candidate
09	PCI Express (PCIe)		Draft
10	PCIe & XGMAC PHY	128	Release Candidate
11	USB 2.0 Controller	75	Release Candidate
12	Serial ATA 6G (SATA)	84	Release Candidate
13	SATA PHY	53	Release Candidate
<b>Low Speed Peripherals</b>			
14	PVT sensor	21	Release Candidate
15	Watchdog Timer (WDT)	22	Release Candidate
16	Peripheral Timers	19	Release Candidate
17	Inter-IC Controller (I2C)	70	Release Candidate
18	Universal Asynchronous Receiver-Transmitter (UART)	51	Release Candidate
19	Serial Peripheral Interface (SPI)	48	Release Candidate
20	General Purpose I/O Interface (GPIO)	24	Release Candidate

**Total 1245**