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ProVidia9692(tm)
64 Bit UMA WIN95 and
Video Accelerator

Features

Complete Hardware Compatibility

- Fully PCI 2.1 compliant
- Supports VESA DDC, DPMS, and VAFC
- VGA compatible on BIOS, register, and hardware levels
- 208-pin PQFP package is pin compatible with Trident TGUI9660, TGUI9680 and ProVidia9682

2D Graphics Acceleration

- Internal 64 bit memory data bus
- 256 Raster Operations (ROPs) for 8 bpp (PseudoColor), 15-/16 bpp (HiColor), and 24 bpp (True Color)
- Optimized graphic engine for BitBLTs, line drawing, short stroke vectors, rectangle/trapezoid fills, clipping, ROP4 assist, and text transfer
- Internal hardware cursor (64x64x2 or 32x32x2)
- Built-in 64x32 pattern register

3D Graphics Acceleration

- Supports perspective correct, MIP-map texture mapping
- Supports video texture mapping
- Optional hardware Z-buffer (24 and 16 bit)
- Gouraud shading for smooth surfaces
- Alpha blending for transparency and fog effects
- Color dithering from allows 24 bit quality in 16 bit and 8 bit modes

Software MPEG and Video Acceleration

- On-chip Color Space Conversion (CSC), horizontal and vertical interpolated scaling, and overlay control for 30 fps software MPEG/video CODEC acceleration
- Page flipping, anti-tear support for DirectDraw

Unified Memory Architecture (UMA) Support

- Unified frame buffer support for leading UMA core logic chipsets
- Minimal system performance degradation w/o L2 cache

Hardware Interface to MPEG and Video Decoders

- VMI compliant hardware interface to MPEG or video decoder for live video inputs (TV, Camera, VCR, LDP)
- Dual aperture for simultaneous access for Graphics and Video display memory areas

True Video® Hardware Support

- Trident's True Video® horizontal and vertical interpolated scaling and proprietary image recovery
- Dual live video support for video conferencing

Highly Integrated Design

- Fully integrated 24 bit True Color DAC with color look-up table, 135 MHz clock synthesizer, read cache, command FIFO, and GUI accelerator
- 256x18 color look-up table and HiColor and True Color bypass mode support
- Two wire interface to EEPROM or VESA DDC

Simple Bus Interface Support

- Flexible Bus Interface Unit for zero wait state, 32 bit glueless connection to a PCI 2.1 Bus
- PCI Bus master and PCI burst mode
- Big-endian and little-endian data formats
- Memory Mapped I/O PCI Bus
- Zero-wait state host write buffer and read cache

ProVidia9692 Application Diagram

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About Trident	Home	Acceleration
Products &	Investor Relations	Human Resources
Drivers & Support	Press Releases	Trident Multilingual

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