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TVG9470(tm) TV ACCELERATOR

FEATURES

Complete Hardware Compatibility

- PCI rev. 2.1 and VL Bus 2.0 and ISA compliant
- Supports VESA DDC and VAFC Standards
- 100% IBM VGA compatible on BIOS, register, and hardware levels
- 208-pin PQFP package

Flexible Display Memory Interface

- 1/2 MB, 1 MB, or 2 MB display memory configuration with memory densities of 256Kx4, 256Kx8, and 256Kx16
- Requires only one 256Kx16 DRAM for 1024x768 SVGA solutions
- 32-bit memory interface with programmable DRAM timing

Extended VGA Display Resolutions

- High resolution non-interlaced display through 1280x1024-256, 1024x768-256, 800x600-64K colors, or 640x480-16M colors
- Extended text modes (80 or 132 columns by 25, 30, 43, or 60 rows)

Integrated Features for TV Display

- NTSC/PAL resolution interlaced display for 640x480 (NTSC) or 800x600 (PAL)
- NTSC/PAL display for DOS games
- Flicker Removal for output to interlaced monitors
- Interpolated vertical scaling for NTSC or PAL resolutions
- Video Passthrough analog switch for selection of direct output of VGA or VCR Data to TV monitor
- Overscan/underscan to TV display
- Simultaneous display of VGA and TV (NTSC or PAL) monitors with external DAC
- Direct interface to external NTSC/PAL Encoder

Improved Manufacturability

- Pin scan testing
- Signature analysis

Highly Integrated Design

- Fully integrated 24-bit TrueColor DAC with color look-up table, 108 MHz programmable clock synthesizer, 100% IBM compatible VGA core, and GUI Accelerator
- 256x18 color look-up table with HiColor and TrueColor bypass mode support, dual loop memory, and video clock
- Two wire interface to EEPROM or VESA DDC

Advanced Multimedia Support

- VESA Advanced Feature Connector (VAFC)
- Port for high bandwidth video overlay on graphics
- Programmable Color Space Converter
- Palette snooping

Simple Bus Interface

- Flexible Bus Interface Unit supports 16-bit ISA Bus interface and 32-bit "glueless" connection to VESA Local Bus or PCI with no additional TTL
- Linear display memory addressing
- Zero wait state host write buffer and read cache
- PCI Burst mode support

"Deep Green PC" Power Management

- VESA Display Power Management Signaling (DPMS) compatible
- Simple RAMDAC power-down and clock idle register interface
- Maintain default video throughput at power-down.

Accelerated Graphics Functions

- Optimized graphic functions for BitBLTs, line drawing, short stroke vectors, polygon fills, and text transfer
- 256 Raster Operations (ROPs) for 8-bits per pixel (PseudoColor) and 15/16-bits per pixel (HiColor) graphic modes
- Accelerated color expansion modes
- Internal hardware cursor (64x64x2 or 32x32x2)

TVG9470 Application Diagram



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Trident Microsystems, Inc.
 189 North Bernardo Avenue
 Mountain View, CA 94043-5203
 415.691.9211
 415.691.9260 (sales fax)
 415.691.9265 (corporate fax)

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