



Real Time Clock plus CMOS RAM

Product Brief

Features:

- Common calendar year with leap year compensation
- Year 2000 compliant
- Binary or BCD data representation
- 12/24 hour mode
- Daylight savings time option
- Interfaces with software as 256 bytes of RAM
 - 15 bytes of clock and control registers
 - 241 bytes of general-purpose RAM
- Low power, high speed, CMOS technology
- 24-pin dual-in-line and SOP package
- Three interrupts are separately software maskable and testable
 - Month-of-day alarm
 - Periodic rates from 122 us to 500 ms
 - End of clock update cycle

Pinout Diagram

