Application Note of IT8702F/IT8712F for BIOS Issue about Early I/O

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While the battery is installed on motherboard or the ATX power connector is plugged in the motherboard, the bouncing glitches may be generated on "VBAT" (from battery) or "5VSB" (from ATX power supply). Such kind of bouncing glitches may cause the initialization of "Environment Controller Configuration Registers" of IT8702F or IT8712F is unstable.

To prevent any abnormal behaviors of IT8702F or IT8712F, to program some bits of "Environment Controller Configuration Registers" to suitable value, after power-on and before POST sequence, is necessary (We may call it as a "Early I/O initialization" sequence.).

The register bits need to be programmed to suitable value in the Early I/O initialization is described as follows;

Environment Controller Configuration Registers (LDN=04h):

Index F2/ Bit 6, Bit 4 = 0,0
Index F4/ Bit 7 = 1
Index F4/ Bit 5 = 0
Index F5/ Bit 7, Bit 6 = 00

By the way, please also set

LDN0, Index F1/ Bit 7 = 1
LDN3, Index F0/ Bit 3 = 1 (If the POST Data Port function is used, please still set LDN3/IndexF0/Bit3 to 1 after POST.)